

FCCC\_C0\_0



FCCC\_C0

MisoAdcA

MisoAdcB

MisoAdcC

MisoAdcD

nDryAdcA

nDryAdcB

nDryAdcC

nDryAdcD

nHVFaultA

nHVFaultB

nHVFaultC

nHVFaultD

FineSteeringMirror\_sb\_0



DEVRST\_N

DEVRST\_N

FineSteeringMirror\_sb

Rxd0

Rxd1

Rxd2

Rxd3

TxdLab

PPS

MisoMonAdc0

nDryMonAdc0

MisoMonAdc1

nDryMonAdc1

PowerCycd

FaultNegV

Fault1V

Fault2VA

Fault2VD

Fault3VA

Fault3VD

Fault5V

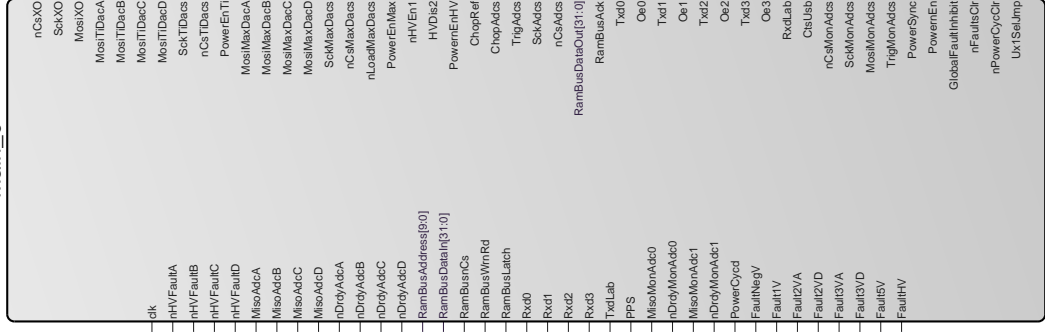
FaultHV

Main\_0\_RamBusDataOut

Main\_0\_RamBusAck

INIT\_DONE

Main\_0



Main