

The diagram shows a logic circuit for a 4-bit counter. The counter is a 4-bit register with output 'counter' and input 'mclk'. The counter is incremented by 1 when the output is 0. The counter is also connected to a 4-bit register with output 'bin2ssd' and input 'di'. The counter is also connected to a 4-bit register with output 'ab' and input 'di'.

