CSC390 (spring 2016)

Quiz#3

Suppose the processor is executing the instruction, SW $t1, 20($t2). Highlight the different control signals and multiplexer inputs that will be activated during the execution process of the above store instruction. Also indicate the buses where the $t1, $t2, the offset (20), offset+$t2, and the content of the $t1 can be found.

