Dec. 27, 2020

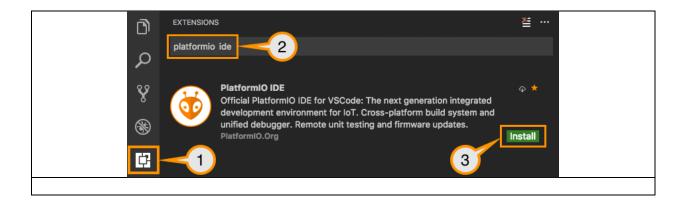
https://docs.platformio.org/en/latest/boards/espressif32/featheresp32.html

1. Installation

1.1 <u>Download</u> and install official Microsoft's Visual Studio Code, PlatformIO IDE is built on top of it https://code.visualstudio.com/docs/?dv=win

1. 2. Quickstart Platform IO

https://docs.platformio.org/en/latest/integration/ide/vscode.html#quick-start



- 1.2.1 **Open** VSCode Package Manager (#1)
- 1.2.2 select PlatformIO IDE 2.2.1 (#2)
- 1.2.3 select install (#3)
- 1.2.4 select reload VS window
- 1.3. Click on "New Project", select a board and create new PlatformIO Project
- 1.3.1 Board: espressif32: adafruit ESP32 Feather, framework Arduino, default location, finish (wait for tools to be installed)
- 1.4. Build (check mark on bottom of tool bar), upload (right arrow on bottom of tool bar) (LED on feather board should blink).

2. Install ESP-IDF framework

- 2.1. Click on "New Project", select a board and create new PlatformIO Project
- 2.2 Board: espressif32: adafruit ESP32 Feather, Framework: <u>Espressif IoT Development Framework</u>, default location, finish

(wait for tools to be installed)

Configuration File\r\n\nError: Processing featheresp32 (platform: espressif32; board: featheresp32; framework: espidf)\r\n—

Error: Detected a whitespace character in project paths.\r\n

Make sure Name: does not contain space or will crash

WiFi node example:

https://docs.platformio.org/en/latest/tutorials/espressif32/espidf_debugging_unit_testing_analysis.html#tutorial-espressif32-espidf-debugging-unit-testing-analysis

2.3 Make sure this new file main.c is registered as source file using idf_component_register function in src/CMakeLists.txt file:

```
idf component register(SRCS "main.c")
```

2.4 allow vscode to install CMake Tools.

3. Hello World example for platformio

Examples setup for platformio are located in:

```
~/home/.platformio/platforms/espressif32/examples
```

The other examples (see later sections) were set up for command line tools, and need slightly different setup.

Examples setup for platformio:

https://github.com/platformio/platform-espressif32/tree/master/examples

~/home/.platformio/platforms/espressif32/examples already has platformio.ini

3.1 File-> Open Folder -> examples/espidf-hello-world

(also can be downloaded from github:

https://github.com/platformio/platform-espressif32/blob/master/examples/espidf-helloworld/src/helloworld main.c

3.2 update the platformio.ini file:

```
[env:featheresp32]
platform = espressif32
framework = espidf
board = featheresp32
monitor_speed = 115200
```

- 3.3 delete the other boards in platformio.ini as they are not used.
- 3.3 build (takes about 6 minutes)/upload for project toolbar on bottom of window (right arrow symbol)3
- 3.4 upload
- 3.5 works in putty terminal

7. SkeletonHuzzah32

- 7.1 git clone https://github.com/ucb-ee192/SkeletonHuzzah32
- 7.2 build in platformio (first time will also build libraries for eps-idf framework)
- 7.3 flash, and monitor in either built-in terminal or putty terminal

8. SoftWiFi Access Point

```
https://github.com/espressif/esp-
idf/tree/release/v4.1/examples/wifi/getting started/softAP
```

also ~/home/.platformio/packages/framework-espidf/examples/wifi/getting_started

8.1 VS: PlatformIO (ant icon) -> PIO Home -> Projects and Configuration -> + create new project

Name: softAP [no space!]

Board: Adafruit ESP32 Feather (can type esp32, and Feather will be an option)

Framework: Espressif IoT Development Framework,

Uncheck use default location

[choose location such as ~/home/EE192/]

Finish

Get ESPRESSIF window. Ignore.

Would you like to configure 'UDPSockets'-> yes

```
8.2 Edit platformio.ini to add monitor speed = 115200
8.3 copy files into src directory
       softap example main.c
       8.3.1 copy
       examples/common components/protocol examples/common/connect.c,
       into src directory
8.4 copy needed header file
./\texttt{examples}/\texttt{common}\_\texttt{components}/\texttt{protocol}\_\texttt{examples}\_\texttt{common}/\texttt{include}/\texttt{protocol}\_\texttt{examples}\_\texttt{common}
8.4 add defines to platformio.ini
(note extra single quotes)
build flags =
   '-D CONFIG ESP WIFI SSID="Huzzah32"'
     '-D CONFIG ESP WIFI PASSWORD="1234"'
     -D CONFIG ESP MAX STA CONN=2
     -D CONFIG ESP WIFI CHANNEL=1
9. Other Examples
Application programming interface (API) description can be found here:
https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-
reference/index.html
Other example code for esp32 can be found here (missing platformio.ini, which needs to be added):
https://github.com/espressif/esp-idf/tree/master/examples
~/home/.platformio/packages/framework-
espidf/examples/protocols/sockets
8.1 VS: PlatformIO (ant icon) -> -> Projects and Configuration -> + create new project
       Name: UDPSockets [no space!]
       Board: Adafruit ESP32 Feather (can type esp32, and Feather will be an option)
       Framework: Espressif IoT Development Framework,
       Uncheck use default location
       [choose location such as ~/home/EE192/Sockets]
       Finish
```

```
Would you like to configure 'UDPSockets'-> yes
8.2 Edit platformio.ini to add monitor speed = 115200
8.3 copy file such as udp server.c into src directory
      8.3.1 copy
      examples/common components/protocol examples/common/connect.c,
      into src directory
8.4 copy needed header file
./examples/common_components/protocol_examples_common/include/protocol_examples_common.h
into include directory
8.4 add defines to udp server.c
build flags =
    -DCONFIG EXAMPLE CONNECT IPV4
    -DCONFIG EXAMPLE CONNECT WIFI
    -DCONFIG EXAMPLE PORT=3333
Build/upload (CMakeLists.txt seems to work with src/*.*)
Python examples at:
~/home/.platformio/packages/framework-espidf/examples/protocols/sockets/scripts
8.1 Adding files to project folder (VS: file -> open file)
CMakeLists.txt: idf component register(SRCS "hello world main.c"
"other1.c"
               INCLUDE DIRS "")
note that platformio.ini should be copied as above for featheresp32
```

Get ESPRESSIF window. Ignore.

9. Basic Text IO

```
printf works. Need to have bigger stack configMINIMAL STACK SIZE + 2048
make sure to use flush(stdout); to make sure print finishes.
snprintf is recommended, as buffer length is specified
printing floating point numbers: almost 1000 us
print an int and long, about 400 us
integer to ascii: itoa()
Scanf does not work. And is not recommended due to input insecurity (buffer overflow)
fgetc(stdin) and fputc(char,stdout) work
ch = fgetc(stdin);
  if (ch!=0xFF) // discard idle character (non-input)
https://stackoverflow.com/questions/58403537/what-can-i-use-for-input-conversion-instead-of-scanf
printf uses a lot of stack space and cause tasks to run out of space. The log task saved almost 800 bytes of
stack space by eliminating printf
void printString(char *string)
     int i=0;
     while (string[i] != '\0')
           { fputc(string[i], stdout); // print single character, avoid
printf to save on stack space and speed up
                i++;
           }
}
```

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-reference/system/console.html

ESP-IDF provides console component, which includes building blocks needed to develop an interactive console over serial port. This component includes following facilities:

9.1 LED to blink GPIOLED? IO13

9. 2 Advanced IO

#define WIFILOG

Then Huzzah32 is a soft access point, connect and use python script to get debugging/log info.

To add a debugging message, do:

```
snprintf(log, sizeof(log), printf args here);
log_add(log);
```

log add() takes about 40 us

```
esp_netif_lwip: DHCP server assigned IP to a station, IP is:
192.168.4.2
```

10. Configuring Options

There are many, many options to configure setting up the ESP32 hardware and software environment. The readable settings file is sdkconfig, which is autogenerated. Editing sdkconfig manually will be overwritten. These parameters are accessible to your program if needed through sdkconfig.h (also autogenerated).

For version control, this file may be storing setup information, but not yet verified:

```
.pio/build/featheresp32/config/kconfig menus.json
```

WARNING: idf.py menuconfig is incompatible with platformio and will mess up build.

10.1 Menuconfig

- 1. (optional) Exit VS so there is not a conflict with sdkconfig. Make a backup copy of sdkconfig just in case
- 2. To edit sdkconfig use command prompt or shell:
 - a. C:> ~/.platformio/penv/Scripts/pio.exe run -t menuconfig(if pio.exe is not in search path, add)
 - b. Alternatively, there is a command line interface (CLI) inside platformio, but up down arrow keys do not work.

```
From VS, PlatformIO icon (ant) => QUICK ACCESS =>
Miscellaneous => PlatformIO Core CLI
Then pio.exe run -t menuconfig
```

- 3. In menuconfig, use slash to find symbol if option is hard to find from menus.
- 4. Save, then exit.
- 5. After restarting VS, allow VS to automatically remake CMakelist

- 6. Would you like to configure project 'SkeletonHuzzah32'? YES
- 7. If getting strange results,
 - a. If get something like:

```
Error: Couldn't find target config target-__idf_cbor-
a7ffc6f8bf1ed76651c1.json
```

Then do 'clean' from toolbar in platformio.

- b. the sdkconfig may have been corrupted. Start over with a fresh version, such as from examples directory.
- 8. May be possible to close and open project folder in VS to read sdkconfig?

10.2 Useful menuconfig things to change

The documentation describes all the options:

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-reference/kconfig.html#component-config

1. CONFIG FREERTOS HZ

```
Component config > FreeRTOS ( -> Tick rate (Hz)
```

Tick rate (Hz) set to 1000 Hz (can update steering servo at 5 ms interval). This is a reasonable tradeoff considering task switch overhead, and gives potentially 1 ms slice for each task.

2. CONFIG FREERTOS GENERATE RUN TIME STATS

```
Component config > FreeRTOS -> Enable FreeRTOS to collect run
time stats
```

vTaskGetRunTimeStats() will display the run time of each task as a % of the total run time of all CPUs (task run time / no of CPUs) / (total run time / 100)

3. CONFIG ESP COREDUMP TO FLASH OR UART

Component config → Core dump (22 down) → Data destination -> UART

Select place to store core dump: flash, uart or none (to disable core dumps generation).

4. CONFIG_ESP_SYSTEM_PANIC_PRINT_REBOOT or

```
ESP SYSTEM PANIC PRINT HALT
```

Component config → ESP32-specific (8 down)→ Panic handler behavior -> Print Registers and halt

Outputs the relevant registers over the serial port and halt the processor. Needs a manual reset to restart.

11. FreeRTOS

ESP-IDF FreeRTOS is based on the Xtensa port of FreeRTOS v8.2.0 with significant modifications for SMP compatibility (see ESP-IDF FreeRTOS SMP Changes). SMP= Symmetric Multi Processing

<u>Furthermore</u>, <u>float</u> cannot be used in interrupt service routines. (probably due to saving state of FPU registers?) Be careful on this. Double is ok, and it may be possible to save FPU state.

https://github.com/espressif/esp-idf/tree/master/components/freertos

Level 1 so ok to use FPU (it will be interrupted by higher priority process)

• Timer 0 (int 6, level 1) (FREERTOS_CORETIMER_0) Select this to use timer 0

Enable FreeRTOS static allocation API: avoids problems with alloc/fragmentation. Probably safer for production code

The function vTaskGetRunTimeStats() will also be available if FREERTOS_USE_STATS_FORMATTING_FUNCTIONS and FREERTOS_USE_TRACE_FACILITY are enabled. vTaskGetRunTimeStats() will display the run time of each task as a % of the total run time of all CPUs (task run time / no of CPUs) / (total run time / 100)

When creating task, make sure stack space is sufficient (good to use for debugging)

See in hell_world_main.c print tasks()

<u>Component config</u> > <u>FreeRTOS</u> -> Allow use of float inside Level 1 ISR (EXPERIMENTAL)

When enabled, the usage of float type is allowed inside Level 1 ISRs.

https://hop.freertos.org/Documentation/RTOS_book.html

Make sure IDLE process is not starved. It is critical for proper operation. Using vTaskDelay() gives Other processes a chance to run.

Task examples

11.1 Getting System Time

System time is read from an interrupt and made available to other processes through a queue. The queue avoids the problem of reading a 64 bit value in the middle of an update, giving an inconsistent time value.

Timer can also be read directly (this is a safe 64 bit read, protected by a spinlock.

```
timer_get_counter_value(timer_group_t group_num, timer_idx_t
timer num, uint64 t *timer val)
```

xQueuePeek: reads queue without emptying

xQueueOverwriteFromISR: overwriting data that is already held in the queue.

xQueueOverwriteFromISR() is intended for use with queues that have a length of one,

meaning the queue is either empty or full.

Time stamp is used for delay measurement such as velocity estimate, and for debugging to keep track of events

11.2 Monitoring Tasks

Things to watch out for:

Running out of stack space (printf uses a lot of stack space) for a task

Starving the ``idle'' process (will cause a crash). Make sure every process has vTaskDelay() for a lower priority process to run

SkeletonHuzzah32 includes void print tasks()

283440623	49%
142791054	24%
145004887	25%
6661	<1%
194739	<1%
55	<1%
60360	<1%
209	<1%
10215	<1%
95764	<1%
13490	<1%
15121	<1%
	142791054 145004887 6661 194739 55 60360 209 10215 95764 13490

Shows how many cycles each task has been using. Make sure

IDLE0 and IDLE1 are gettings cycles, otherwise there will be a "watch dog timer" wdt reset.

vTaskList:

Name	State	Priority	Stack	Task#
control_task	R	2	348	14
usertask	R	0	504	16
IDLE1	R	0	1116	7
IDLE0	R	0	1012	6
heartbeat	В	1	1584	15
timer_evt_tas}	ςВ	2	756	13
Tmr Svc	В	1	1592	8
main	S	1	2476	5
log_task	В	1	856	12
esp_timer	В	22	3640	1
ipc1	В	24	596	3
ipc0	В	24	564	2

Priority: 0 is lowest priority. Usertask is alos low priority as it busy waits for input

Task #: order of task startup

State: R running, B blocked

12. Debugging Tools

Useful menuconfig things to change:

1. CONFIG COMPILER OPTIMIZATION

Compiler options -> Optimization Level

Optimization level: "None" with -O0 produces compiled code without optimization. (sometimes needed if scanning a value which is changed by an external routine such as an IO line or an interrupt.)

2. CONFIG COMPILER STACK CHECK MODE

Compiler options -> Stack smashing protection mode Stack smashing protection mode, Emit extra code to check for buffer overflows, such as stack smashing attacks

CONFIG_ESP_MAIN_TASK_STACK_SIZE
 <u>Component config</u> > <u>Common ESP-related</u> (11 from top) -> Main task stack size check stack size here if run out, default is 3584 byes. ~450 doubles

12.1 Core dump

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-guides/core dump.html?highlight=assert

- 1. Copy and paste base64 encoded core dump to a text file.
- 2. Remove =========== CORE DUMP START ===========
- 4. espcoredump.py info_corefile -t b64 -c
 </path/to/saved/base64/text> </path/to/program/elf/file>

c:>python c:\Users\ronf\esp\esp-idf\components\espcoredump\espcoredump.py info_corefile -t b64 -c coredump.txt .pio\build\featheresp32\firmware.elf

Shows lots of information, not so easy to parse, but does show active tasks/threads. Perhaps dbgcorefile (using gdb) would be more useful.

12.2 Error Codes

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-reference/system/esp err.html

Convert error codes: esp err to name() and esp err to name r()

12.3 Watchdog timers

Watchdogs are useful for detecting stuck code.

Use RTC watchdog in start code: causes watchdog if startup fails

Task Watchdog Timer API Reference

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-reference/system/wdts.html?highlight=task%20watchdog%20timer%20api%20reference

The Interrupt Watchdog Timer and the Task Watchdog Timer (TWDT). The Interrupt Watchdog Timer and the TWDT can both be enabled using Project Configuration Menu, however the TWDT can also be enabled during runtime.

CONFIG ESP INT WDT

Component config → Common ESP-related -> Interrupt Watchdog

Useful menuconfig things to change:

1. CONFIG ESP INT WDT

Component config → Common ESP-related -> Interrupt Watchdog
This watchdog timer can detect if the FreeRTOS tick interrupt has not been called for a certain
time, either because a task turned off interrupts and did not turn them on for a long time, or
because an interrupt handler did not return. (300 ms default timeout). Make this higher than the
FreeRTOS tick rate.

The INT WDT timeout should always be longer than the period between FreeRTOS ticks (see CONFIG FREERTOS HZ).

2. CONFIG ESP TASK WDT TIMEOUT S.

Component config > Common ESP-related > Task Watchdog timeout period (seconds) Watch dog timer triggered (5 seconds by default) if a task does not yield. The Task Watchdog Timer can be used to make sure individual tasks are still running. Enabling this option will cause the Task Watchdog Timer to be initialized automatically at startup. The Task Watchdog timer can be initialized after startup as well (see Task Watchdog Timer API Reference)

A task can then subscribe to the TWDT using <u>esp task wdt add()</u> in order to be watched. Each subscribed task must periodically call <u>esp task wdt reset()</u> to reset the TWDT. Failure by any subscribed tasks to periodically call <u>esp task wdt reset()</u> indicates that one or more tasks have been starved of CPU time or are stuck in a loop somewhere.

Have to make sure idle can run, otherwise will get watchdog timeout. For example, if main() is waiting for keyboard input, then idle can be startved. Always use a

vTaskDelay(1000 / portTICK PERIOD MS); to temporarily block process.

13. Virtual file system:

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/apireference/storage/vfs.html

included by default in sdkconfig, This component allows C library functions, such as fopen and fprintf, to work with FS drivers.

14. Floating Point and Interrupts

From forum: https://esp32.com/viewtopic.php?f=19&t=1292&p=6078&hilit=FPU+state#p6078

Yes, it is solvable: it means messing with the asssembler in the coprocessor handler to add the code to store the FPU state when an interrupt happens and restoring it when the interrupt is done. I have it on my list, but it's fairly low-priority: you can expect a fix eventually, but I don't know the timeframe for that.

```
uint32_t timer0_int = 0;
float timer0_float = 0.0;
DRAM_ATTR float timer0_k = 1.1111111;
uint32 t cp0 regs[18];
```

```
void IRAM ATTR timer0 intr() // Interrupt handler for timer 0
  // get FPU state
  uint32 t cp state = xthal get cpenable();
  if(cp state) {
   // Save FPU registers
   xthal save cp0(cp0 regs);
  } else {
   // enable FPU
   xthal set cpenable(1);
  timer0 int++;
  timer0 float = timer0 int * timer0 k;
  if(cp state) {
   // Restore FPU registers
   xthal restore cp0(cp0 regs);
  } else {
   // turn it back off
   xthal set cpenable(0);
}
```

https://docs.espressif.com/projects/esp-idf/en/latest/esp32/api-guides/freertos-smp.html#floating-points

Likewise due to Lazy Context Switching, only interrupt service routines of lowest priority (that is it the Level 1) can use float, higher priority interrupts do not support FPU usage.

6. SSH/WiFi

https://libraries.io/platformio/LibSSH-ESP32

https://libraries.io/cargo/esp32-hal

https://github.com/espressif/esp-drone/blob/master/docs/en/rst/communication.rst

https://github.com/espressif/esp-idf/tree/master/examples/protocols/sockets

https://github.com/espressif/esp-idf/blob/master/examples/protocols/sockets/udp_client/example_test.py

https://freertos.org/FreeRTOS-Plus/FreeRTOS Plus TCP/API/socket.html

Tutorial here: https://realpython.com/python-sockets/

ERRATA

Windows filenames:

ccache: error: Failed to create temporary file

It looks like you might be running up against path length limitations on Windows. Windows is limited to 260 character paths by default, and it looks like the total path length for some of these build files is pushing up against that.

Use short directory names