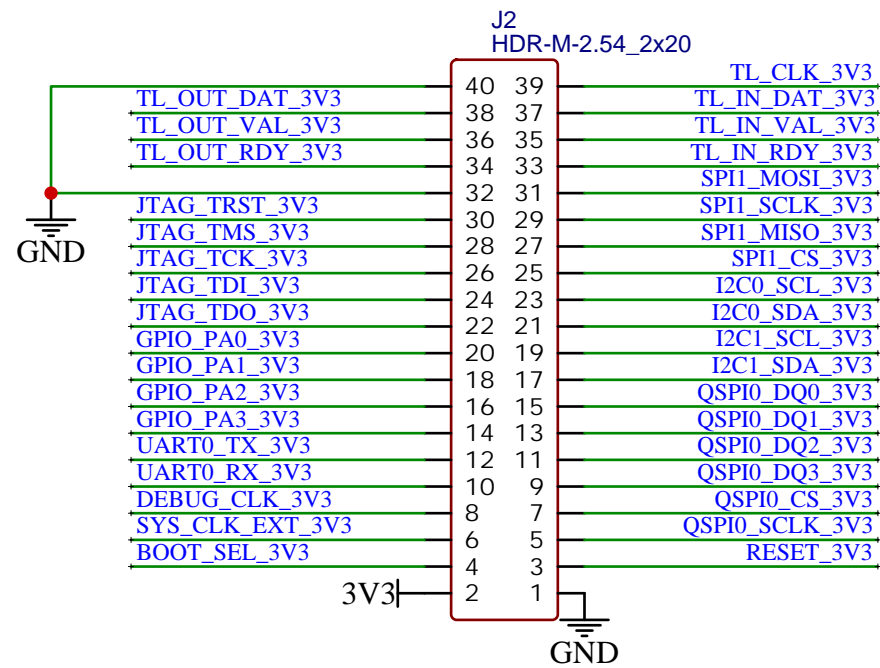
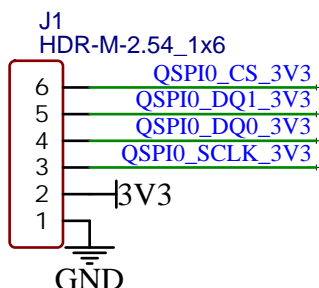


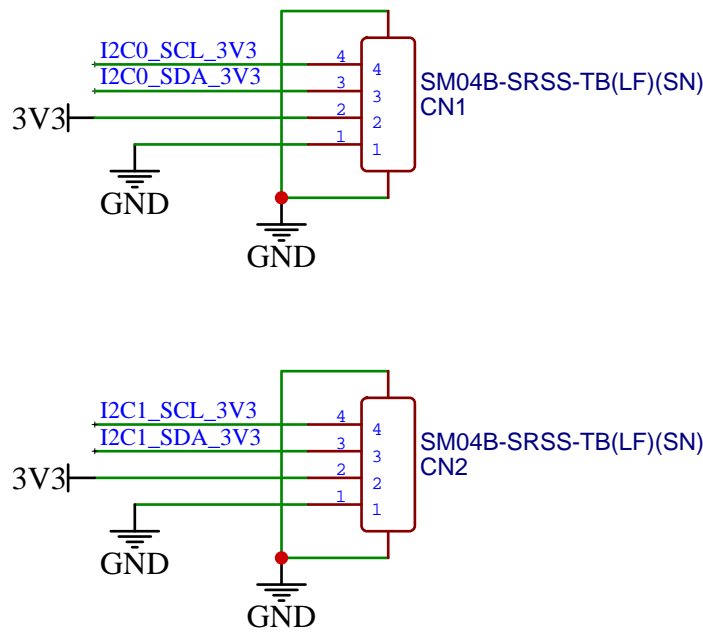
Breakout Header



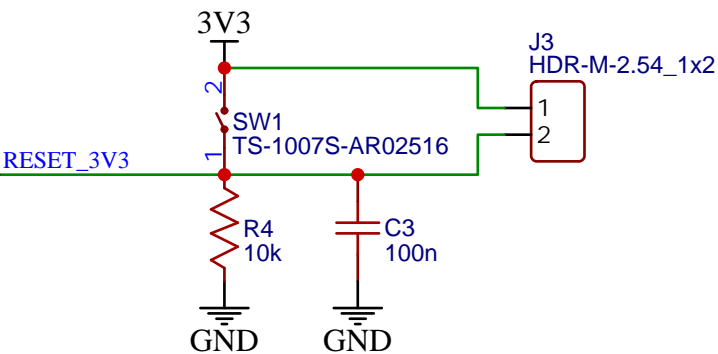
Flash Programming Header



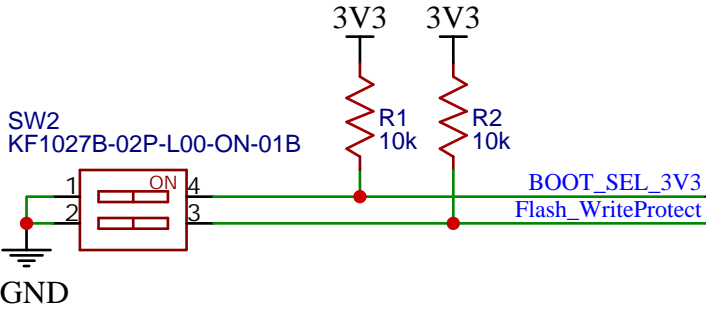
Qwiic I2C Header



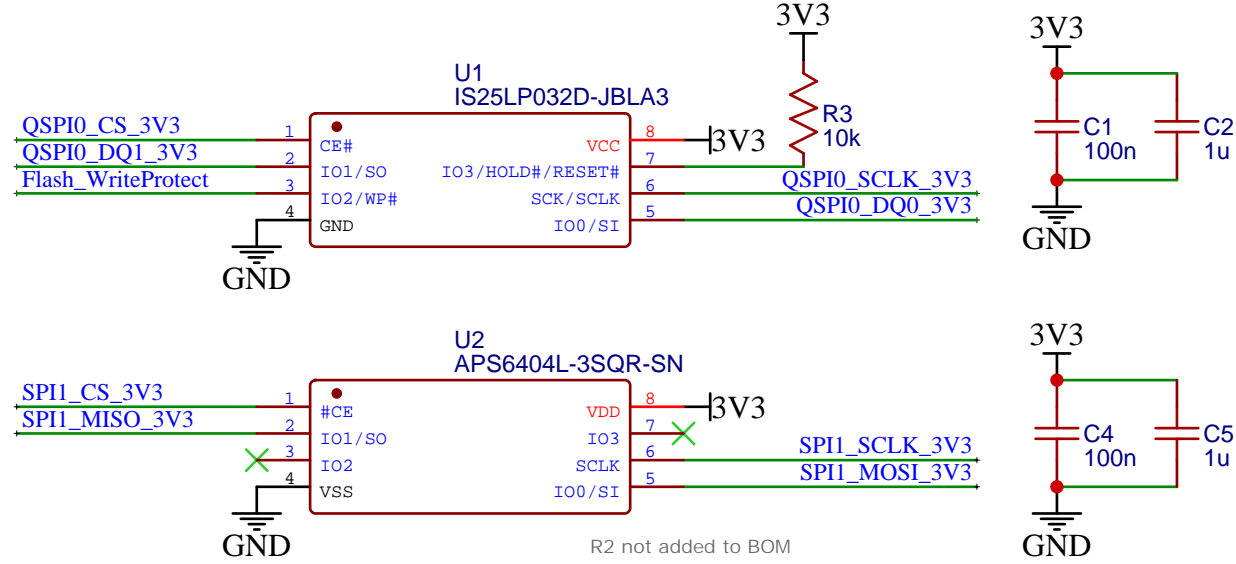
Reset Control



Mode Control



Memory Modules

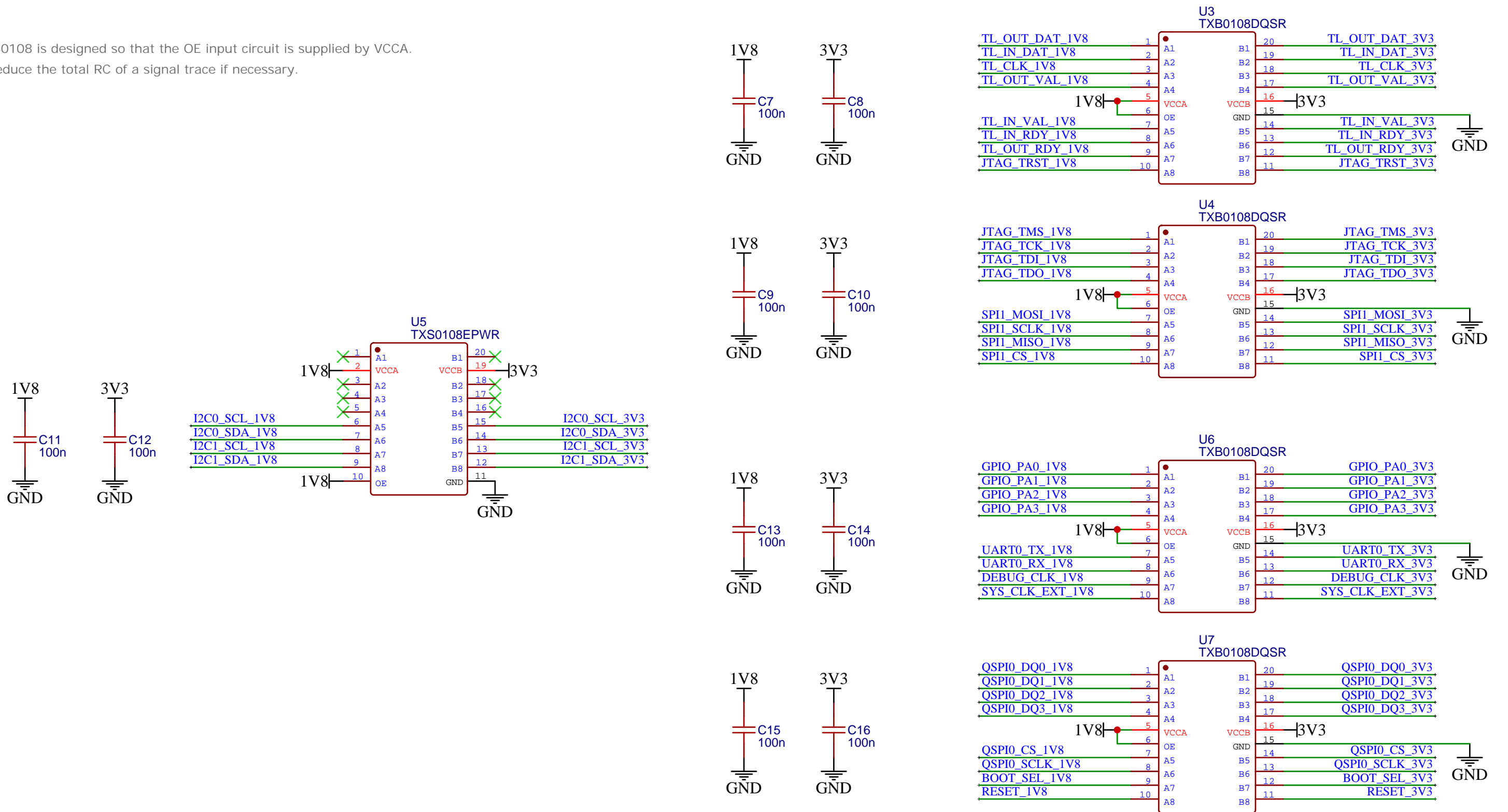


Level Shifts

If pullup or pulldown resistors are needed, the resistor value must be over 50 k Ω .

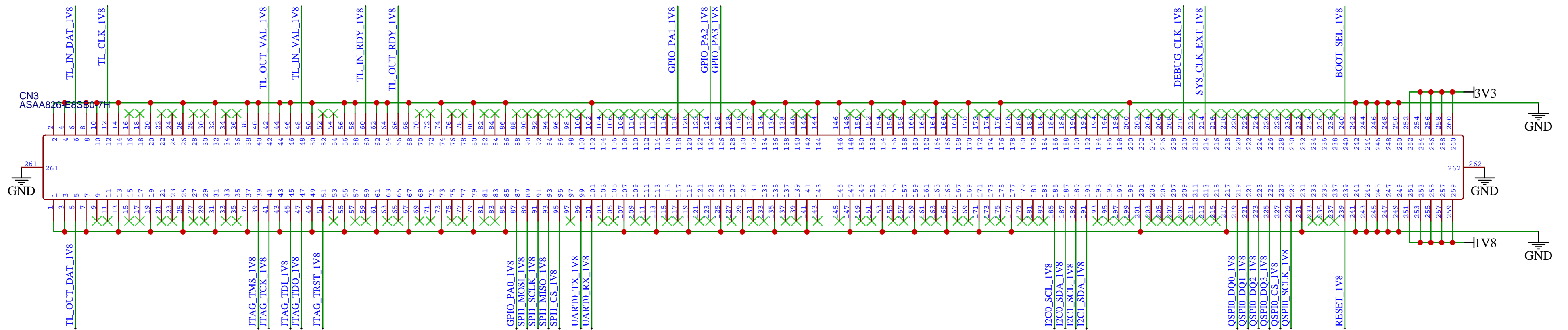
When the output-enable (OE) input is low, all outputs are placed in the high-impedance state. The TXB0108 is designed so that the OE input circuit is supplied by VCCA.

The TXS0108E device has smart internal pull-up resistors. External pull-up resistors can be added to reduce the total RC of a signal trace if necessary.

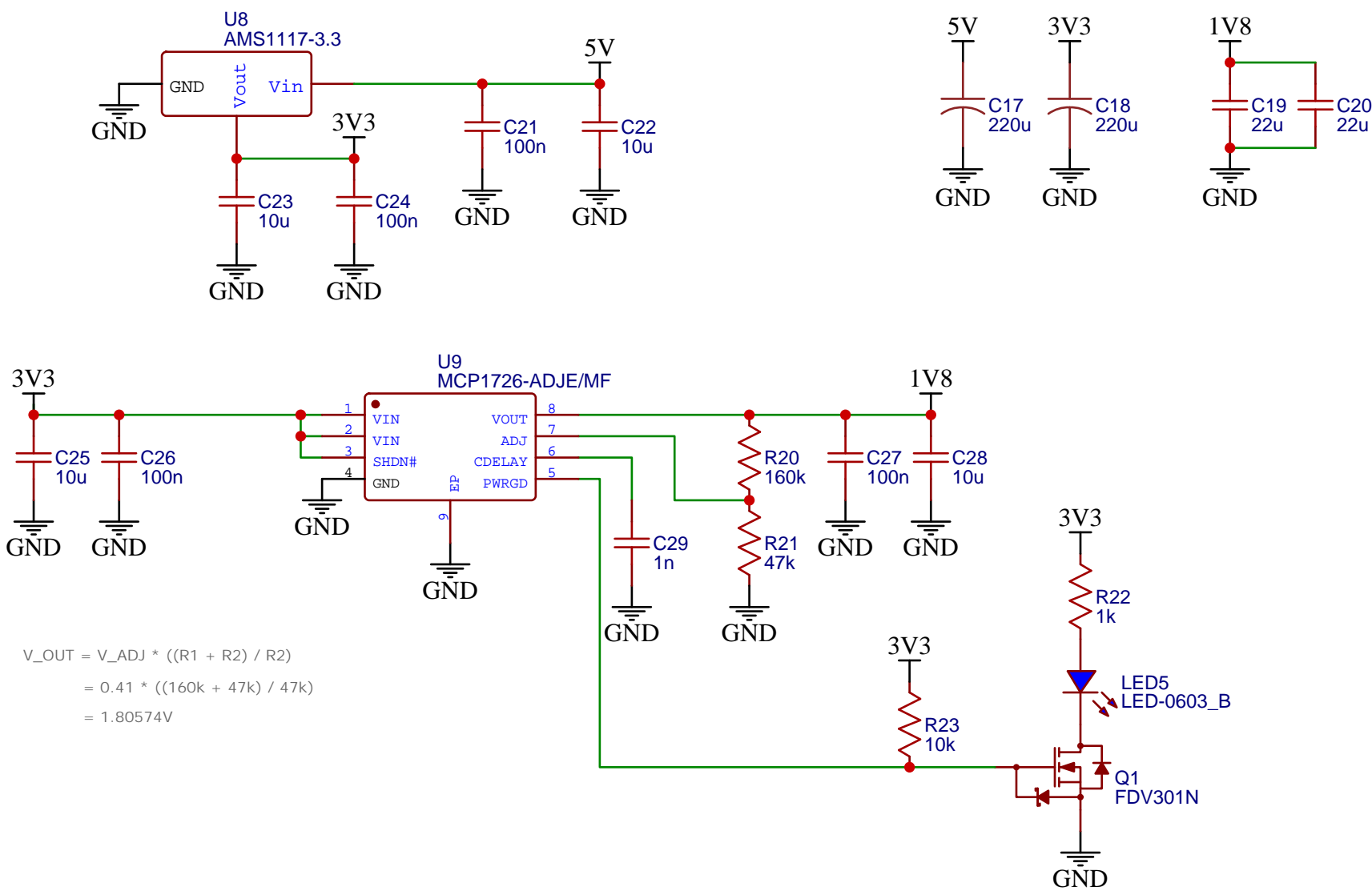


SODIMM Connection

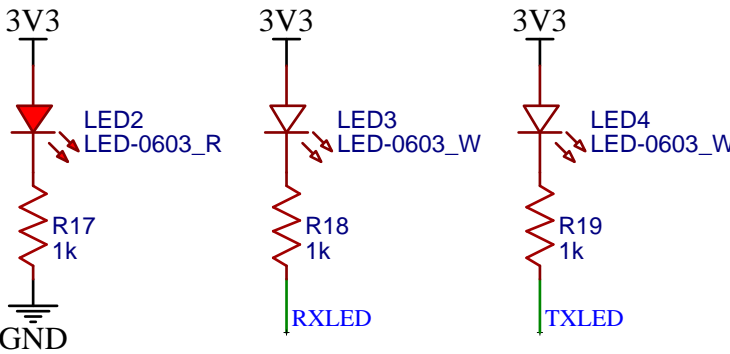
SODIMM Layout: https://atta.szlcsc.com/upload/public/pdf/source/20180612/C225725_24F8E3125FAF536DE50EBBF33D1AD3EA.pdf



LDO

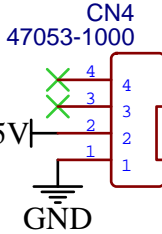


Power Indicator LEDs



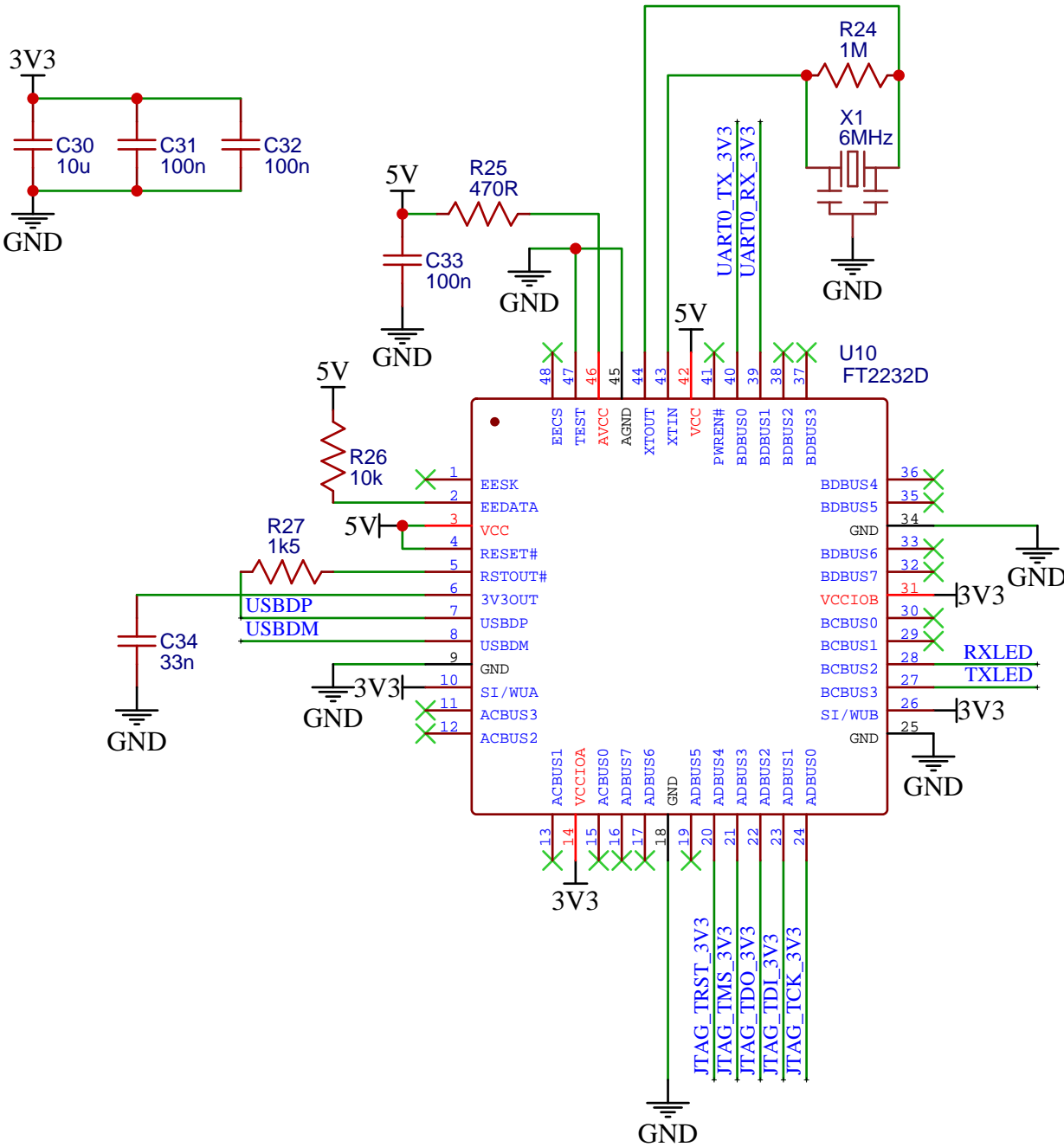
Fan Controller

currently using passive fan control with only a power header



TITLE: Sheet_3		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2022-12-16	Drawn By: T-K-233

FT-LINK



TITLE: Sheet_4		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2022-12-17 Drawn By: T-K-233	