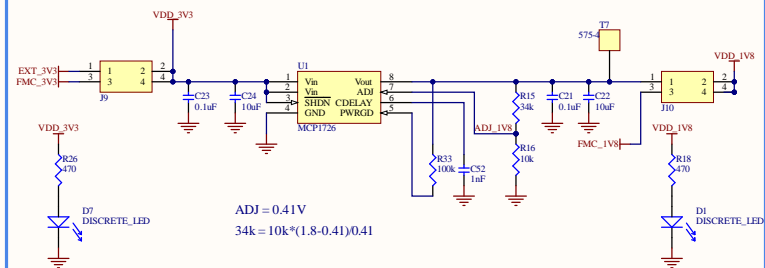
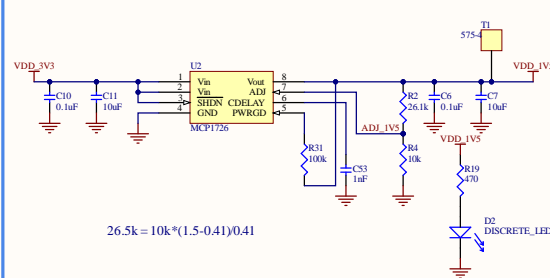


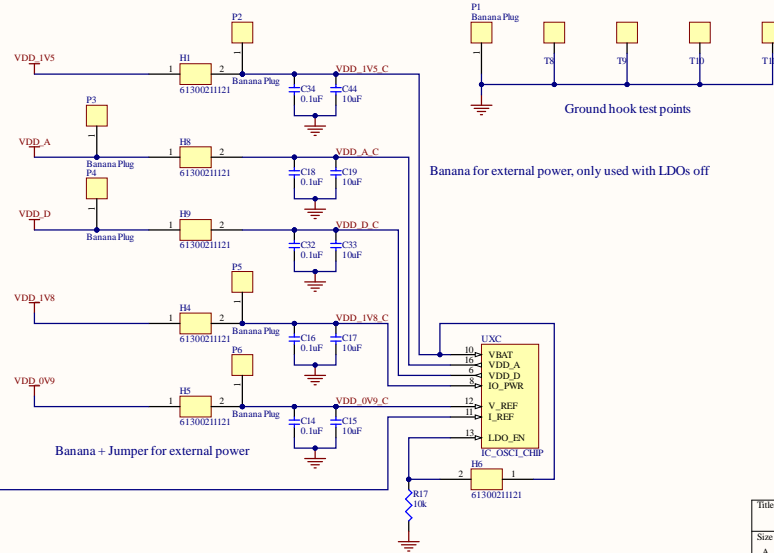
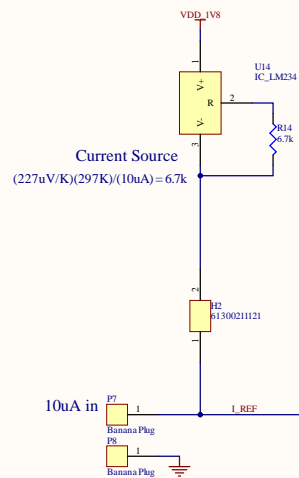
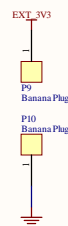
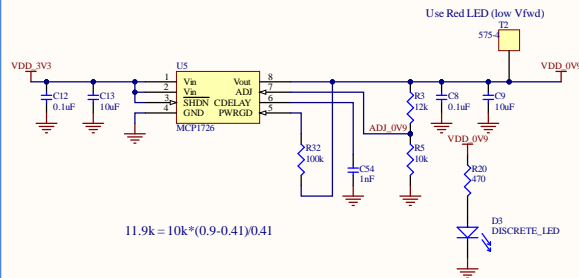
1.8V



1.5V (VBAT)



0.9V (VREF)



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File:	C:\Users\jpower\SchDoc	Drawn By:

1

2

3

4

A

A

B

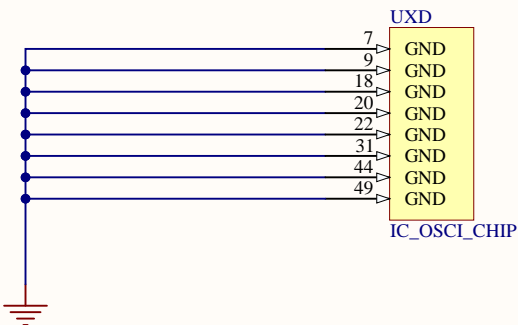
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C

C

D

D



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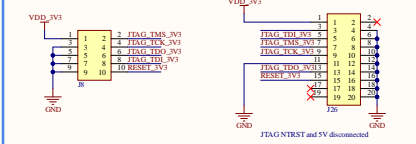
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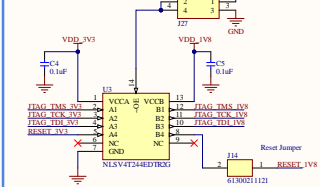
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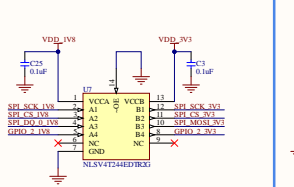
JTAG Connector



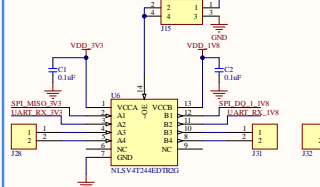
3.3V to 1.8V



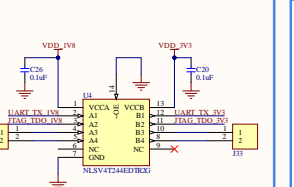
1.8V to 3.3V



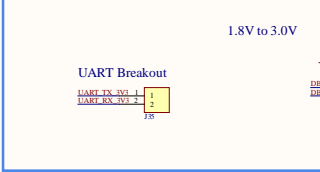
Enable toggle



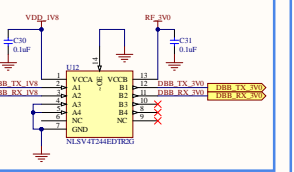
Reset Jumper



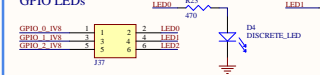
Spare shifter lines, tie unused to ground



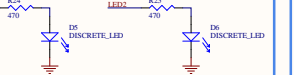
Spare shifter lines, tie unused to ground



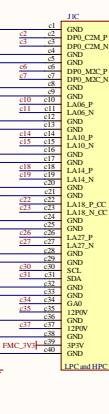
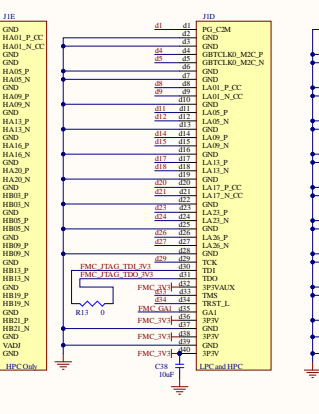
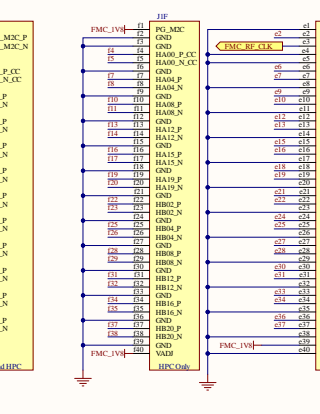
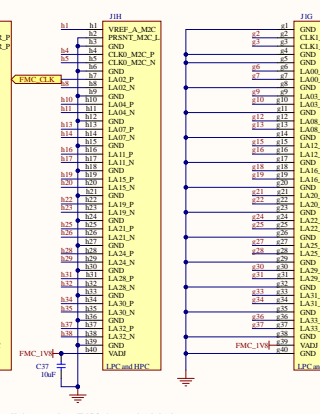
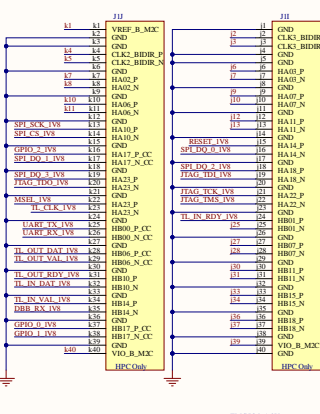
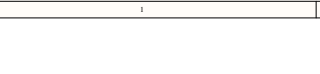
UART Breakout



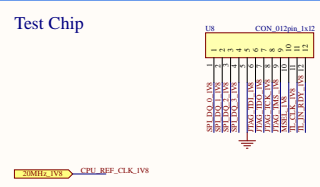
1.8V to 3.0V



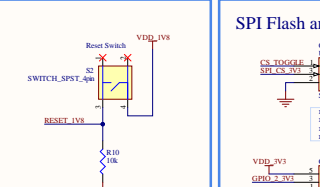
GPIO LEDs



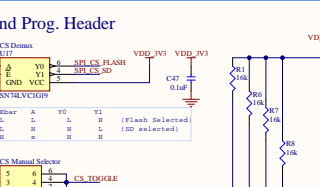
Test Chip



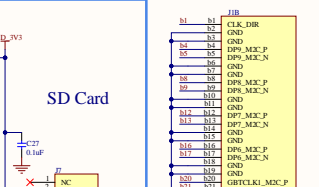
SPI Flash and Prog. Header



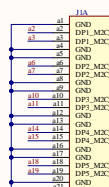
SD Card



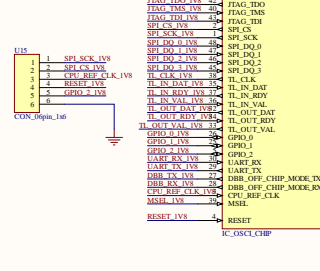
Flash Programming Header



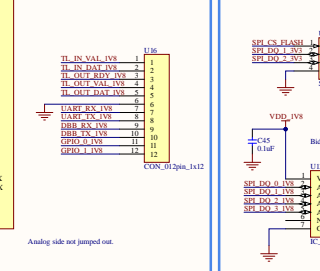
SD Card



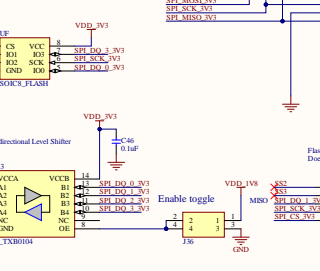
GPIO LEDs



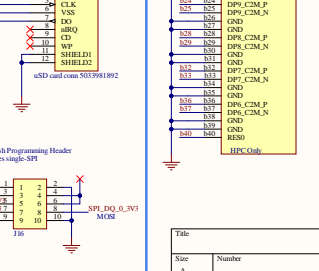
UART Breakout



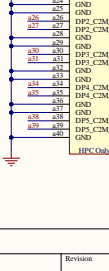
1.8V to 3.0V



Spare shifter lines, tie unused to ground

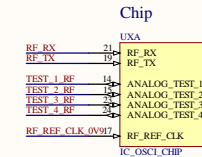
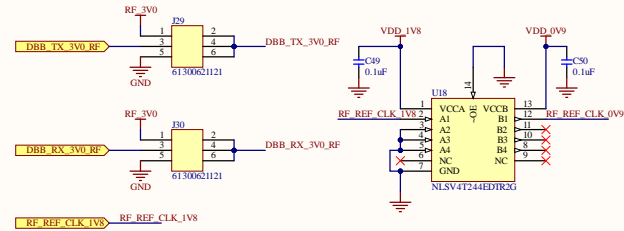
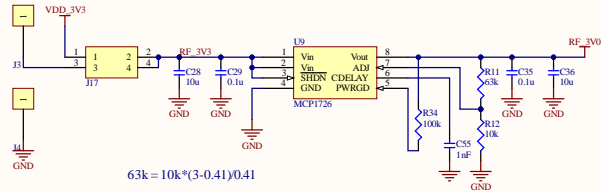


Spare shifter lines, tie unused to ground

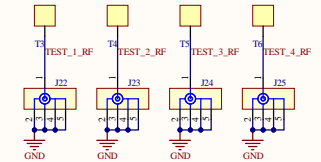


3.3V to 3V Regulator

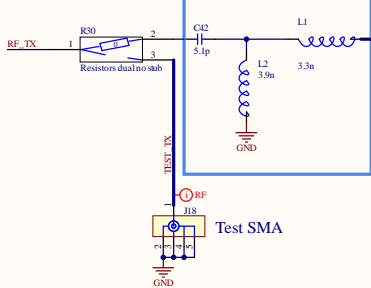
Banana Jack Power or onboard 3V3



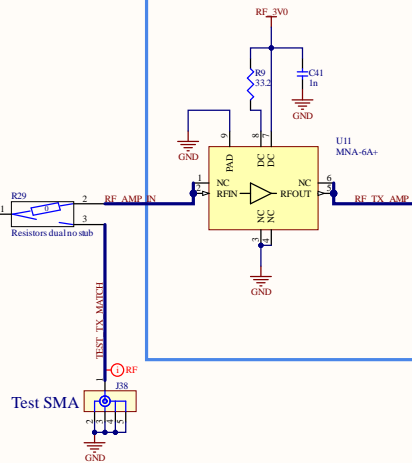
Analog Test Pins



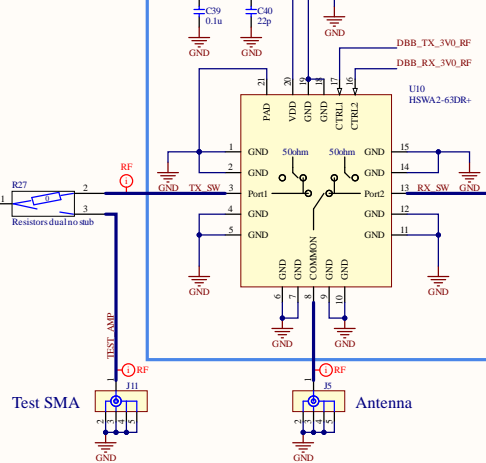
TX Matching



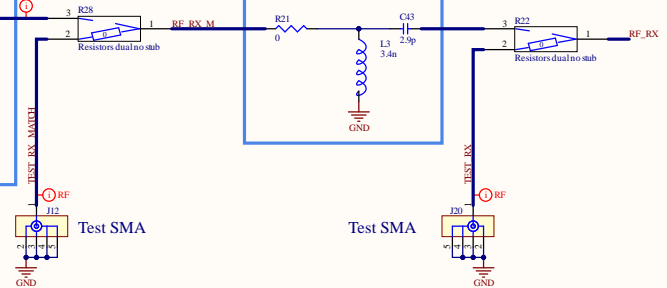
RF Amplifier



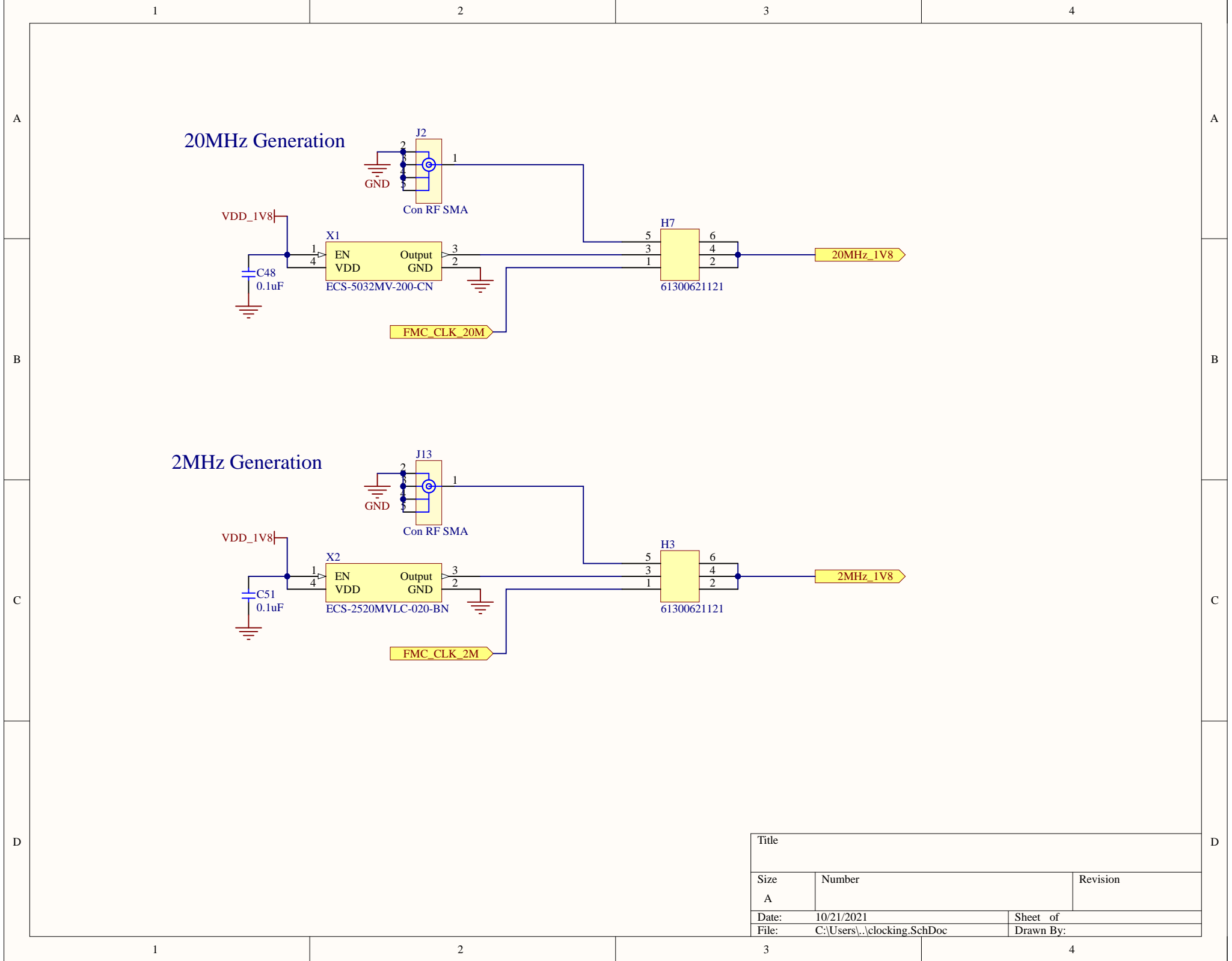
RF Switch



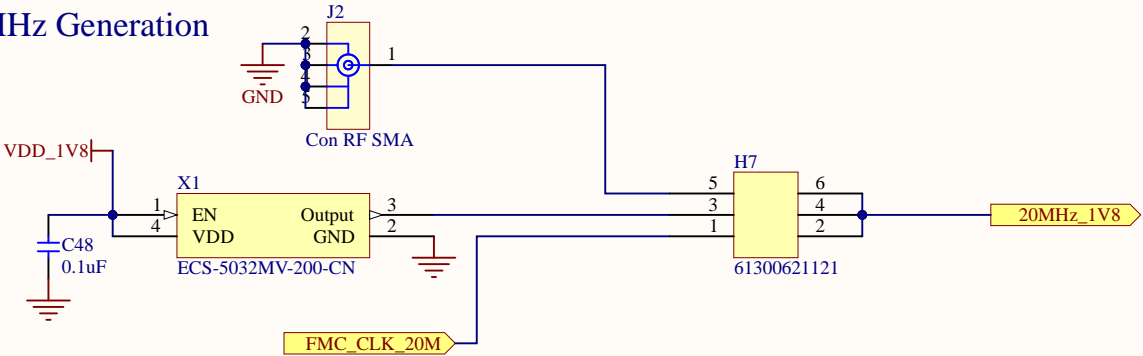
RX Matching



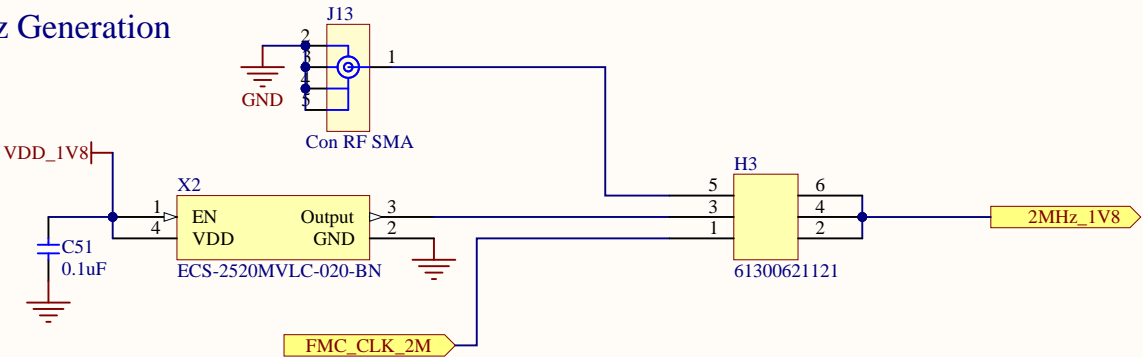
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20MHz Generation



2MHz Generation



Title		
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Date:	10/21/2021	Sheet of
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