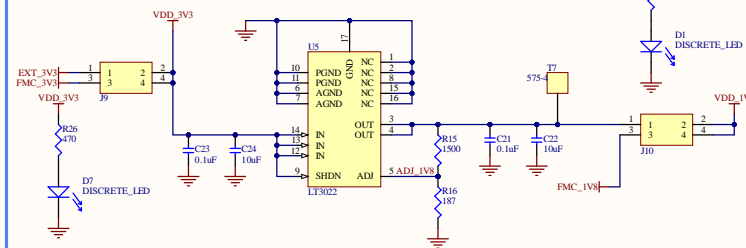
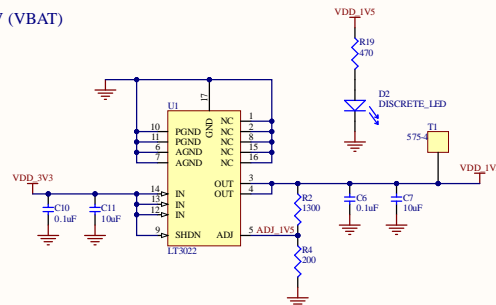


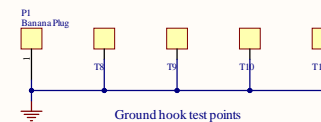
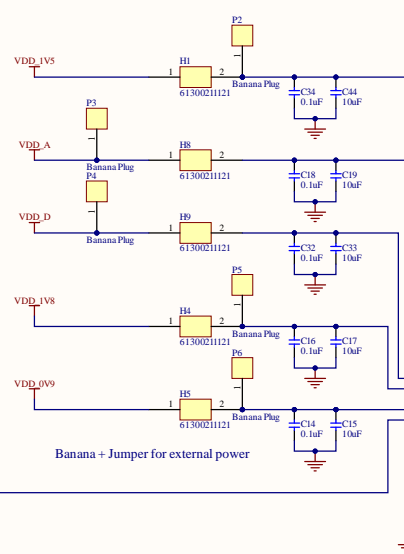
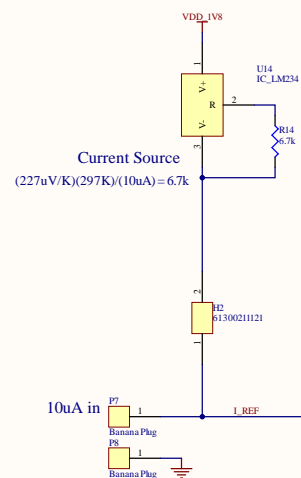
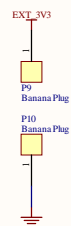
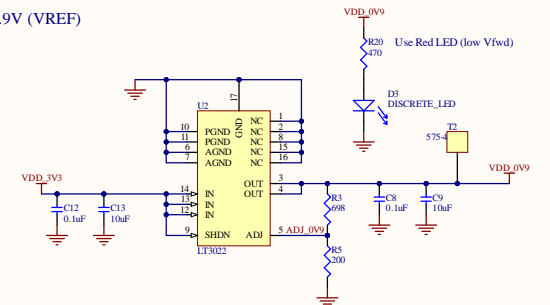
1.8V



1.5V (VBAT)



0.9V (VREF)



Banana for external power, only used with LDOs off

Banana + Jumper for external power

Title		
Size	Number	Revision
A		
Date:	9/24/2021	Sheet of
File:	C:\Users\...power\SchDoc	Drawn By:

1

2

3

4

A

A

B

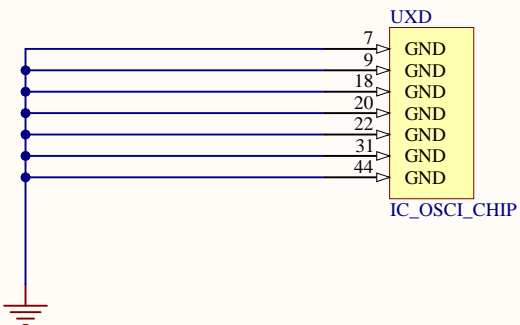
B

C

C

D

D



Title		
Size	Number	Revision
A		
Date:	9/24/2021	Sheet of
File:	C:\Users\...\ground.SchDoc	Drawn By:

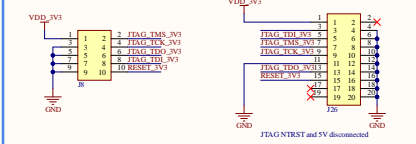
1

2

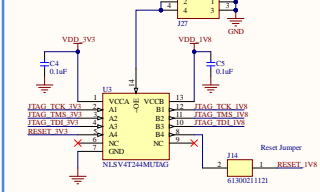
3

4

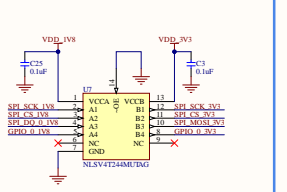
JTAG Connector



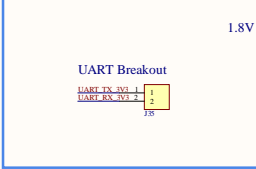
3.3V to 1.8V



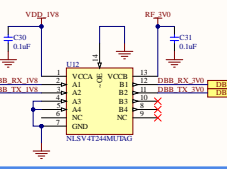
1.8V to 3.3V



Spare shifter lines, tie unused to ground



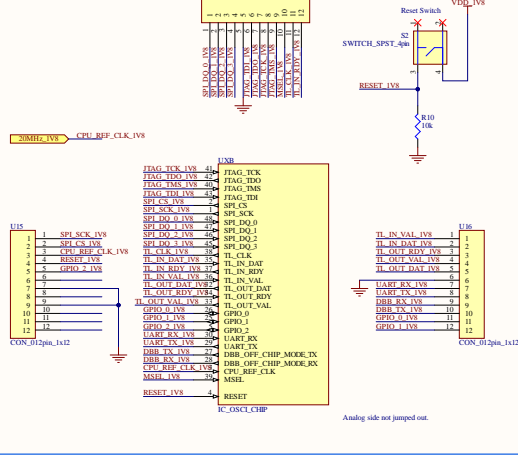
Spare shifter lines, tie unused to ground



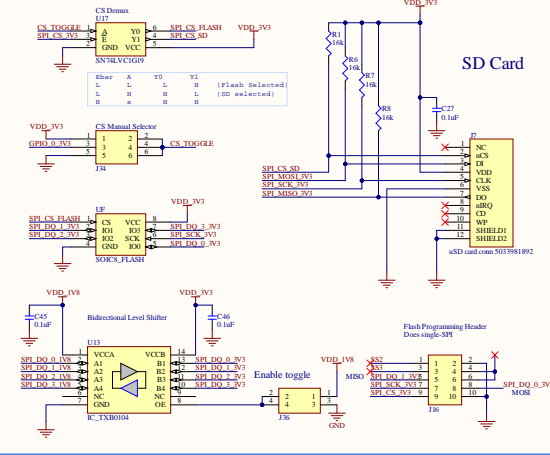
GPIO LEDs



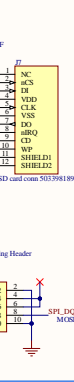
Test Chip



SPI Flash and Prog. Header

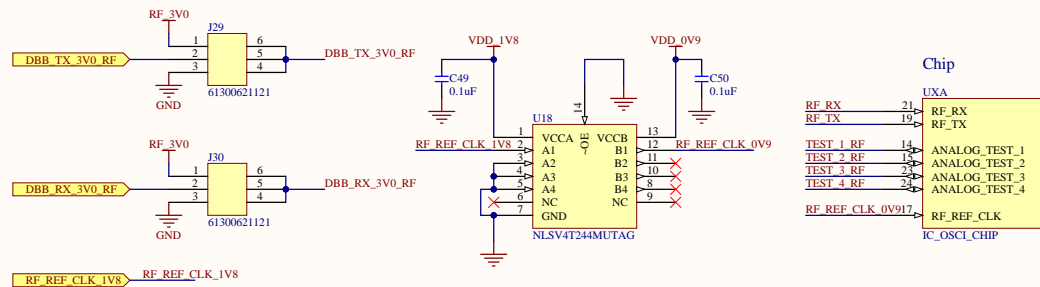
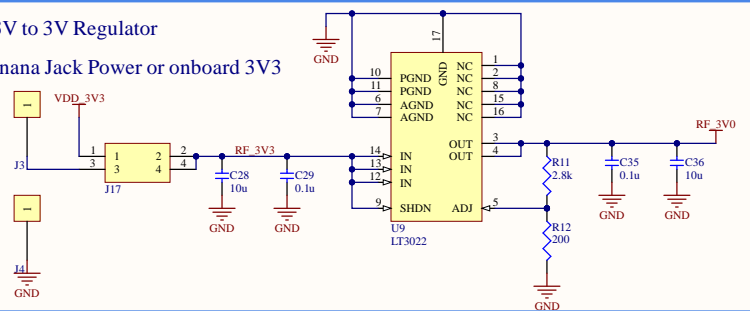


SD Card

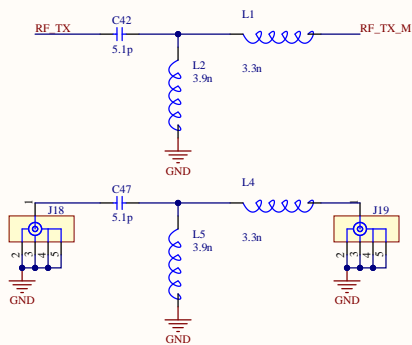


3.3V to 3V Regulator

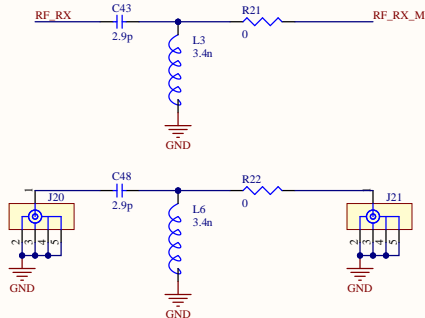
Banana Jack Power or onboard 3V3



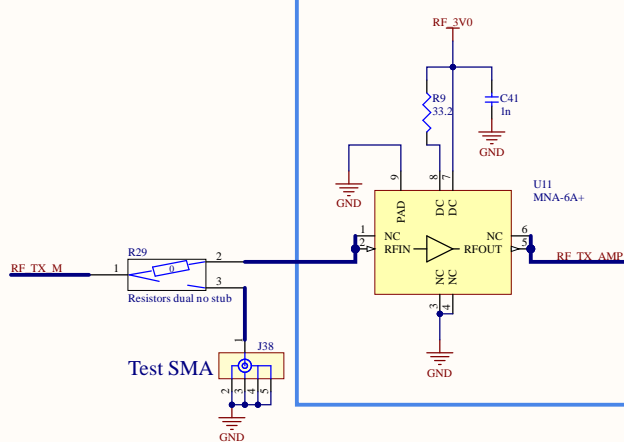
TX Matching



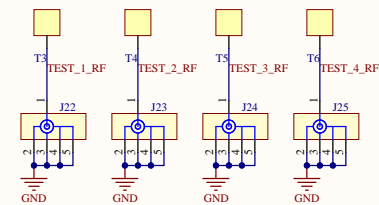
RX Matching



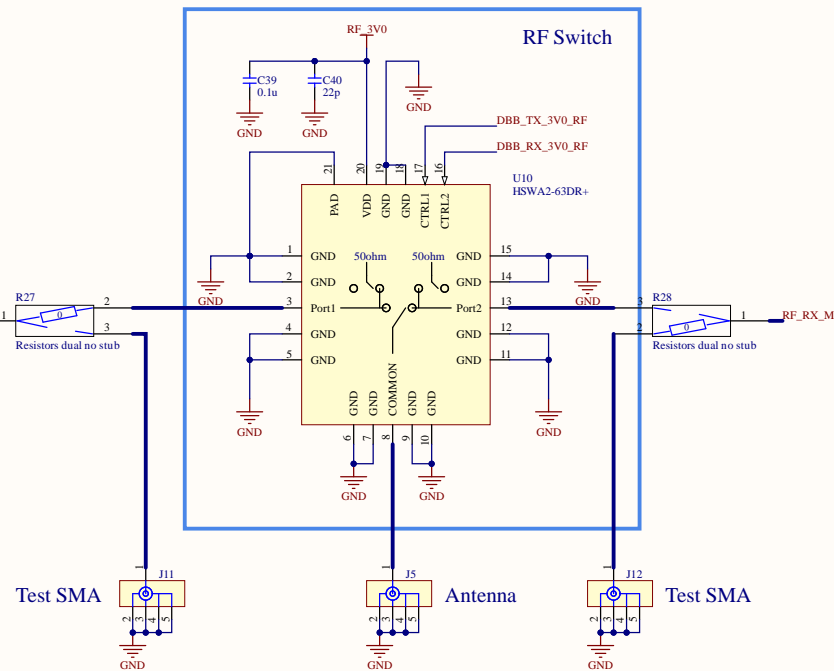
RF Amplifier



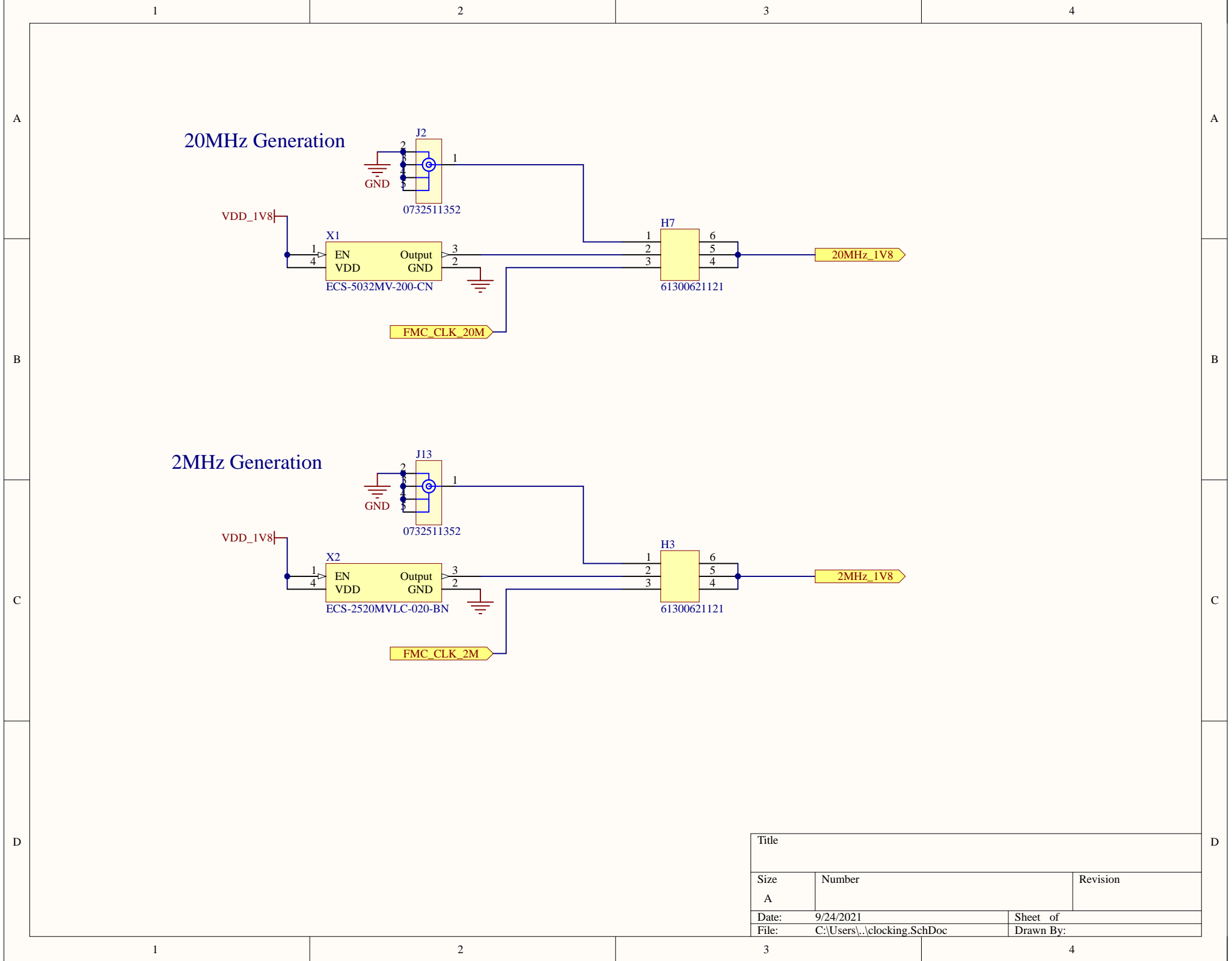
Analog Test Pins



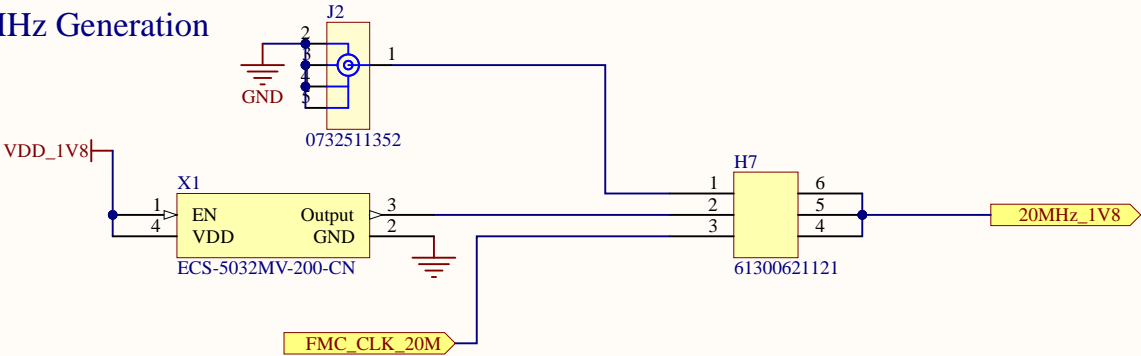
RF Switch



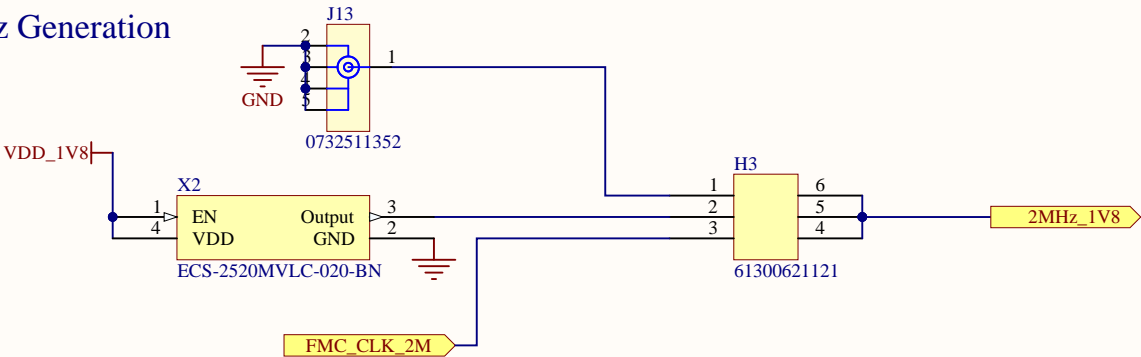
Title		
Size	Number	Revision
A		
Date:	9/24/2021	Sheet of
File:	C:\Users\...\rf_top.SchDoc	Drawn By:



20MHz Generation



2MHz Generation



Title		
Size	Number	Revision
A		
Date:	9/24/2021	Sheet of
File:	C:\Users\...\clocking.SchDoc	Drawn By: