

Lab 1: Elements of Electronic Systems

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1 Objectives

The main objective of this lab is to understand the basic components of a typical electronic system. Specifically, this include the following:

1. Understand the functionality and characteristics of linear and switching voltage regulators;
2. Learn how to use simple micro-controllers, such as the Arduino Uno and the Teensy 3.1;
3. Learn the functionality and use of digital-to-analog converters (DAC) and analog-to-digital converters (ADC);
4. Learn how to use the serial programming interface to control electronic components;
5. Learn the basic skills of designing and laying out a printed circuit board (PCB);
6. Learn the basic skills PCB assembly involving surface mount (SMD) components.

To this end, we will build a simple electronic system shown in Fig. ??.

Be warned that this lab is a fairly aggressive one and it will take a lot of time for you and your group to finish all the reading, the pre-lab assignment, the actual lab, and the reports. It’s a good idea to start early! And divide up tasks between group members wisely!

2 Prelab

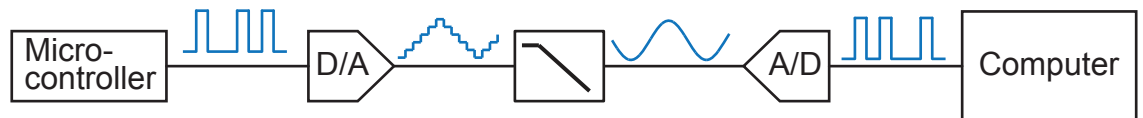


Figure 1:

2.1 Voltage Regulators

In the late 1880s, a heated battle over the best mechanism to transport electricity over long distance broke out between proponents of direct current (primarily Thomas Edison) and alternating current (primarily George Westinghouse and several European companies). History eventually settled on ac current as the preferred method for long distance distribution because of its ability to be easily transformed into high voltages to reduce resistive loss along the wires. So today we all have ac outlets at home and in the lab. However, most if not all the circuits we have studied in our curriculum are powered from dc supplies. Have you ever wondered how dc voltages are generated from an ac supply, other than the simple and trivial answer of “get it from the lab power supply”? The following videos may be instructive. Make sure you watch them carefully.

1. How to build an AC-DC power supply:
<https://www.youtube.com/watch?v=cyzpFqXwdA>
2. Linear voltage regulator:
https://www.youtube.com/watch?v=GSzVs7_aW-Y
3. Adjustable linear voltage regulator:
<https://www.youtube.com/watch?v=IjJWWGPjc-w>
4. Switch-mode voltage regulator:
https://www.youtube.com/watch?v=CEhBN5_f05o

Assignment 1.1

Due: TBD

Please answer the following questions:

- a) What are the advantages and disadvantages of using switch mode voltage regulator vs a linear voltage regulator?
- b) For the circuit of Fig. 2, with sing an input voltage of 9V and $R_1=510\ \Omega$, what's the value of R_2 such that the output voltage is 5 V. What is the efficiency of the regulator in this case?

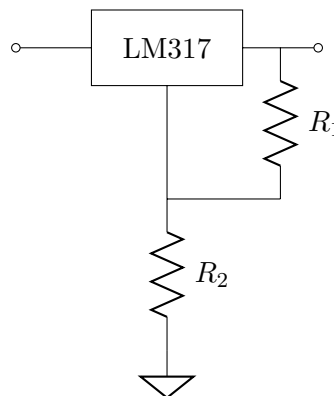


Figure 2: LM317 linear regulator circuit.

- c) For LM317 from TI, what is the typical drop out voltage? If an input voltage of 12 V is used, what range of output voltage can be considered regulated?
- d) What is the maximum efficiency of the LM2694 switch mode voltage regulator for an output voltage of 5 V? Under what conditions is this efficiency achieved?

3 Equipment Supplies

- breadboard
- jumper wires
- Arduino UNO development board
- Teensy 3.1 development board (optional)
- MCP4921 digital-to-analog converter (DAC)
- misc resistors
- misc capacitors
- 8x AA rechargeable batteries
- battery pack

4 Procedures

4.1 Power supply/voltage regulator

We will use batteries to power up our system. Batteries provide the cleanest (i.e. very little noise) type of supply but with one major disadvantage, their voltage keeps dropping due to increased internal resistance under discharge. A voltage regulator is therefore often used to provide a relatively stable dc voltage supply.

In our design, a LM317 adjustable regulator IC will be used to provide the supply voltages for several parts in the system. LM317 is a linear voltage regulator, which can provide a very clean output voltage and is quite simple to use. A linear regulator can only provide regulated output voltage lower than the primary voltage source. Another disadvantage of a linear regulator is its low efficiency when the difference between the input and output voltages is large. Because the same amount of current flows through the regulator and the load, the power being dissipated is roughly.

$$P_d = I_{load} * (V_{in} - V_{out}) \quad (1)$$

When the load current is high, this power dissipation can be significant. It is therefore important to provide good heat sinking to the regulator IC. In

fact, many regulator ICs, such as the LM317, have large exposed metal pad with low thermal resistance to facilitate mounting a heat sink. However, it is very important to note that the metal pad on some regulator ICs, such as the LM317, is connected electrically to the output of the IC. If the heat sink may potentially come in contact with any other part of the circuit (including the enclosure, which often is tied to ground), proper isolation is needed between the IC and the heat sink.

1. Using Fig. 3 as a reference, build the regulator circuit on the breadboard.

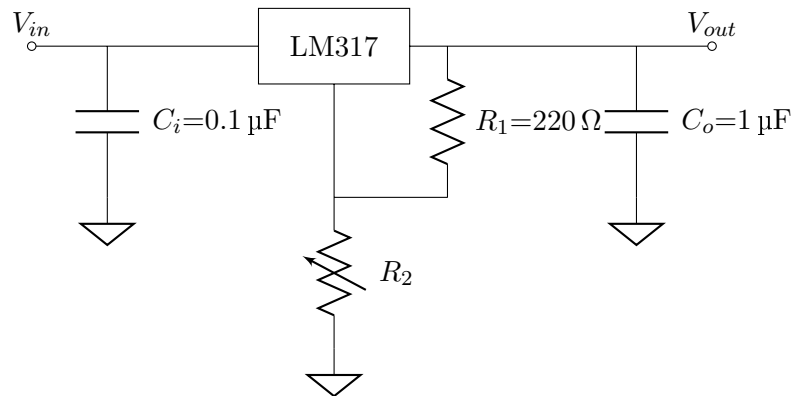


Figure 3: Schematic of the voltage regulator circuit using LM317.

2. Use the batteries as your input power. Adjust R_2 until the output voltage is 5 V. For the LM317, you will need 8 AA batteries in total (2 packs). You should use the multimeter to measure the output voltage.
3. Use the oscilloscope to measure the regulator output vs time. Is the output stable?
4. Now use the bench power supply as the input to the regulator, adjust R_2 until the output voltage is 5 V. Observe the output voltage on the oscilloscope. Compare with the case when batteries were used as the input power. Which one gives a more stable output? What could have caused the difference?
5. Line regulation:
 - (a) Use the bench power supply as the input; set it at 7 V;
 - (b) Adjust R_2 until the output is 5 V;
 - (c) Adjust the input voltage from 7 V to 5 V at 0.25 V intervals, and then from 5 V to 2 V at 0.5 V intervals, record the output voltage;
 - (d) Plot your results. In what input voltage range does the LM317 provide good line regulation?

4.2 Function generator

There are numerous ways to build a function generator. You can use a 555 timer, a ring of inverters, dedicated oscillator ICs, or the more recent invention of direct digital synthesis (DDS) circuits. A DDS circuit takes advantage of the ever-increasing speed of digital circuits to implement fast waveform generators. It does so by outputting values from a look-up table (LUT), which stores the desired waveform in discretized values, and converting them to an analog signal by a digital-to-analog converter (DAC).

In this lab, we are going to use the Arduino UNO to generate and send data to an MCP4921 DAC in order to generate a triangle wave. Since DAC outputs are discrete in nature, we are really just approximating the triangle wave with a stair-case waveform (Fig. 4). Obviously, the higher the resolution of the DAC, the better this approximate is.

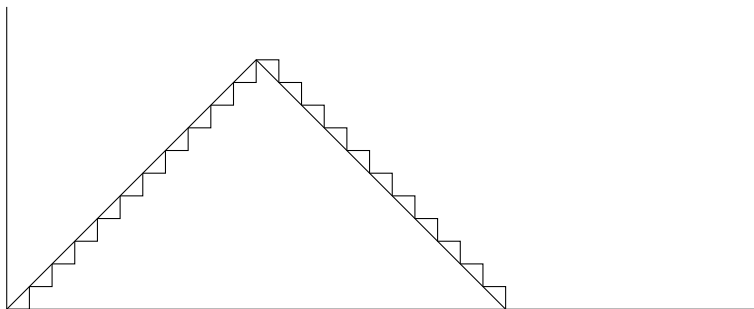


Figure 4: Approximating a triangle wave using the stair-case output of a DAC.

1. Using Fig. 5 as a reference, connect the Arduino UNO to the MCP4921 DAC. Connect VDD to 5 V and both $AVSS$ and \overline{LDAC} to ground. A $0.1\ \mu\text{F}$ and $1\ \mu\text{F}$ capacitors could be used at $VREF$ to minimize noise at the reference. Connect the UNO with the computer using the USB cable, which is used both to program the UNO and power it up. It is very important to keep your circuit (ICs, resistors, capacitors, wires, etc) organized. Use as short of a wire as possible between two connection points. Refer to Fig. 6 for an example layout.
2. Once the circuit is built, open the Arduino IDE, create a new sketch, and input the code “triangle.ino” (<https://github.com/ucdart/UCD-EEEC134/blob/master/Lab1/triangle.ino>). **Make sure you read through and understand the code.** You may want to read the datasheet of MCP4921 to understand its SPI control protocol. Compile and upload the code to the Arduino Uno as explained in the pre-lab tutorials.
3. In your report, include a screen capture of both the triangle (at $VOUTA$) and sync (at $SYNC$) output signals on the oscilloscope. Record their amplitudes and periods.
4. Disconnect the $VREF$ pin of the DAC from the 5-V regulator and connect it to an external 2.5-V supply. How does changing the reference



Figure 5: Schematic of the connection between Arduino UNO and the MCP4921.

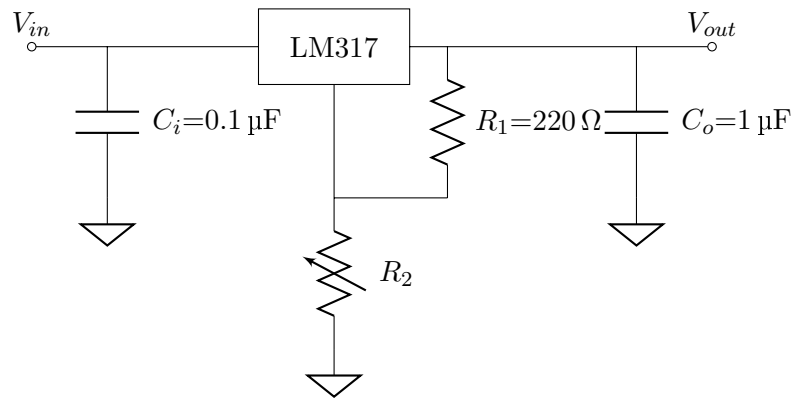


Figure 6: Connection of the Arduino and the DAC chip. Only the top DAC section of the breadboard needs to be assembled for this lab. Power supply is also not shown in this figure.

voltage affect the output signal?

5. How can you modify the code to change the amplitude and period of the output waveform? What is the fastest triangle wave you could generate? What do you think is the limitation to going even faster?
6. Modify the code to generate a sinusoidal wave. What is the highest frequency sinusoidal wave you can generate? The following link may give you some hints. <http://interface.khm.de/index.php/lab/experiments/arduino-dds-sinewave-generator/>
7. **(Challenge!)** Research and propose a design that will allow you to generate a 100-kHz triangle and/or sine wave

4.3 Low-pass filter

In this part of the lab, we will implement an active low pass filter (LPF) with a gain stage. Fig. 7 shows the schematic of this part of the circuit. Fig. 8

Figure 7: Schematic of the active low-pass filter.

Figure 8: Schematic of the active low-pass filter.

shows an example of the actual circuit on a breadboard. It also filters out any spurious signals above a certain threshold frequency (15 kHz in this design). To get the maximum resolution from the digitizer, the maximum strength of the signal should be less than the allowed input voltage range of the digitizer.

In our simple radar system, we are going to use the soundcard of the PC as the digitizer. The amplified and filtered IF signal is fed to the right channel of an audio card, which is conventionally connected using the red wire of a 3.5mm stereo jack or the ring of the connector (Fig. 6.10.3).

4.3.1 Gain stage

We will use the Texas Instrument OPA4228 quad Op-Amp for both the gain stage and the active LPF. A pinout diagram of OPA4228 is shown in Fig. 6.10.4.

Identify the gain stage of the audio amplifier from Fig. 6.10.1. What is the expression of its gain? Notice the +5V bias does not affect this result, rather it simply shifts the bias of the overall amplifier since a single 9V supply (battery) is used vs a double +/- supply. Build just the gain stage of the audio amplifier. Remember to power the op-amp chip by using 9V for V+ and ground for V-. Use the 9V battery as the supply for all the measurements to achieve better noise performance. Remember to disconnect the battery when measurements are not being made. The gain is controlled using a 20 k potentiometer (POT). To test the circuit, input a 1-kHz 100 mVp-p sine wave to the IF input using a signal generator. What is the maximum gain before clipping occurs, and what is the corresponding peak-to-peak voltage at the output? The audio card on a typical laptop is designed to take in line level signals. Notice that this limits the sound card input signal strength to approximately 3 Vp-p. Adjust the pot to achieve 3 Vp-p on the oscilloscope at 1 kHz. Tabulate and plot the gain from 100 Hz to 3 MHz while keeping the pot fixed. Find the 3-dB cutoff frequency or the 3-dB bandwidth at this gain. Use at least 20 data points.

4.3.2 Active LPF

Build the rest of the audio amplifier circuit, i.e. the active LPF. To measure just the filter response, disconnect R4 and R14 from the circuit (Fig. 6.10.1). Input a 3-Vp-p signal in place of R4 and measure the output in place of R14. R14 will only be needed as a current limiting resistor when connecting the amplifier to the laptops audio card. Again tabulate and plot the frequency response from 100 Hz to 20 kHz. What is the cutoff frequency of the filter? The overall filter consists of 2 low pass filters. What is the order of the overall filter and how is it determined from the schematic? Is the bandwidth of

the filter tunable? How would one increase or decrease the cutoff frequency? What limitation does the cut-off frequency of the LPF place on the radars performance? (Hint: what happens when your object is too far away)

4.3.3 Gain Stage + Filter

Reconnect R4 (R14 is still disconnected) and measure the overall response of the amplifier. Input a 100 mVp-p sine wave to the IF input. Adjust the pot for a 3 Vp-p output at 1 kHz and once again sweep the frequency from 100 Hz to 20 kHz while tabulating the voltage output and the gain. What is the cutoff frequency? Compare the results to previous measurements. Tabulate and plot all of the results for the report. Reconnect R14. Break the audio cable and connect the LPF output to the right channel and the SYNC signal (from the modulator) to the left channel.

4.4 Analog to digital converter

4.5 Signal processing

Use teensy 3.1 to sample and process data? sounds like too much work...
Using the laptop

References

- [1] Leslie Lamport, *L^AT_EX: a document preparation system*, Addison Wesley, Massachusetts, 2nd edition, 1994.