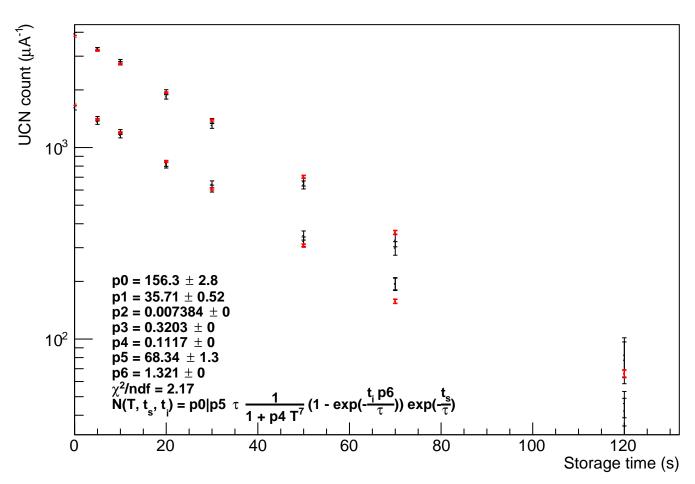
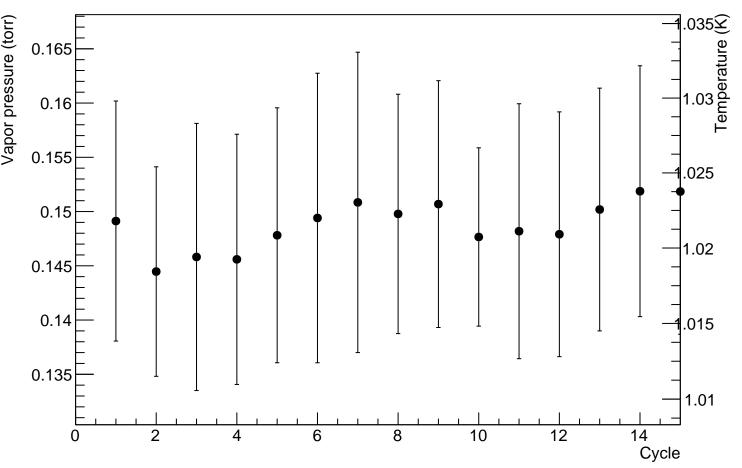
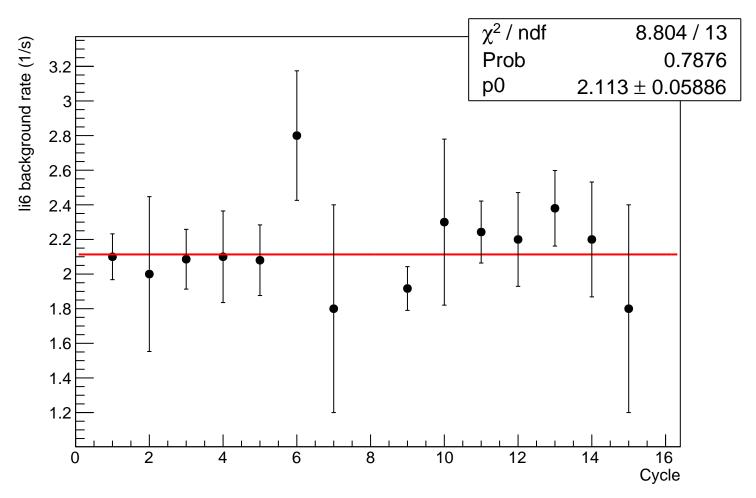


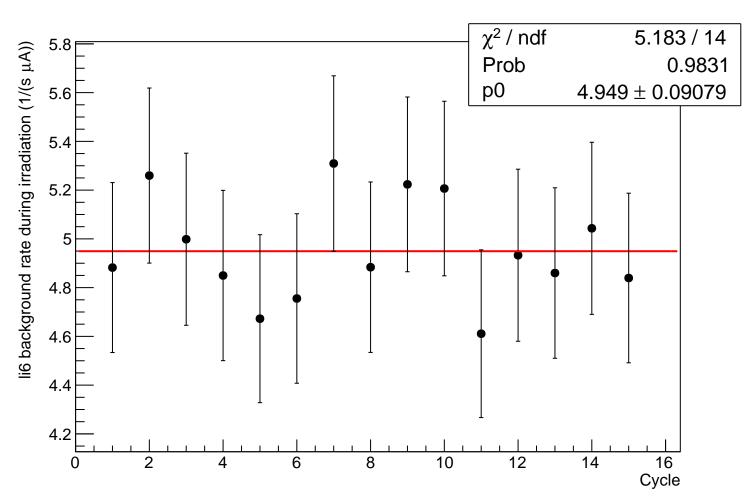
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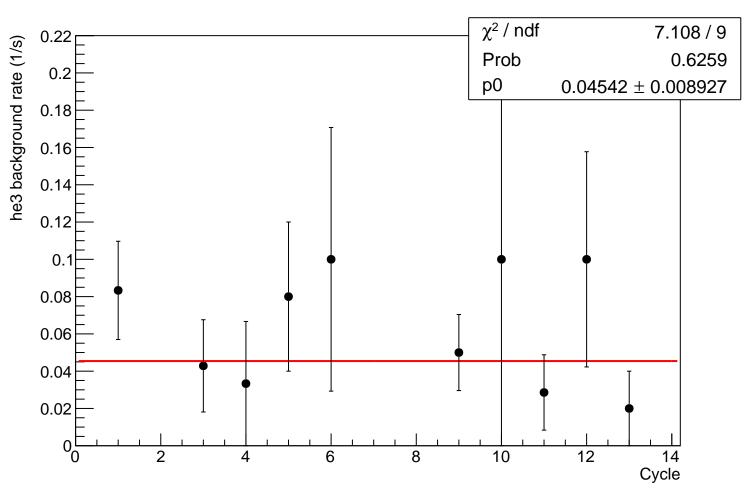


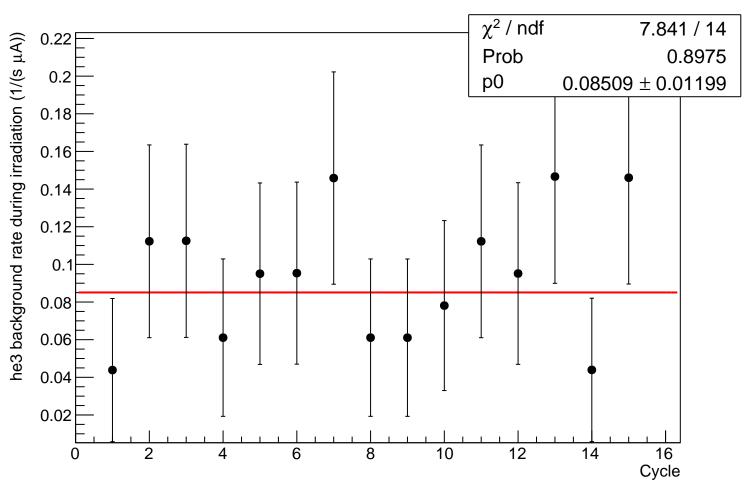
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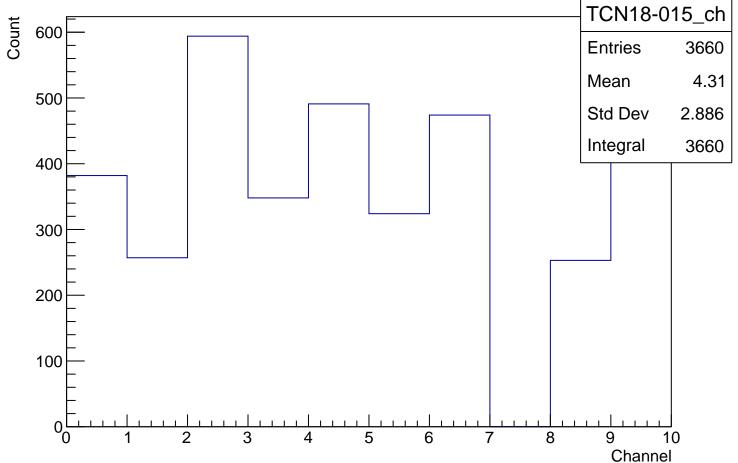


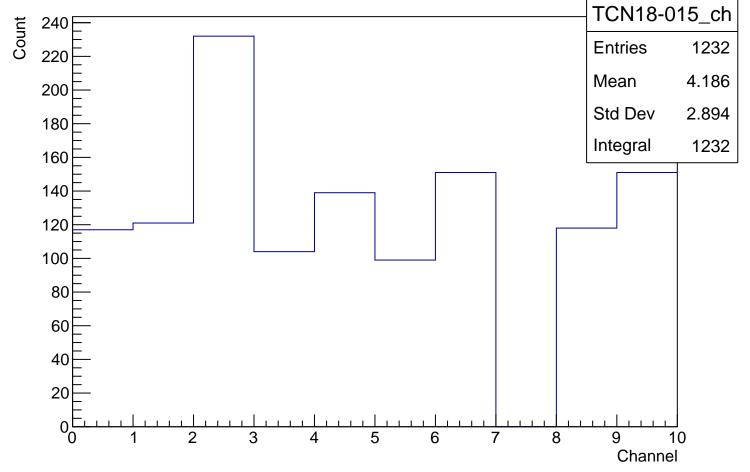




TCN18-015 TCN18-015\_ch Count 200 **Entries** 1032 Mean 4.073 180 Std Dev 2.854 160 Integral 1032 140 120 100 80 60 40 20 0,

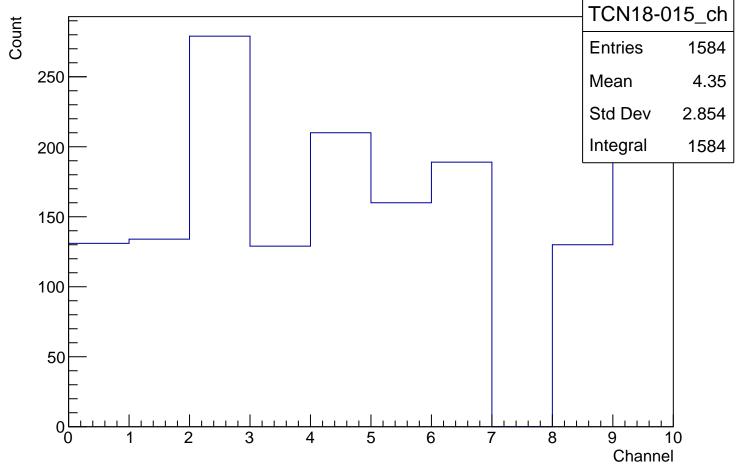
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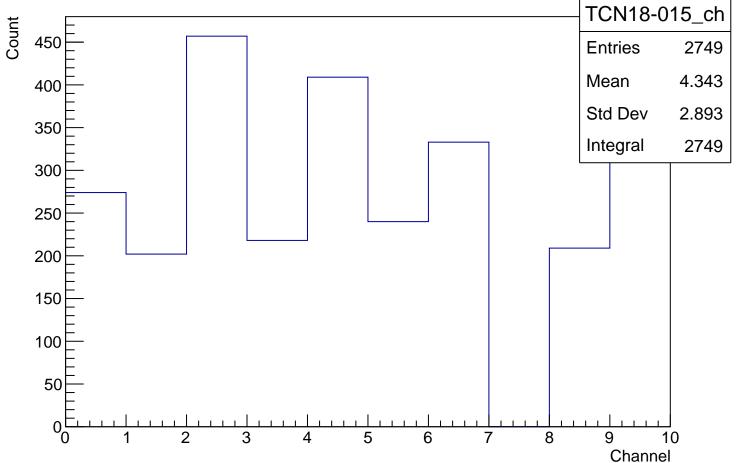


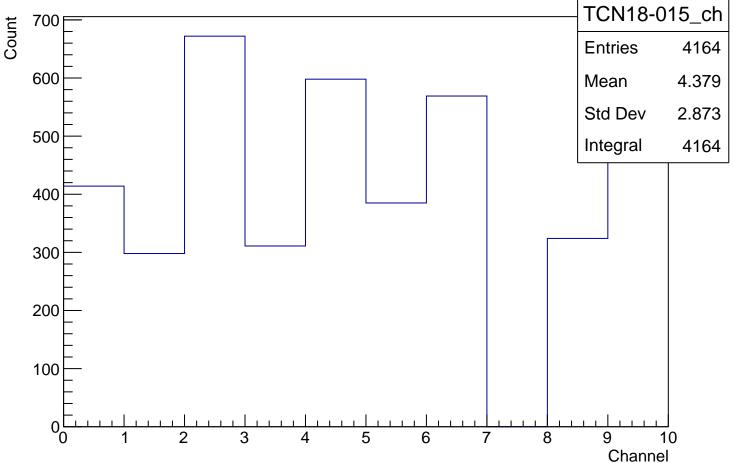


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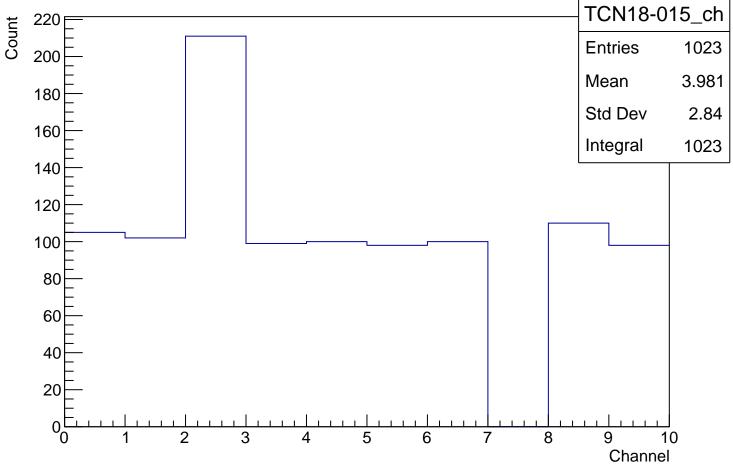
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TCN18-015 TCN18-015\_ch Count **Entries** 4774 700 Mean 4.44 600 Std Dev 2.868 Integral 4774 500 400 300 200 100 10



TCN18-015 TCN18-015\_ch Count **Entries** 3717 600 Mean 4.258 Std Dev 2.875 500 Integral 3717 400 300 200 100

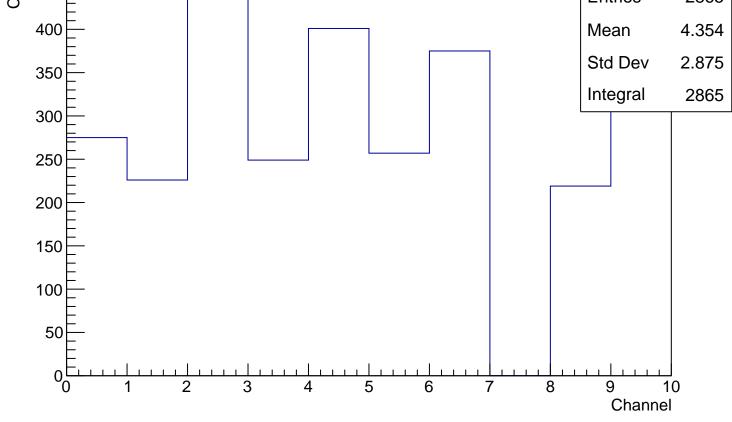
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TCN18-015 TCN18-015\_ch Count 220 **Entries** 1267 200 Mean 4.281 180 Std Dev 2.851 Integral 160 1267 140 120 100 80 60 40 20 0, 10

TCN18-015 TCN18-015\_ch Count **Entries** 2252 350 Mean 4.357 Std Dev 2.876 300 Integral 2252 250 200 150 100 50 10

TCN18-015 TCN18-015\_ch Count **Entries** 1570 250 Mean 4.255 Std Dev 2.931 200 Integral 1570 150 100 50 0 r 10 Channel

TCN18-015 TCN18-015\_ch Count 450 **Entries** 2865 400 Mean 4.354 Std Dev 2.875 350 Integral 2865 300 250 200 150



TCN18-015 TCN18-015\_ch 700 Count **Entries** 4138 600 Mean 4.427 Std Dev 2.893 500 Integral 4138 400 300

