



# Logic Operations on Binaries

## Intro to MIPS

**CS 64: Computer Organization and Design Logic**

**Lecture #3**

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***Why do CPU programmers celebrate  
Christmas and Halloween  
on the same day?***

**Because Oct-31 = Dec-25 !!!**

# Administrative Stuff

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- The class is full... waitlist is closed... no adds given... ☹️
- Assignment 1 is due on Wednesday
  - How was lab on Friday?
- Assignment 2 will be issued on Wednesday

# Any Questions From Last Lecture?

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# 5-Minute Pop Quiz!!!

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## YOU MUST SHOW YOUR WORK!!!

1. Calculate, give your answer in hexadecimal, AND identify carry out (C) and overflow (V) bit values:

**(0x3E + 0xFC)**

2. Convert from binary to decimal AND to hexadecimal. Use any technique(s) you like:

**1001001**

# Answers...

1. Calculate, give your answer in hexadecimal, AND identify carry out (C) and overflow (V) bit values: **(0x3E + 0xFC)**

$$\begin{array}{r} 0011 \ 1110 \\ + \ 1111 \ 1100 \\ = \ 1 \ 0011 \ 1010 \end{array}$$

There is a carry out, so C = 1  
There's no overflow (why?), so V = 0

2. Convert from binary to decimal AND hexadecimal. Use any technique you like: **1001001**

$$= 0100 \ 1001 = 0x49 \text{ (collect-the-bits method)}$$

$$= 64 + 8 + 1 = 73 \text{ (positional notation method)}$$

# Binary Logic Refresher

## NOT, AND, OR

X	NOT X $\overline{X}$
0	1
1	0

X	Y	X AND Y X && Y X.Y
0	0	0
0	1	0
1	0	0
1	1	1

X	Y	X OR Y X    Y X + Y
0	0	0
0	1	1
1	0	1
1	1	1

# Binary Logic Refresher

## Exclusive-OR (XOR)

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The output is “1” only if the inputs are opposite

X	Y	X XOR Y $X \oplus Y$
0	0	0
0	1	1
1	0	1
1	1	0



# Bitwise NOT

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- Similar to logical NOT (!), except it works on a bit-by-bit manner
- In C/C++, it's denoted by a tilde: ~

$$\sim(1001) = 0110$$

# Exercises

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- Remember: hexadecimal numbers are often written in the **0xhh** notation, so for example:

The hex 3B would be written as **0x3B**

- What is  $\sim(0x04)$ ?
  - Ans: 0xFB
- What is  $\sim(0xE7)$ ?
  - Ans: 0x18

# Bitwise AND

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- Similar to logical AND (&&), except it works on a bit-by-bit manner
- In C/C++, it's denoted by a single ampersand: &

$$\begin{array}{rcl} (1001 \ \& \ 0101) & = & 1 \ 0 \ 0 \ 1 \\ & & \& \ 0 \ 1 \ 0 \ 1 \\ & & \\ & = & 0 \ 0 \ 0 \ 1 \end{array}$$

# Exercises

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- What is  $(0xFF) \& (0x56)$ ?
  - Ans: 0x56
- What is  $(0x0F) \& (0x56)$ ?
  - Ans: 0x06
- What is  $(0x11) \& (0x56)$ ?
  - Ans: 0x10
- Note how  $\&$  can be used as a “masking” function
  - Masking?! What’s being “masked”???

# Bitwise OR

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- Similar to logical OR (`||`), except it works on a bit-by-bit manner
- In C/C++, it's denoted by a single pipe: `|`

$$\begin{array}{rcl} (1001 & | & 0101) \\ & & = \begin{array}{cccc} 1 & 0 & 0 & 1 \\ | & 0 & 1 & 0 & 1 \end{array} \\ & & = \begin{array}{cccc} 1 & 1 & 0 & 1 \end{array} \end{array}$$

# Exercises

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- What is  $(0xFF) \mid (0x92)$ ?
  - Ans:  $0xFF$
- What is  $(0xAA) \mid (0x55)$ ?
  - Ans:  $0xFF$
- What is  $(0xA5) \mid (0x92)$ ?
  - Ans:  $0xB7$

# Bitwise XOR

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- Works on a bit-by-bit manner
- In C/C++, it's denoted by a single carat: ^

$$\begin{array}{rcl} (1001 \text{ } ^\wedge \text{ } 0101) & = & 1 \ 0 \ 0 \ 1 \\ & & ^\wedge \quad 0 \ 1 \ 0 \ 1 \\ & & \\ & = & 1 \ 1 \ 0 \ 0 \end{array}$$

# Exercises

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- What is  $(0xA1) \wedge (0x13)$ ?
  - Ans: 0xB2
- What is  $(0xFF) \wedge (0x13)$ ?
  - Ans: 0xEC
- Note how  $(1 \wedge b)$  is always the inverse of  $b$  ( $\sim b$ )  
and how  $(0 \wedge b)$  is always just  $b$



## Bit Shift *Left*

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- Move all the bits N positions to the left
- What do you do the positions now empty?
  - You put in N number of 0s
- Example: Shift “1001” 2 positions to the left  
$$1001 \ll 2 = \mathbf{100100}$$
- Why is this useful as a form of multiplication?

# Multiplication by Bit Left Shifting

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- Veeeery useful in CPU (ALU) design
  - Why?
- Because you don't have to design a “multiplier” function
- You just have to design a way for the bits to shift (which is a relatively easier design)

# Bit Shift *Right*

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- Move all the bits N positions to the ***right***, subbing-in either N number of 0s or N 1s on the left
- Takes on two different forms
- Example: Shift “1001” 2 positions to the right  
 $1001 \gg 2 = \text{either } \mathbf{0010} \text{ or } \mathbf{1110}$
- The information carried in the last 2 bits is lost.
- If Shift Left does *multiplication*, what does Shift Right do?
  - It divides, **but** it truncates the result

# Two Forms of Shift Right

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- Subbing-in 0s makes sense
- What about subbing-in the leftmost bit with 1?
- It's called "***arithmetic***" shift right:  
$$1100 \text{ (arithmetic)} \gg 1 = 1110$$
- It's used for *twos-complement* purposes
  - *What?*

# YOUR TO-DOs

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- Finish your reading for this week's classes
  - Ch. 2.2, 2.3, 2.6, 3.2, 3.3
- Finish Assignment #1
  - You have to submit it as a **PDF** using ***Gradescope***
  - Due on **Wednesday, 10/9, by 11:59:59 PM**

**</LECTURE>**