Let's add print and definitions and calls to our compiler

```
fn snek_print(val: i64) -> i64 {
   if (val == 1) {
     println!("false")
   } else if (val == 3) {
     println!("true")
   } else {
     println!("{}", val>>1)
   }
   return val;
}
```

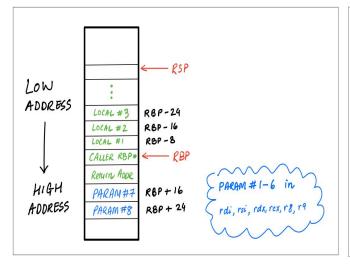
```
enum Expr {
    ...
    Print(Box<e>),
}
```

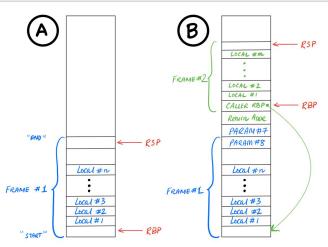
Example

Generated code

```
(let (x (print input))
  (+ x 1))
```

```
print
```





Let's add print and definitions and calls to our compiler

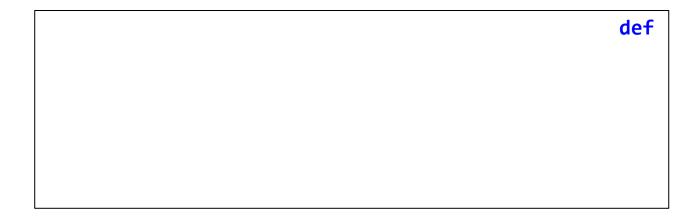
```
enum Defn {
   Fun1(String, String, Box<Expr>),
   Fun2(String, String, String, Box<Expr>),
}
enum Expr {
   ...
   Call1(String, Box<Expr>)
   Call2(String, Box<Expr>, Box<Expr>)
}
```

Example

Generated code

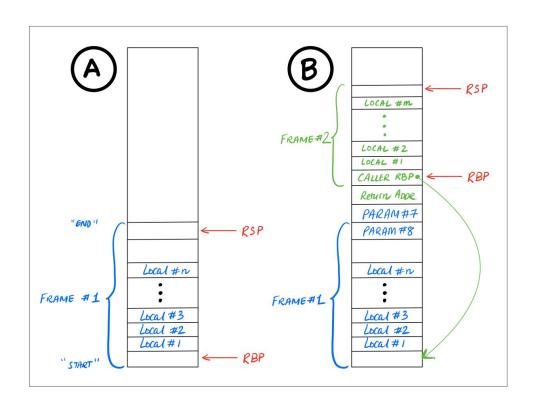
```
(defn (add x1 x2)
    (+ x1 x2))

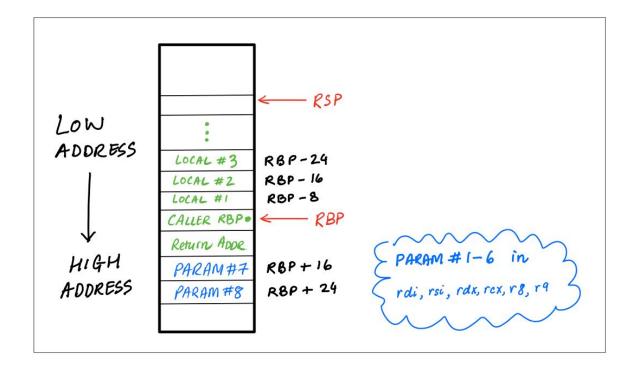
(add input 10)
```



```
call
```

```
fn compile_def_body(args: &[String], sp: usize, body: &Expr, count: &mut i32) -> String
  let fun_entry = compile_entry(body, sp);
  let body_code = compile_expr(body, &init_env(args), sp, count, "time_to_exit");
  let fun_exit = compile_exit();
  format!("{fun_entry}
         {body_code}
         {fun_exit}")
}
fn compile_entry(e: &Expr, sp: usize) -> String {
  let vars = expr_vars(e) + sp;
  format!("push rbp
           mov rbp, rsp
           sub rsp, 8*{vars}")
}
fn compile_exit() -> String {
  format!("mov rsp, rbp
           pop rbp
           ret")
}
```





Recursive Calls

Tail Calls

```
sumTo(5)
                                                        sum 5 0
==>5 + sumTo(4)
                                                        ==> sum 5 0
        \Lambda\Lambda\Lambda\Lambda\Lambda\Lambda\Lambda\Lambda
                                                        ==> sum 4 5
==> 5 + [4 + sumTo(3)]
                                                        ==> sum 3 9
            ^^^^^
                                                        ==> sum 2 12
==> 5 + [4 + [3 + sumTo(2)]]
                                                        ==> sum 1 14
==> 5 + [4 + [3 + [2 + sumTo(1)]]]
                                                        ==> sum 0 15
                                                        ==> 15
==> 5 + [4 + [3 + [2 + [1 + sumTo(0)]]]]
==> 5 + [4 + [3 + [2 + [1 + 0]]]]
                                                 (defn (sum n acc)
==> 5 + [4 + [3 + [2 + 1]]]
                                                   (if (= n 0)
==> 5 + [4 + [3 + 3]]
                                                       acc
                                                       (sum (+ n -1) (+ acc n))))
==> 5 + [4 + 6]
         \wedge \wedge \wedge \wedge \wedge
                                                 (sum input 0)
==> 5 + 10
   ^^^^
==> 15
                                          (defn (fac n acc)
e ::= n
                                            (if (= n 0))
       true
       false
                                               acc
                                               (if (= n 2)
       input
                                                  (* 2 (fac (+ n -1) (* acc n)))
                                                  (fac (+ n -1) (* acc n))
     (add1 e)
      (let (x e1) e2)
                                               )
                                            )
      (+ e1 e2)
                                          )
      (= e1 e2)
      (if e1 e2 e3)
      (set x e)
      (block e1...en)
      (loop e)
      (break e)
      (print e)
      (call1 e)
       (call2 e1 e2)
```

This is 64 bits:

This is 5:

This is 5 shifted 1 to the left, AKA 10:

If we're OK with 63-bit numbers, can use LSB for tag

What does this mean for code generation?

What should we do the next time we need a new type? (string, heap-allocated object, etc.)

Condition Codes (that matter for us): Overflow, Sign, Zero

many instructions set these; arithmetic, shifting, etc. mov does not

compute <reg> - <val> and set condition codes (value in <reg> does not change) cmp <reg>, <val> some cases to think about:

> $< reg > = -2^64$. < val > = 1Overflow: ____ Sign: ____ Zero: ____

<reg> = 0, <val> = 10verflow: ____ Sign: ___ Zero: ___

Sign: Zero: <reg> = 1, <val> = 00verflow:

Sign: ___ Zero: < reg > = -1, < val > = -2Overflow:

test <reg>, <val> perform bitwise and on the two values, but don't change <reg>, and set condition

codes as appropriate. Useful for mask checking, test rax, 1 will set Z to true

if and only if the LSB is 1

<label>: set this line as a label for jumping to later

imp <label> unconditionally jump to <label>

ine <label> jump to <label> if Zero is not set (last cmped values not equal)

ie <label> jump to <label> if Zero is set (last cmped values are equal)

jump to <label> if Overflow is the same as Sign (which corresponds to >= for last cmp) ige <label> jle <label> jump to <label> if Zero set or Overflow != Sign (which corresponds to <= for last cmp)

shl <reg> shift <reg> to the left by 1, filling in least-significant bit with zero

sar <reg> shift <reg> to the right by 1, filling in most-significant bit to preserve sign

shr <reg> shift <reg> to the right by 1, filling in most-significant bit with zero