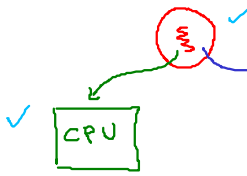
11/04/2025 - Sistemas Operativos - Ude@

1. Repaso modulo anterior

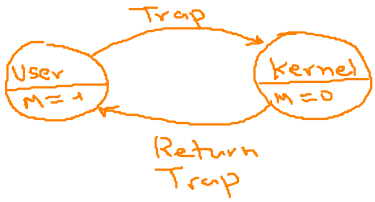
Proceso (Abstracción) ✓



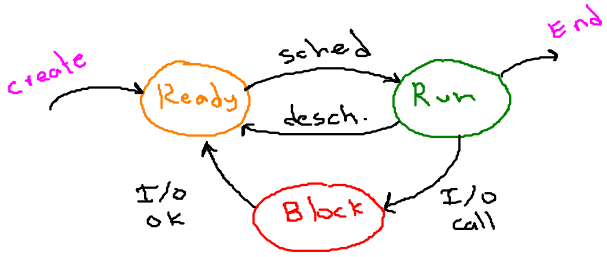
Estructuras de datos:
- Process Table (PT) ✓
- Process Control block (PCB) ✓

LDE (Mecanismo)

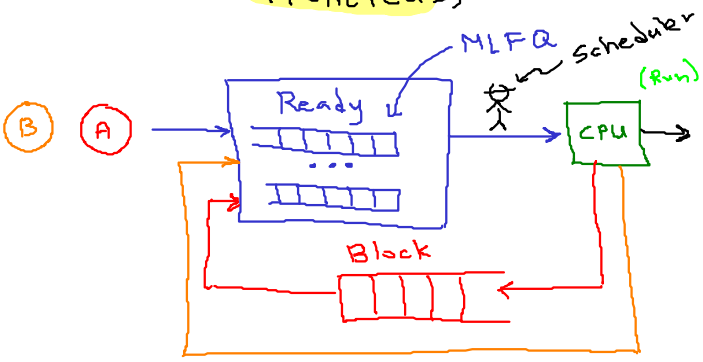
Limited Direct Execution



Modelo de 3 estados



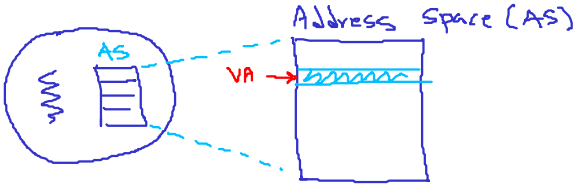
(Políticas)



2. Virtualizacion de Memoria.

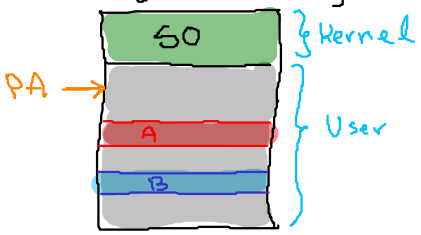
Conceptos:

- Memoria virtual = Address Space (AS)
  - → Direcciones virtuales (VA = Virtual Address)

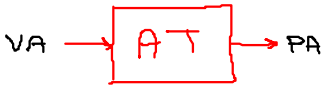


- Memoria Física = Main Memory = RAM = (PM) Physical Memory
  - → Direcciones Físicas (PA = Physical Address)

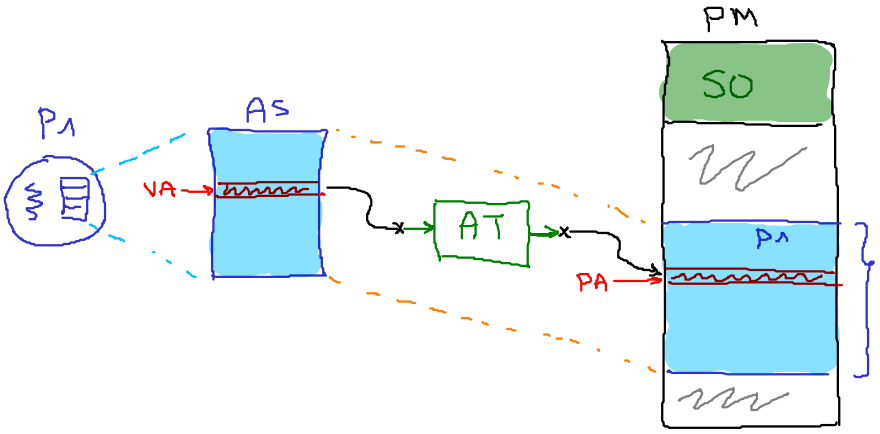
Physical Memory



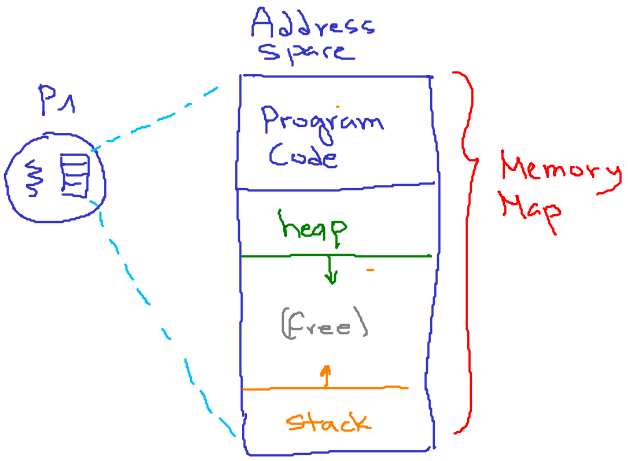
- Adress Translation. (AT)



PA = AT (VA)



- Mapa de memoria (Memory Map)



Program Code	• Instrucciones • Variables globales
Heap	• Memoria **asignada dinámicamente** ◦ `malloc` en C ◦ `new` en Java o C++
Stack	• Memoria **automática** • Direcciones y valores de retorno • Variables locales y argumentos pasados a rutinas