

Design Rules Verification Report

Filename : C:\ALTIUM_PROYECTOS\Proyecto_Integrado\PCB\A4.PCBDOC

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Width Constraint (Min=0.203mm) (Max=25.4mm) (Preferred=0.203mm) (All)	0
Power Plane Connect Rule(Direct Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.3mm) (Conductor Width=0.203mm) (Air Gap=0.203mm)	0
Minimum Annular Ring (Minimum=0.076mm) (All)	0
Hole Size Constraint (Min=0.3mm) (Max=6.3mm) (All)	0
Total	0