

Attack on Prefetcher

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Abstract—An implementation of a reverse-engineering attack is described to extract parameters of the caches present in X86 cores. A denial-of-service attack on the prefetcher is described, followed by its implementation and test results. A hypothetical side-channel using the shared reorder buffer on SMT cores is presented.



1 INTRODUCTION

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John Doe Biography text here.

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2 CONCLUSION

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APPENDIX A

PROOF OF THE FIRST ZONKLAR EQUATION

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APPENDIX B

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Jane Doe Biography text here.

ACKNOWLEDGMENTS

The authors would like to thank...

REFERENCES

- [1] H. Kopka and P. W. Daly, *A Guide to L^AT_EX*, 3rd ed. Harlow, England: Addison-Wesley, 1999.

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