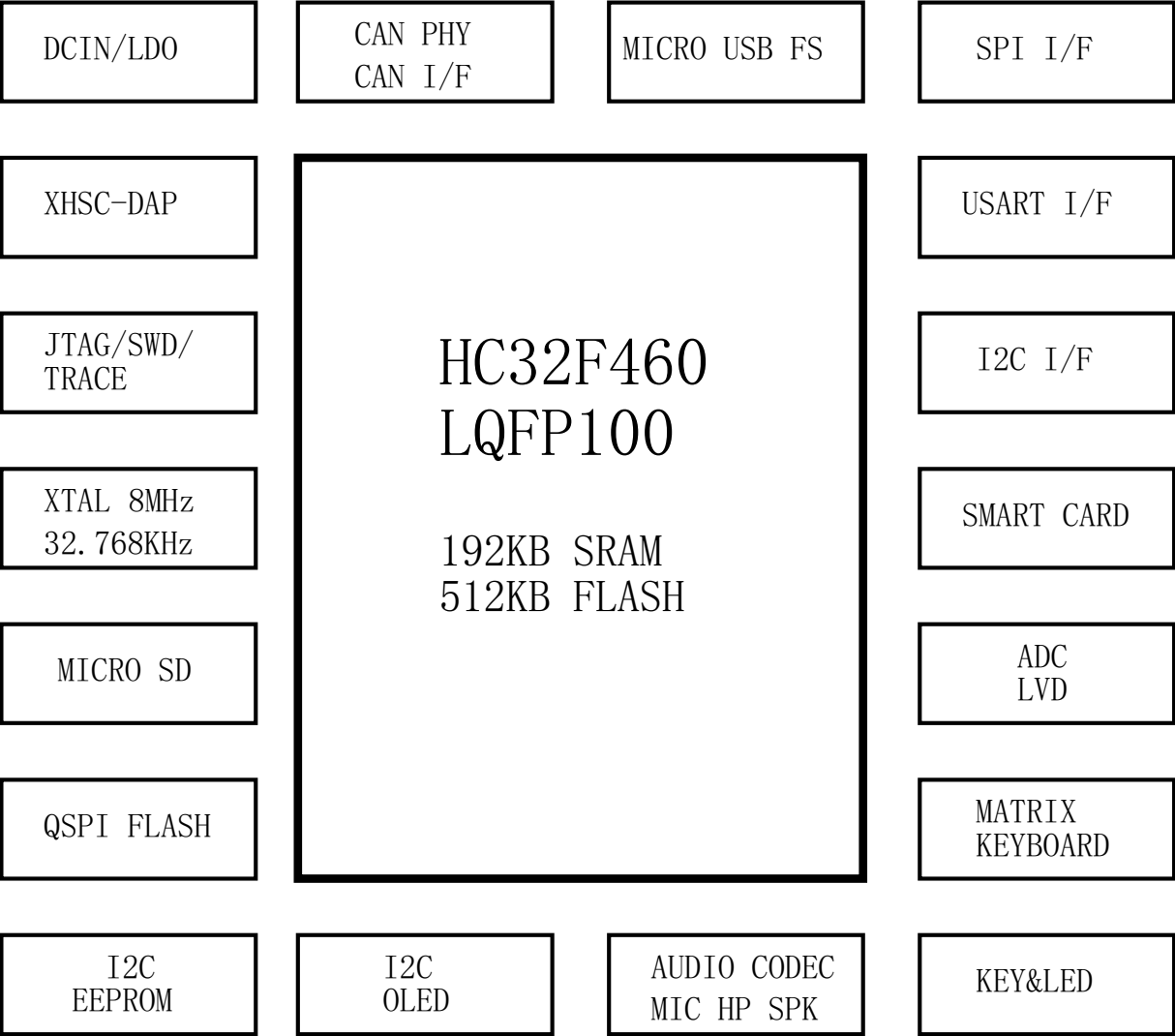


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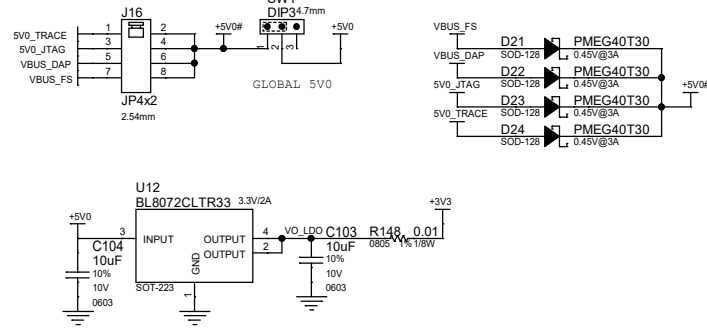
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Page 02	POWER,DEBUG
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Page 06	USB FS,CAN
Page 07	ADC,KEYS,LEDS
Page 08	USART,SPI,I2C,SMART CARD

Revision History

Rev	Date	Description
1.0	08/21/2018	- New design.
1.1	12/12/2018	- Changed UART connector to match SmartCard. - Added R143~R147 to I2S signals. - Added R143 as pull-up resistor to SPI NSS. - Changed matrix keyboard output pin,usart2_ck pin,and LED1 control pin. - Changed CAN and I2C IO pin.
1.2	01/11/2019	- Update HC32F460 pin definition. - C28 is now DNP.
1.3	09/23/2019	- Replaced C72 by R148. - Added C94,C95,C96 to VCCA/VREF. - Removed R25,R26.
2.0	02/23/2022	- DAP module added. - Pin assignment modified. - Analog decoupling capacitor modified.
2.1	09/27/2023	- Update HC32F460 pin definition.



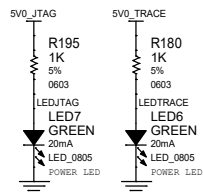
POWER



PIN	FUNC
PA13	JTMS_SWDIO
PA14	JTCK_SWCLK
PA15	JTDI
PB3	JTDO_SWO
PB4	NJTRST
PE3	TRACED0
PE4	TRACED1
PE5	TRACED2
PE6	TRACED3

3 NJTRST
3 JTDI
3 JTMS_SWDIO
3 JTCK_SWCLK

3 JTDO_SWO
3 NRST



3 TRACELCK
3 TRACED0
3 TRACED1
3 TRACED2
3 TRACED3

3 TRACED0
3 TRACED1
3 TRACED2
3 TRACED3

3 TRACED0
3 TRACED1
3 TRACED2
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3 TRACED0
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3 TRACED0
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3 TRACED3

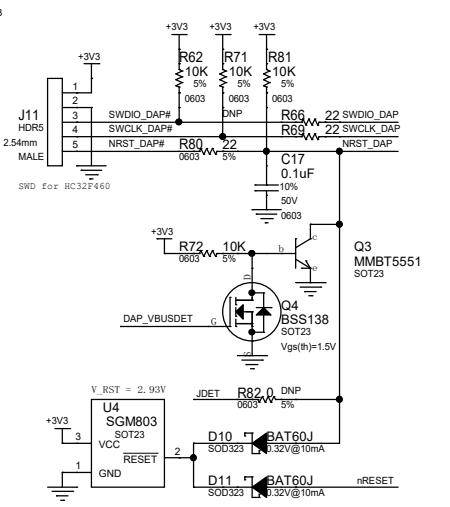
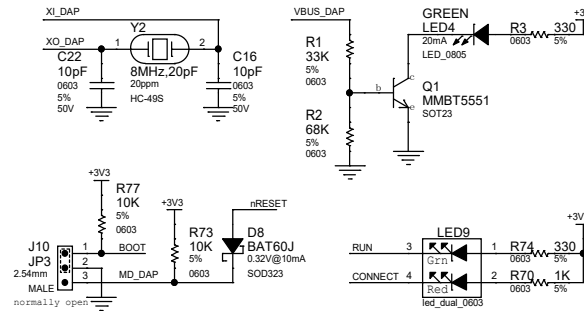
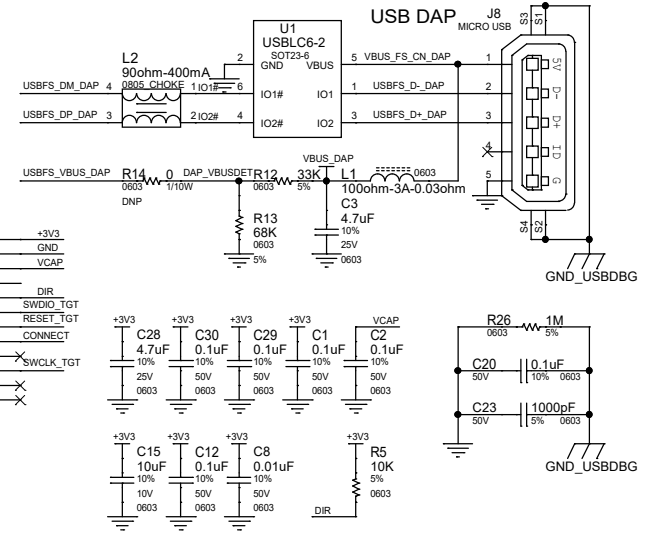
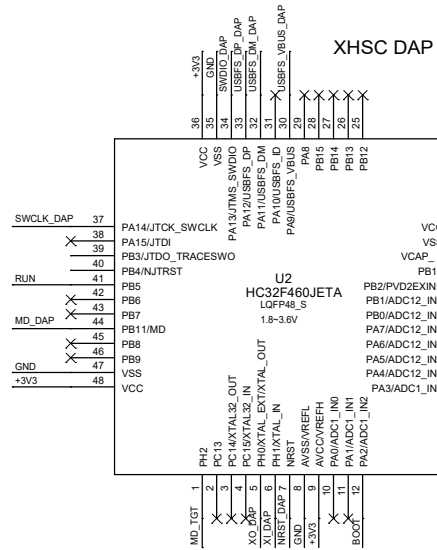
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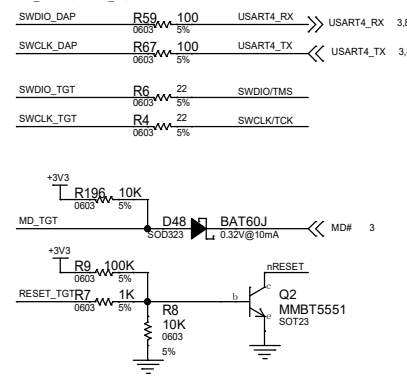
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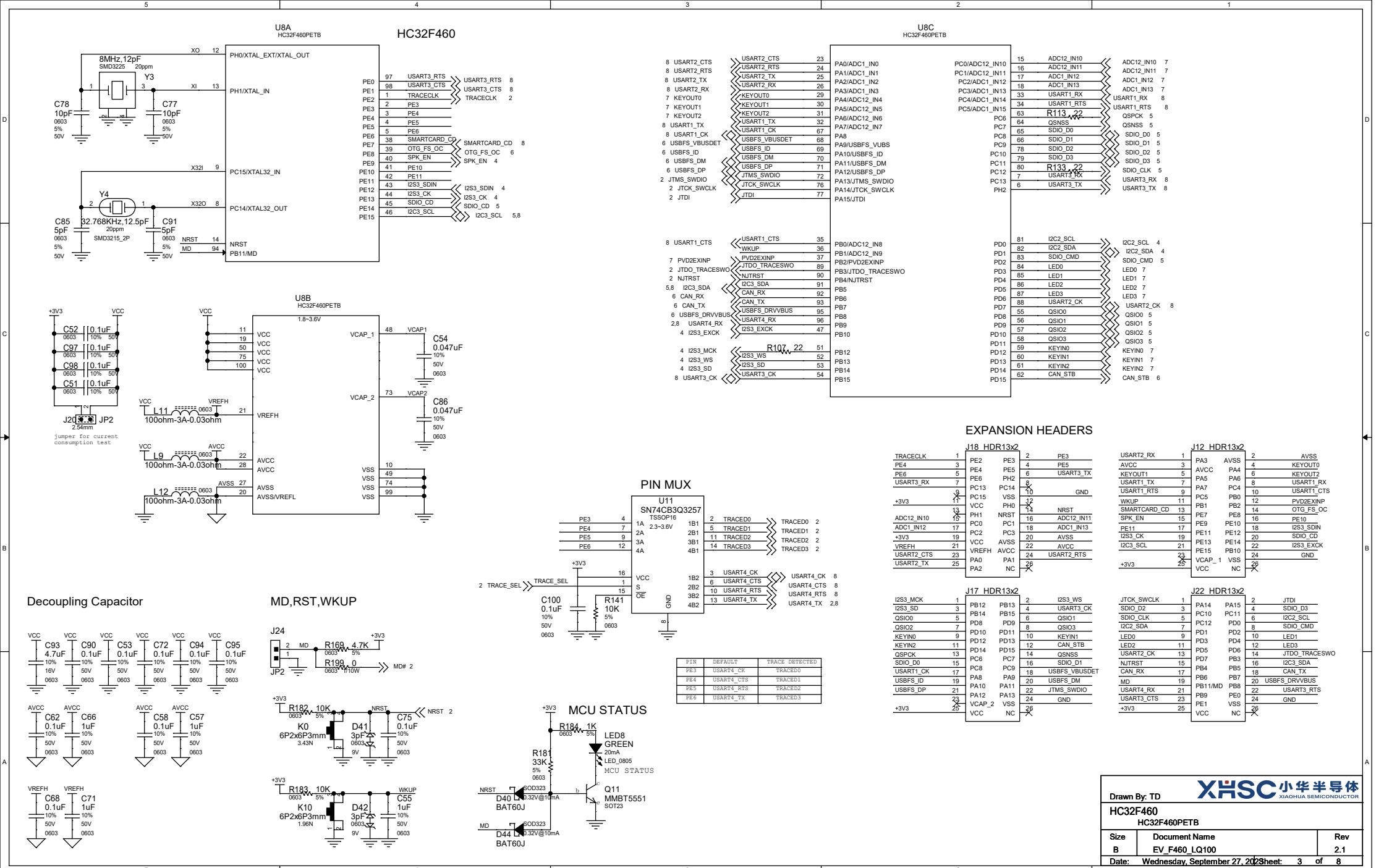
XHSC DAP

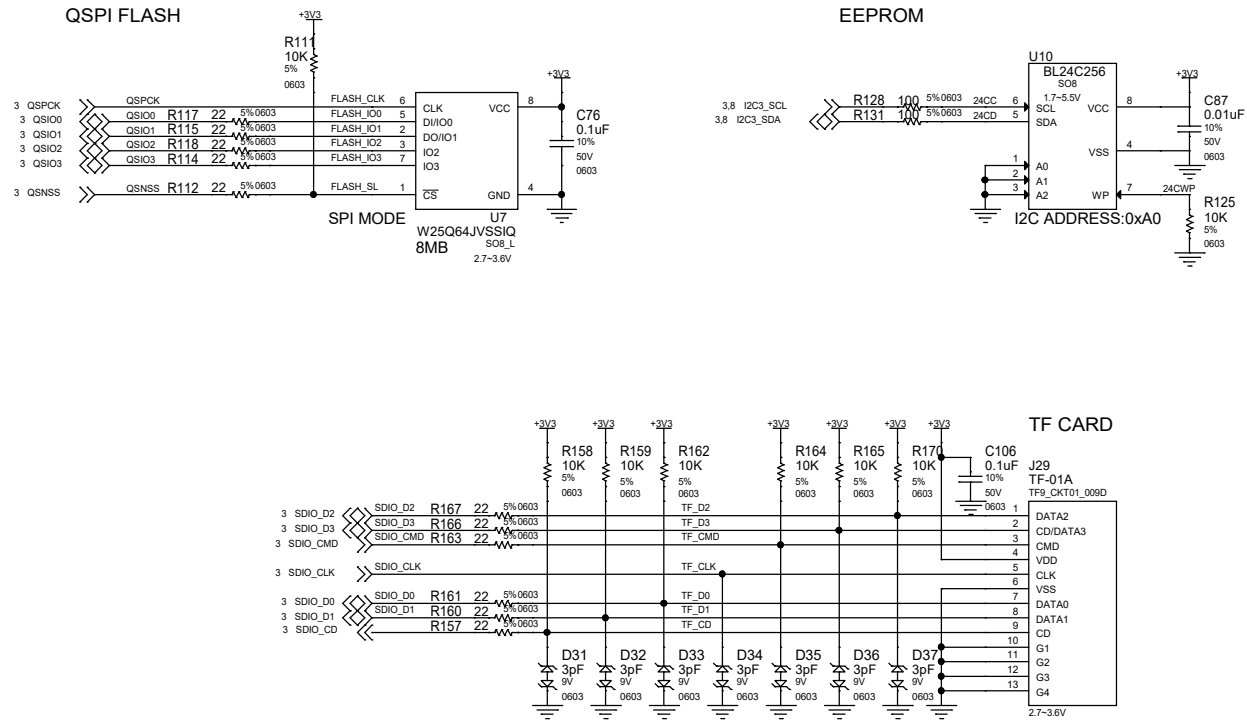


DAP TARGET CONNECTION



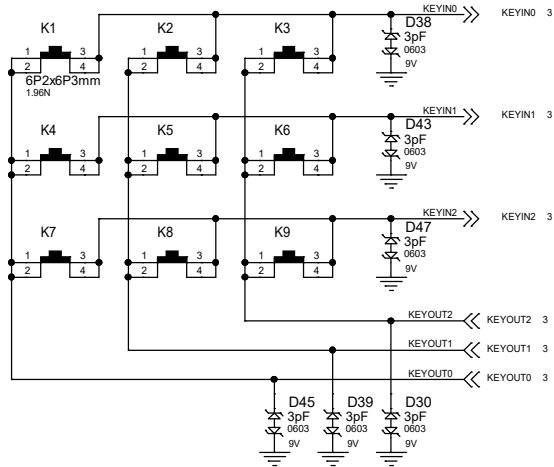
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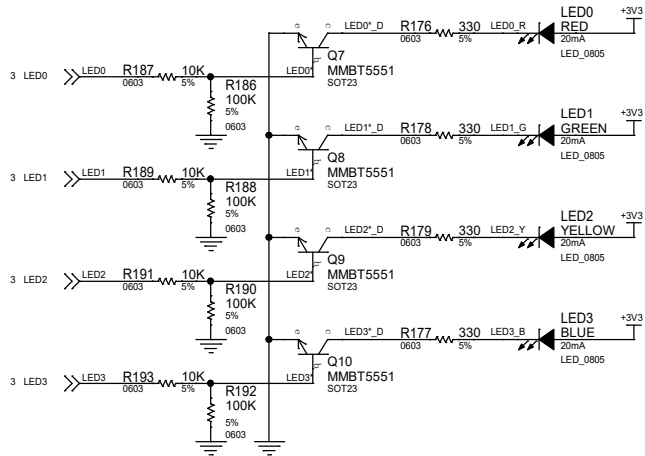


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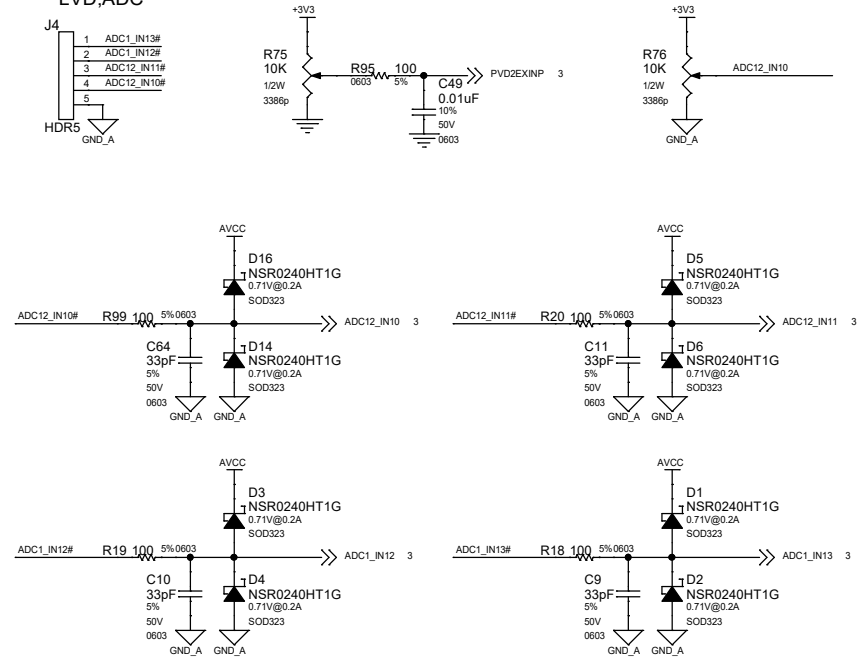
MATRIX KEYBOARD



LEDS



LVD,ADC



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HC32F460		
ADC,KEYS,LEDS		
Size	Document Name	Rev
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Date: Wednesday, September 27, 2023		
Sheet: 7 of 8		

XHSC 小华半导体
XIAOHUA SEMICONDUCTOR

