Group 18

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Traceability Report

Elevator

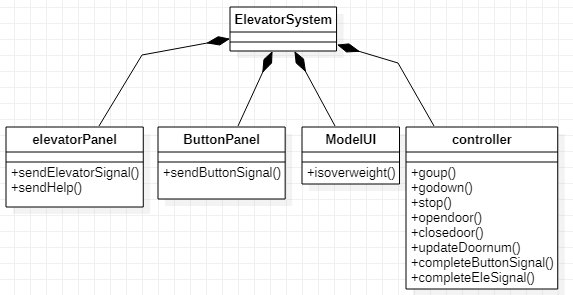
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## System Architecture

The system architecture is shown below:



## Software Traceabili

|  |  |  |
| --- | --- | --- |
|  | Is implemented by | Is validated by |
| R1.1 | S1.3.1 | T1.1.1 |
| R1.1.1 | S1.3.1 | T1.1.1 |
| R1.1.2 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.2 | S1.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.2.1 | S1.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.2.2 | S1.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.2.3 | S1.1.1 | T1.1.1 |
| R1.3 | S3.2.1 | T1.1.1 |
| R1.4 | S3.2.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.5 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.5.1 | S1.3.1 | T1.1.1& T2.1&T2.2 |
| R1.5.2 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.6.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.6.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.7 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.7.1 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.7.2 | S1.3.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.8 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R1.8.1 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R2.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.1.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.1.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.2.1 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R2.2.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.3.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.3.2 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R2.3.3 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R.4 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.4.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.4.2 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R2.5 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R2.5.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.1.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.1.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.2 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R3.3 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.3.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.3.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.4 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R3.4.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.5 | S3.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.5.1 | S3.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R3.5.2 | S3.1.1 | T1.1.1 |
| R3.6 | S3.1.1 | T1.1.1 |
| R3.7 | S3.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.1.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.1.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.1.3 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R4.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.2.1 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.2.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.3 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.3.1 | S2.1.1 | T1.1.1& T2.1&T2.2 |
| R4.3.2 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |
| R4.3.3 | S2.1.1 | T1.1.1& T2.1&T2.2&T2.3 |