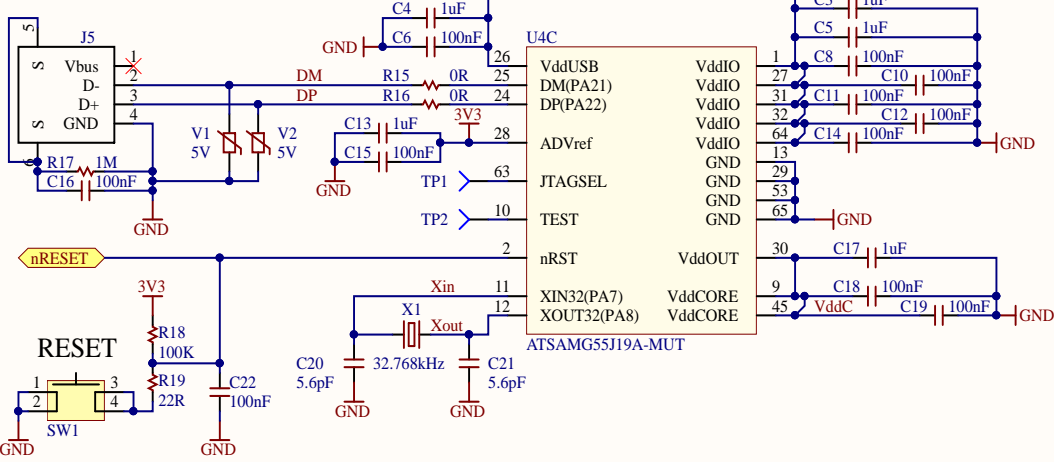
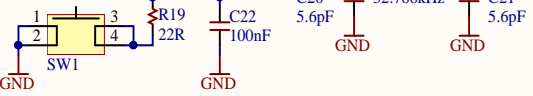


USB COMMS



RESET



U4A

BED-HEAT	6	PA0/I2SCK0/TIOA0/WKUP0
FAN-0	7	PA1/I2SW0/TIOB0/WKUP1
X_MIN	61	PA2/TCLK0/I2SDI0/WKUP2
TX3	5	PA3/TXD3/SPI3_MISO/TWD3/I2SDO0/WKUP9
RX3	8	PA4/RXD3/SPI3_MISO/TWCK3/I2SMCK0/WKUP10
RX2	17	PA5/RXD2/SPI2_MISO/TWCK2/SPI5_nCS1/RTS5/WKUP4
TX2	49	PA6/TXD2/SPI2_MISO/TWD2/PCK0
PH2	48	PA9/RXD0/SPI0_MISO/TWCK0/PDMIC_DAT/WKUP6
PG4	44	PA10/TXD0/SPI0_MISO/TWD0/PDMIC_CLK
SD_SS	43	PA11/SPI5_nCS0/CTS5
MISO	42	PA12/RXD5/SPI5_MISO/TWCK5
MOSI	41	PA13/TXD5/SPI5_MISO/TWD5
SCK	22	PA14/SCK5/SPI5_SPCK/WKUP8
Y-STEP	18	PA15/SPI2_nCS1/RTS2/SCK2/SPI2_SPCK
FAN-1	33	PA16/SPI2_nCS0/CTS2/TIOB1/WKUP7
THERM0	34	PA17/I2SDO0/PCK1/AD0
THERM1	35	PA18/I2SMCK0/PCK2/AD1
Z-STEP	36	PA19/TCLK1/I2SCK1/AD2
Z_MIN	23	PA20/TCLK2/I2SW1/AD3
HEAT-0	58	PA23/I2SDO1/TIOA1/WKUP3
PH7	56	PA24/I2SMCK1/SCK2/SPI2_SPCK/WKUP11
PD5	52	PA25/SPI0_nCS0/CTS0/I2SDO1
PF7	51	PA26/SPI0_nCS1/RTS0/I2SMCK1
E0-STEP	21	PA27/SCK1/SPI1_SPCK/RXD7/SPI7_MISO/TWCK7
E0-DIR	20	PA28/SPI1_nCS0/CTS1/TXD7/SPI7_MISO/TWD7
Z-DIR	19	PA29/SPI1_nCS1/RTS1/SCK7/SPI7_SPCK
Y-DIR	16	PA30/PCK1/SPI7_nCS0/CTS7
X-DIR	16	PA31/PCK2/SPI7_nCS1/RTS7

ATSAMG55J19A-MUT

U4B

PH6	37	PB0/SCK0/SPI0_SPCK/TXD6/SPI6_MISO/TWD6/AD4
PJ2	38	PB1/SCK4/SPI4_SPCK/RXD6/SPI6_MISO/TWCK6/AD5
TMC_DIAG	40	PB2/RXD1/SPI1_MISO/TWCK1/SPI5_nCS1/RTS5/AD6/WKUP12
PF5	59	PB3/TXD1/SPI1_MISO/TWD1/PCK2/AD7/WKUP13
Y_MIN	60	PB8/TXD4/SPI4_MISO/TWD4/SPI4_nCS0/CTS4/WKUP14/XOUT
EN_DRV	46	PB9/RXD4/SPI4_MISO/TWCK4/SPI4_nCS1/RTS4/WKUP15/XIN
Z-CS_DRV	47	PB10/TXD4/SPI4_MISO/TWCK4/SPI4_nCS1/RTS4/WKUP15/XIN
E0-CS_DRV	57	PB11/RXD4/SPI4_MISO/TWCK4/RXD6/SPI6_MISO/TWCK6
X-CS_DRV	15	PB13/SCK3/SPI3_SPCK/SCK6/SPI6_SPCK
Y-CS_DRV	14	PB14/SPI3_nCS0/CTS3/SPI6_nCS0/CTS6
		PB15/SPI3_nCS1/RTS3/SPI6_nCS1/RTS6

ATSAMG55J19A-MUT

TP3

PH6

PJ2

TMC_DIAG

PF5

Y_MIN

EN_DRV

Z-CS_DRV

E0-CS_DRV

X-CS_DRV

Y-CS_DRV

PK0

SDA

PJ5

SCL

not I2C

not I2C

nRESET

GND Testpoints

Mounting Holes

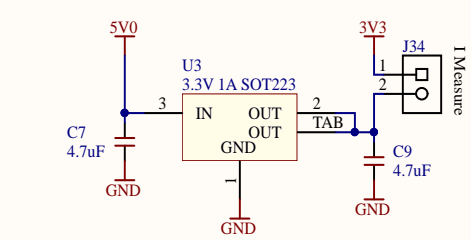
Fiducials

FD1

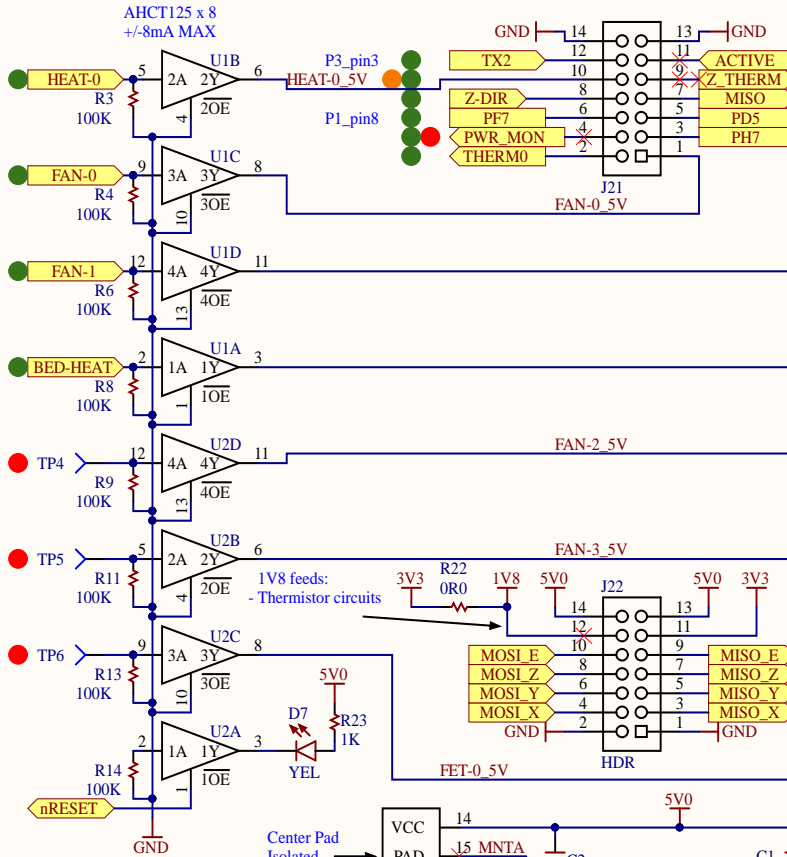
FD2

FD3

Note:
5V SRC is the Einsy



- No local drive source
- 5V logic driver required
- OK



Center Pad Isolated

VCC

PAD

GND

U1E

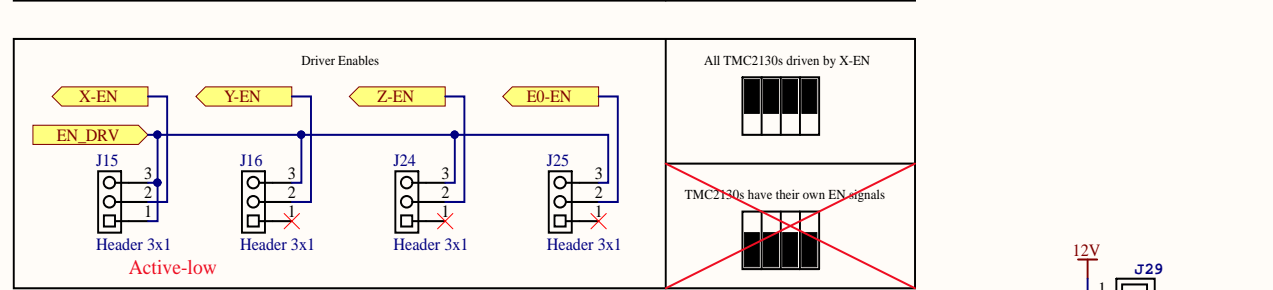
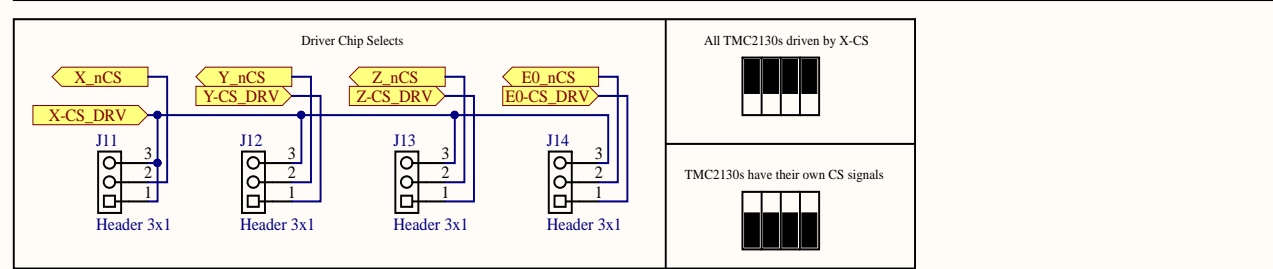
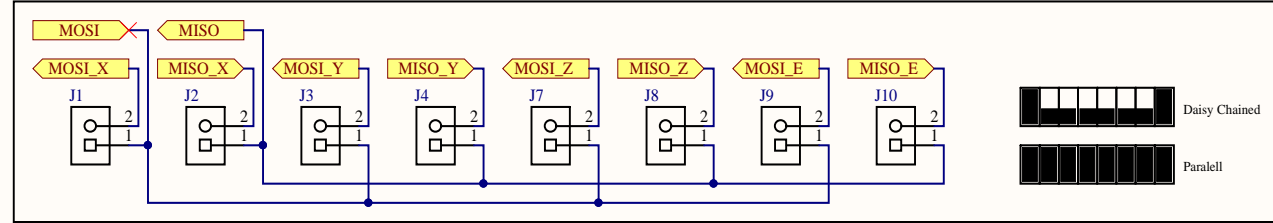
Center Pad Isolated

VCC

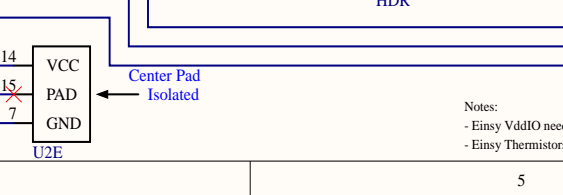
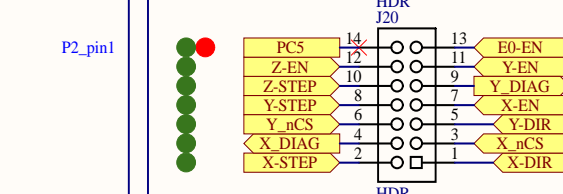
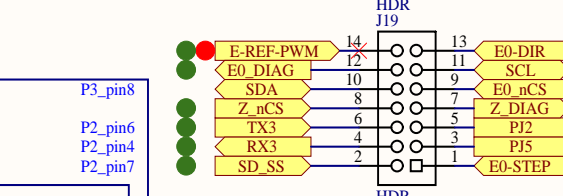
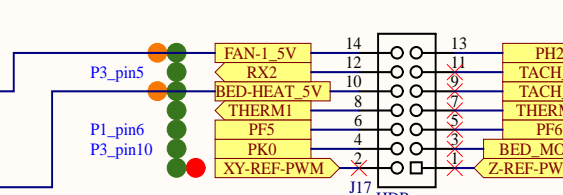
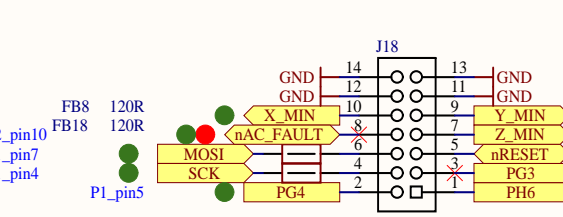
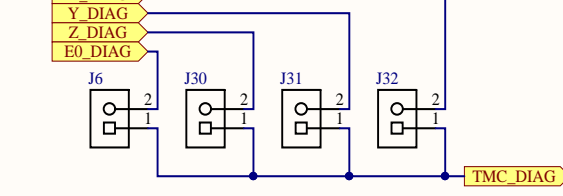
PAD

GND

U2E



Note: Remove all but one 10k pullup on the Einsy Breakout before bussing the DIAG signals



Notes:
- Einsy VddIO needs to be modified for 3v3 source.
- Einsy Thermistors need to be modified for 1v8 operation.

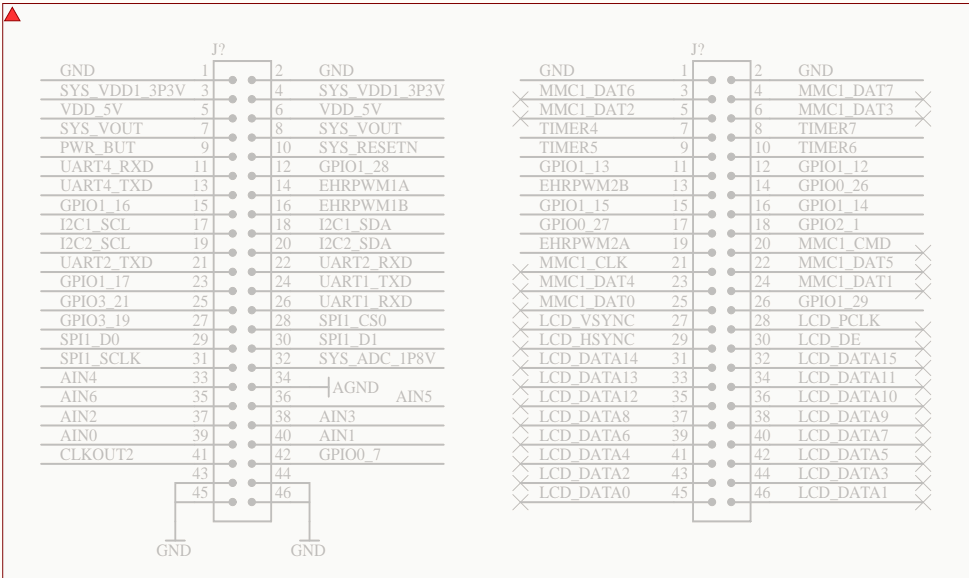
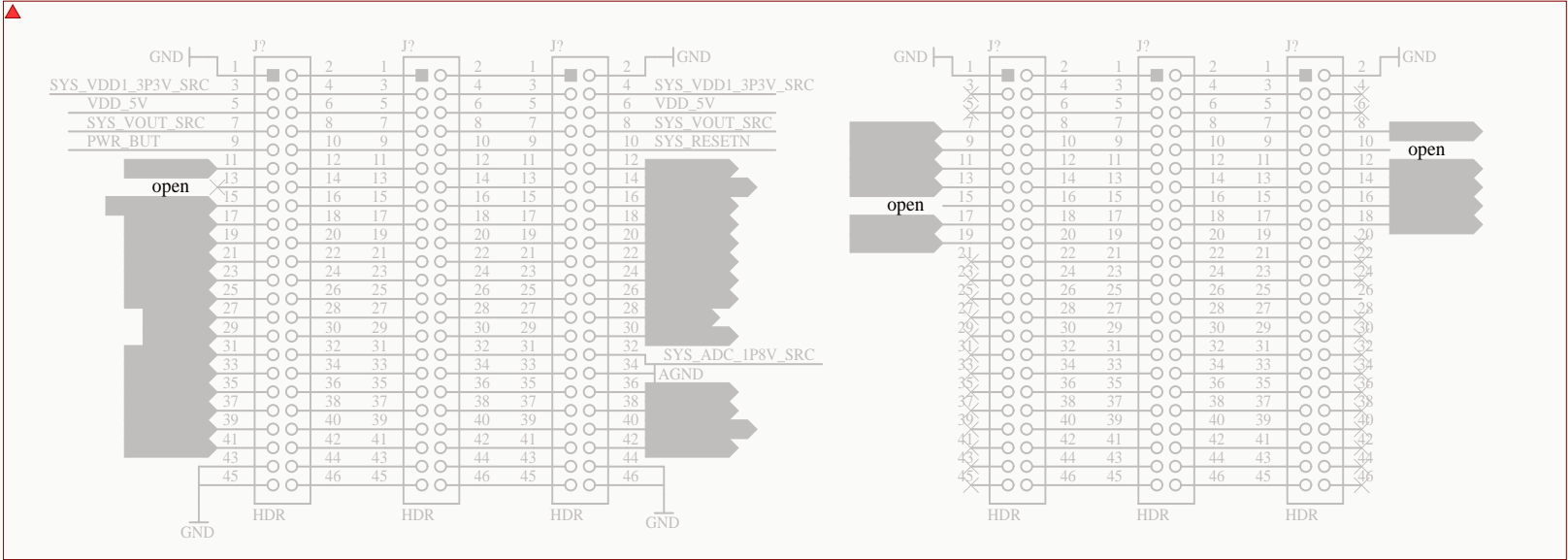
Project: Octavo Jumper Bridge

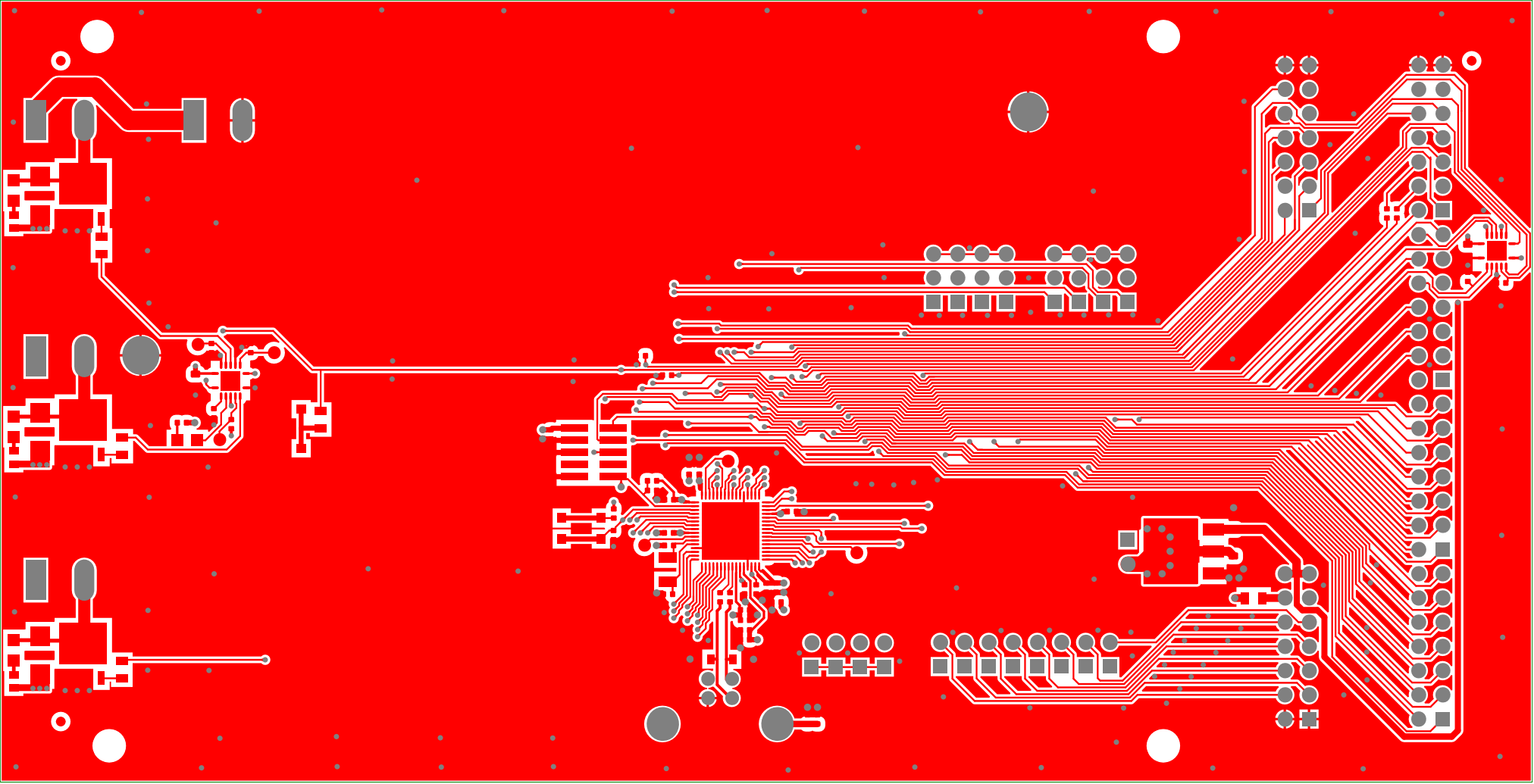
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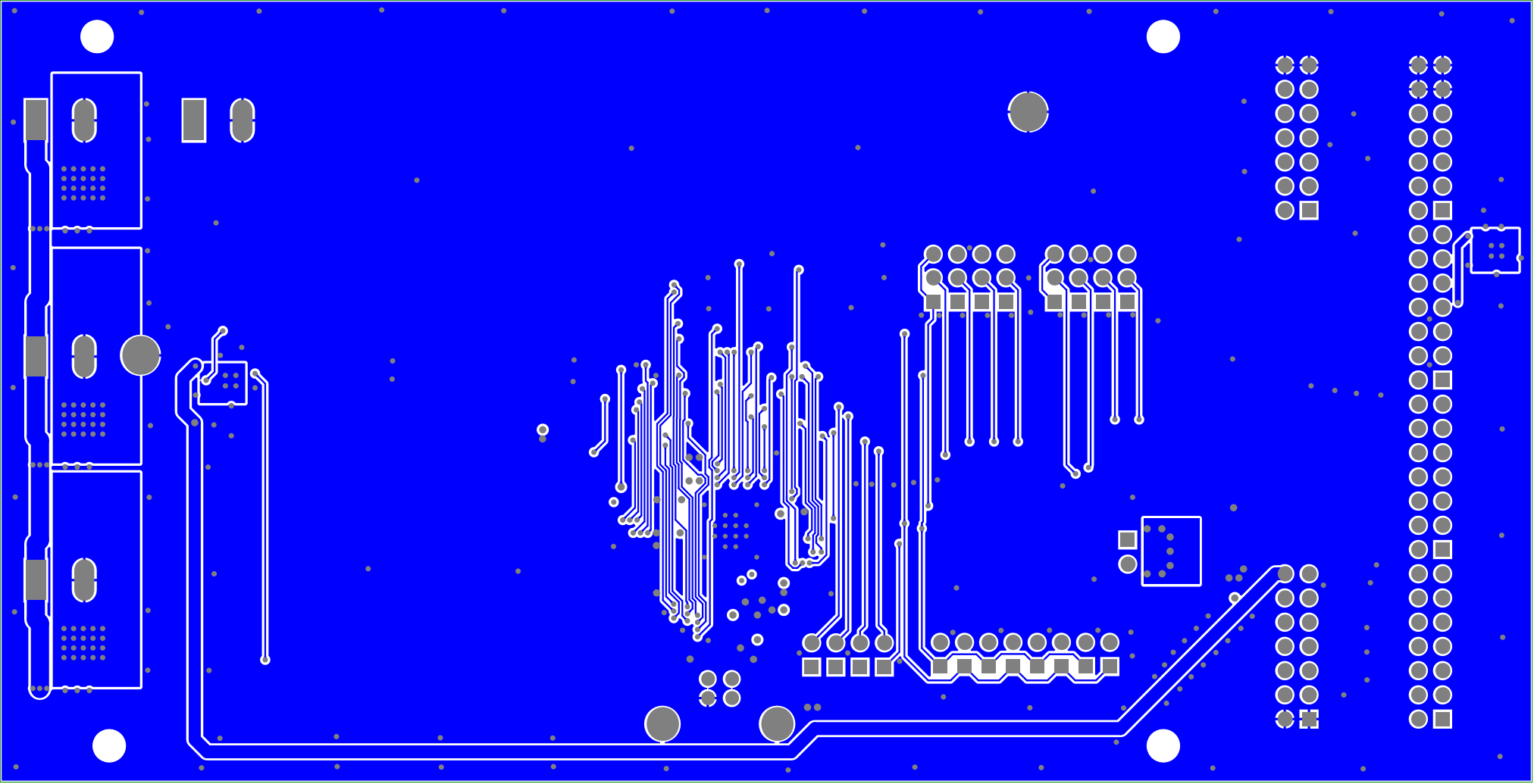
Drawn by: AWS

Sheet: 1 of 1









FAN-2

J26 +

J29 HU IN

EINSY TO G55
BRIDGE BOARD r1.0aTH5
GND

FAN-3

J27 +

GND

nRESET

FET-0

J28 +

Erase

J29

J39