TDTS08: Lab Report

Lab 3: Superscalar Processors

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1 Introduction

The purpose of this lab is to learn how Supersclar Processors work, and to try and modify an processor architecture to make it simpler, but it should still perform within 5% of the inital designs performance.

2 Method

We started out by investigating every part of the design individually, to see how they affected the performance of the design.

We then choose to simplify the parts that didn't affect the performance. We also looked at what parts we couldn't simplify due to that the performance would go further than 5% from the initial performance and thus couldn't be changed.

Now we looked at the parts of the design that we could modify, and at their traces.

3 Result

insert graphs of how the parameters changed
In figure **??** we can see that by changing the floating point, floating point multiplexer and the integer multiplexer, the system didn't perform any worse. **explain why go.ss doesn't need those**