Release Notes - Final Version 3.0

This kit contains preliminary HSPICE models for Cyclone 10 LP regular I/Os for three corner cases (TT/FF/SS). Single ended and differential (LVDS) I/O buffers HSPICE models are provided.

What is in this final release, Cyclone 10 LP HSPICE Kit Version 3.0?

1. Top level sample spice files for single ended and differential I/O simulations including IV curve simulations and Transient simulations.
2. Encrypted output buffer circuit models for single ended and differential I/Os
3. Encrypted transistor model libraries.
4. Unencrypted libraries of various settings of Cyclone 10 LP I/O: drive strength selection library, and load library.
5. Package model.
6. Cyclone 10 LP HSPICE model user guide.

Note: C10 LP buffer model, CIV buffer model and CIII buffer model are essentially the same. No buffer correlation is done for C10 LP HSPICE buffer model. For buffer correlation, please refer to CIII buffer model correlation report.