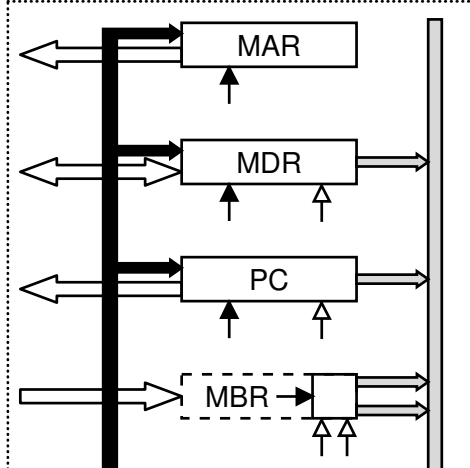
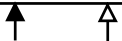


To
and
from
main
memory

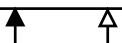


Memory
control
registers

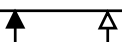
SP



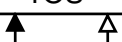
LV



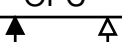
CPP



TOS



OPC



C bus

H



Control signals

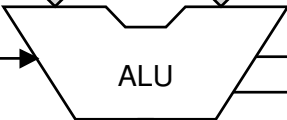
↑ Enable onto B bus

↑ Write C bus to register

B bus

ALU control

6



ALU

N

Z

Shifter

2

Shifter control