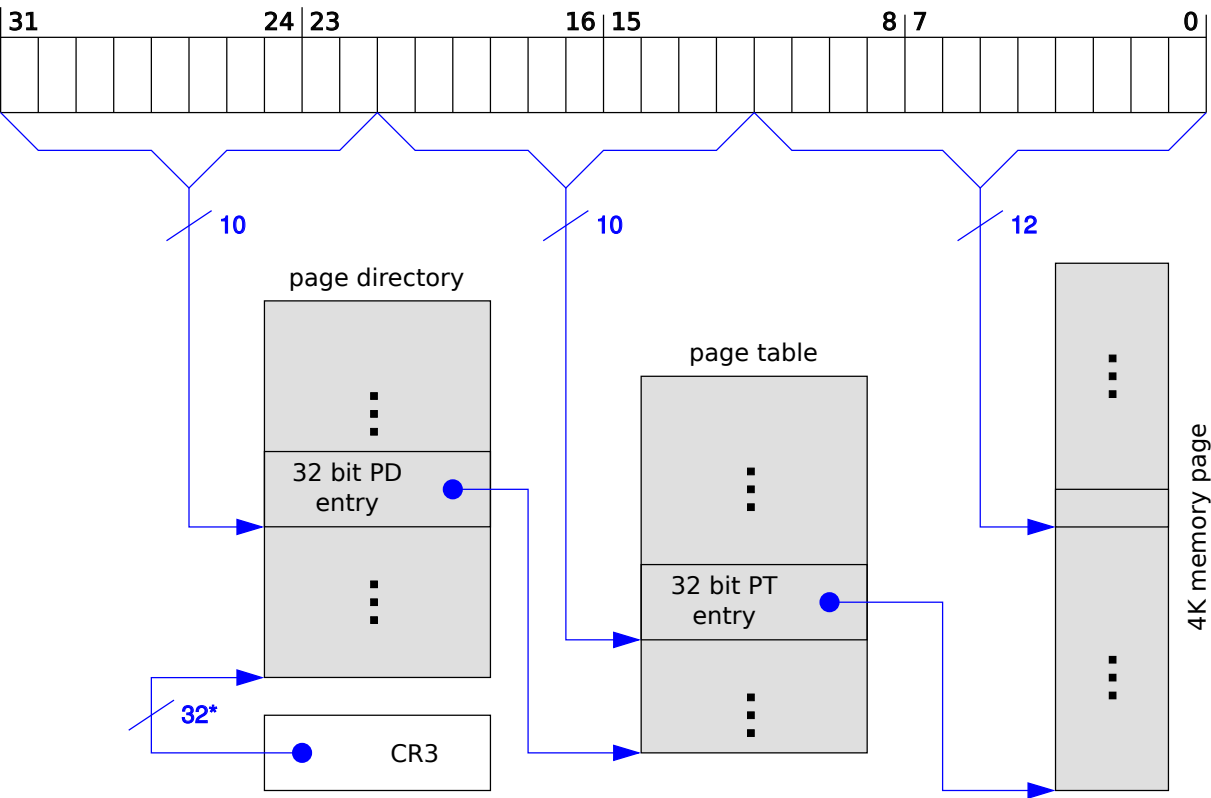


Linear address



*) 32 bits aligned to a 4-KByte boundary