
ERRATA EMBER® EM3598 RADIO BOARD TECHNICAL SPECIFICATION

This document describes issues that pertain to the EM3598 Radio Board. This Errata refers to the following releases of document TS13, *EM3598 Radio Board Technical Specification*:

- TS13 revision 0.1

1 SC4 SPI clock speed should be limited to 6 MHz and not 12 MHz

There is a bug on the EM3598 Radio Board that prevents operation with SC4 SPI clock speed at the default SPI clock speed of 12 MHz.

Silicon Labs provides SC4 application flexibility on the EM3598 Radio Board. There is a mux that configures SC4 to either be used as general purpose IO or as SPI serial flash for OTA bootloader functionality. Unfortunately, the mux selected does not work properly with low drive strength of the EM3598 SC4 pins and does not show full signal swing on the mux output pins, preventing 12 MHz SPI clock operation.

Affected Conditions

This issue is present on revision A03 of the EM3598 Radio Board. This issue will be corrected on future revisions of the EM3598 Radio Board.

Workaround

The board header file currently configures SC4 SPI clock speed as 6 MHz to overcome this issue.

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