

DEPT OF COMPUTER SCIENCE  
MILITARY COLLEGE OF SIGNALS, NUST  
ELECTRONIC CIRCUIT AND DEVICES  
BESE 15 A&B

Exam: Final Term  
Paper Type: (Regular)  
Semester: Spring

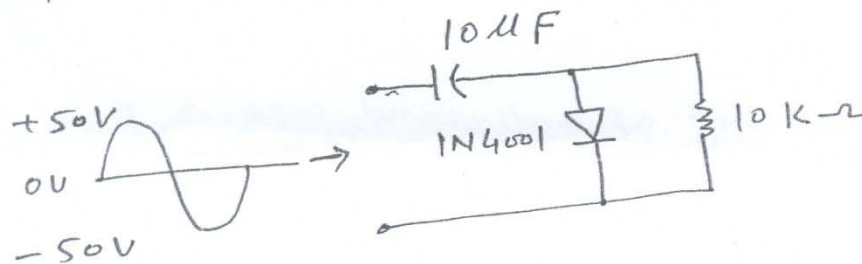
Instructor: Lt Col (R) Saleem  
Max Marks: 50  
Time Allowed: 2 hrs 30 mins

Note:

- 1) Make neat and clean diagram
- 2) Attempt all questions
- 3) Attach question paper with answer sheet

Question No-1

- a. Compute voltage across  $R_L$  in the circuit.  
Assume  $R_E$  is large enough to prevent significant capacitor discharge. (5)

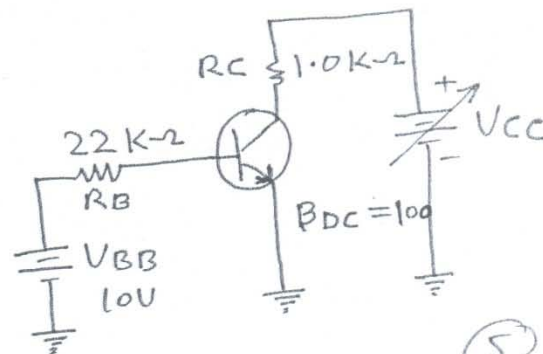


- b. Find the Maximum value to which  $V_{CC}$  can be adjusted without exceeding the ratings. maximum ratings are as under.

$$P_D(\text{max}) = 1000 \text{ mW}$$

$$V_{CE}(\text{max}) = 20 \text{ V}$$

$$I_C(\text{max}) = 150 \text{ mA}$$



Question No-2

- a. sketch ac equivalent circuit for ideal and practical

C. Explain and draw comparison of pinch-off and cutoff voltage (FET). (5)

### Question No-3

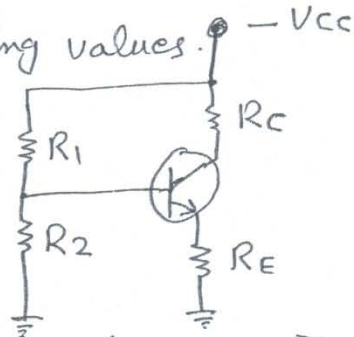
a. Find  $I_C$  and  $V_{CE}$  for following values.

$$R_1 = 68 \text{ K}\Omega \quad V_{CC} = -6 \text{ V}$$

$$R_2 = 47 \text{ K}\Omega \quad \beta_{DC} = 75$$

$$R_C = 1.8 \text{ K}\Omega$$

$$R_E = 2.2 \text{ K}\Omega$$

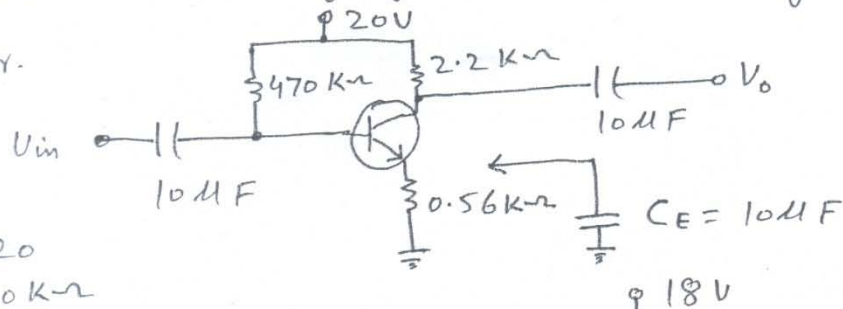


b. The i/p to a device is  $10,000 \text{ W}$  at the voltage of  $1000 \text{ V}$ , The o/p power is  $500 \text{ W}$  and o/p impedance is  $20 \Omega$ . Find voltage and power gain in decibels. (5)

Question No-4 derived expression for close loop voltage gain for non-inverting op-amp. Find close loop gain ( $A_v$ ) if open loop gain of op-amp is  $100,000$ ,  $R_f = 100 \text{ K}\Omega$  and  $R_{in} = 4.7 \text{ K}\Omega$ . (10)

### Question No-5

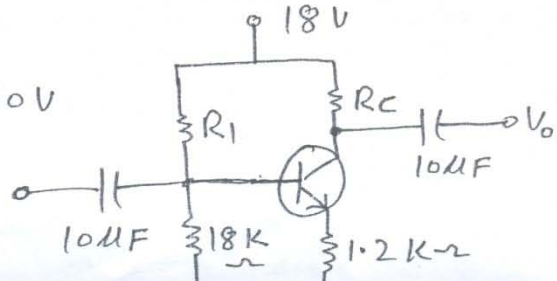
a. Find internal ac resistance of Emitter Junction, i/p impedance, o/p impedance and voltage gain without  $C_E$  (unbypass) capacitor.



$$\beta = 120$$

$$r_o = 40 \text{ K}\Omega$$

b.  $I_{CQ} = 2 \text{ mA}$  and  $V_{CEQ} = 10 \text{ V}$   
calculate  $R_1$  &  $R_C$ .



## Dept of CS-(MCS) MUST

Subj: Electronic Devices and cct; Instr: Saleem.

Course: B E S E 15-A & B ; Final paper soln

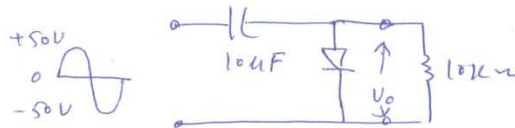
### Question No. 1 :-

a. This is a clamping cct. A negative dc value equal to the i/p peak less the diode drop is inserted by the clamping cct.

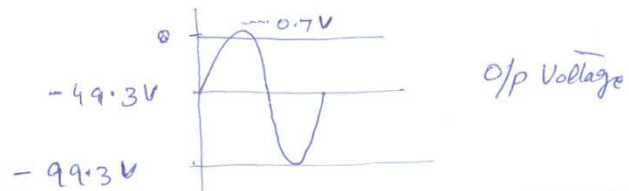
$$V_{DC} \approx -(V_{pin} - 0.7)$$

$$= -(50V - 0.7)$$

$$= -49.3V$$



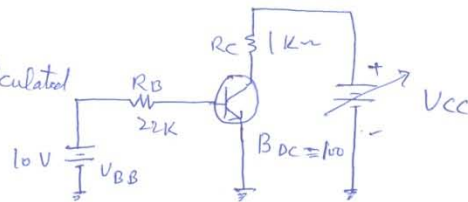
Capacitor discharges a little bit and average value will be slightly less than that i.e. -49.3V



b. First find  $I_B$  so that  $I_C$  be calculated

$$I_B = \frac{V_{BB} - V_{BE}}{R_B}$$

$$= \frac{10V - 0.7V}{22K\Omega} = 423 \mu A$$



$$\underline{I_C} \quad I_C = \beta_{DC} I_B = (100)(423 \mu A) = 0.0423 A = \boxed{42.3 mA}$$

$I_{C(max)} = 150 mA$  so  $I_C$  calculated is very very less than  $I_{C(max)}$  and

## Question No-2

### a. Ideal OP-amp

$$Z_{in} = \infty$$

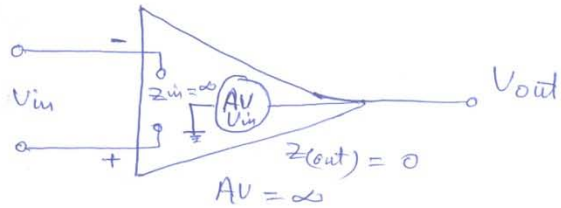
$$Z_{out} = 0$$

$$A_v(\text{gain}) = \infty$$

$$BW = \infty$$

$V_{in}$  &  $V_{out}$

2x Terminal -ve & +ve



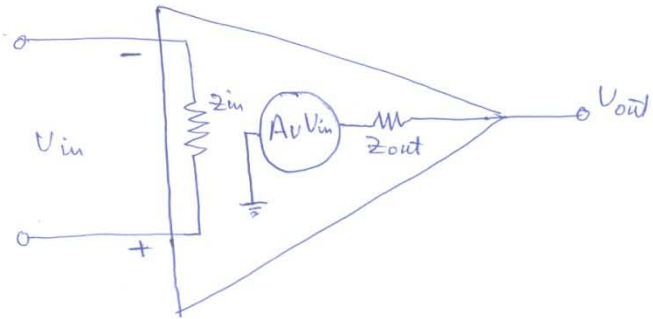
### practical OP-amp

$$Z_{in} = \text{very High}$$

$$Z_{out} = \text{" Low}$$

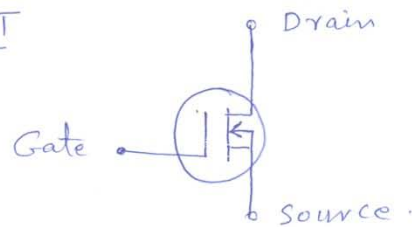
$$\text{Gain} = \text{very } v. \text{ High}$$

$$BW = \text{" High}$$

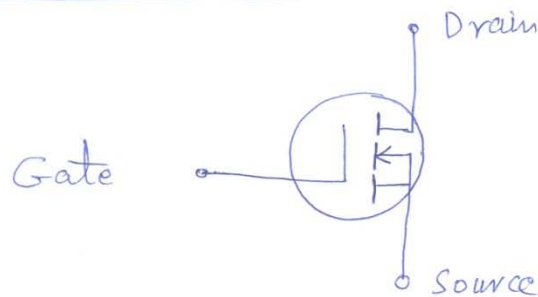


### b. N-channel - D-MOSFET

symbol sketch

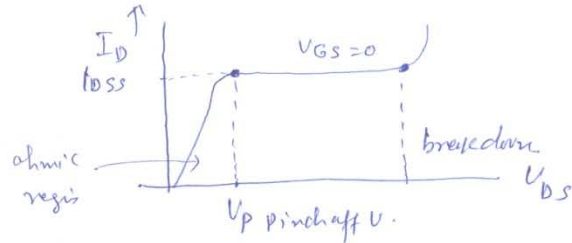


### N-channel - E - MOSFET

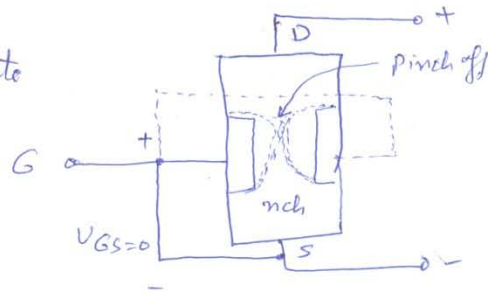


## C. pinch off voltage

- when  $V_{GS} = 0V$ , it is always measured at  $V_{GS} = 0$   
The value of  $V_{DS}$  at which  $I_D$  become constant is known as pinch off voltage
- pinch off occurs for  $V_{DS}$  value less than  $V_p$  when  $V_{GS} \neq 0$



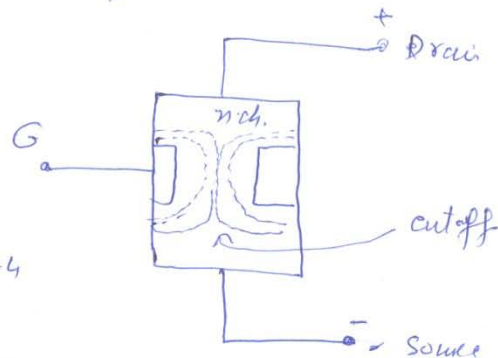
- $V_{GS(off)}$  and  $V_p$  are always equal in magnitude but opposite in sign.
- $V_p$  is measured by shorting the gate to source lead i.e.  $I_D$  increases up to its max value then levels off.
- mfr provides this value.



## • cutoff voltage

- The value of  $V_{GS}$  that makes  $I_D$  approximately zero is cutoff voltage  $V_{GS} = V_{off}$ .
- JFET to be operated b/w  $V_{GS} = 0$  &  $V_{GS(off)}$ , for this range  $V_{DS}$  &  $I_D$  will vary b/w  $\max(I_{DSS})$  to minimum i.e. channel is completely closed.

- $V_{GS(off)}$  and  $V_p$  are always equal in magnitude but opposite in sign i.e. if  $V_{GS(off)} = -4$  then  $V_p$  will be  $+4$ .
- mfr provide one of these values,





### Question No. 3

a.  $R_{in\ base} = \beta_{DC} R_E$   
 $= 75(2.2k\Omega) = 165k\Omega$

As  $R_{in}$  is not more than  $R_2$

i.e.  $(R_2)_{10} = (47k\Omega)_{10} = 470k\Omega$

So

•  $V_B = \left( \frac{R_2 \parallel R_{in\ base}}{R_1 + R_2 \parallel R_{in\ base}} \right) V_{CC} = \left( \frac{(47k\Omega) \parallel (165k\Omega)}{68k\Omega + (47k\Omega) \parallel (165k\Omega)} \right) -6V$   
 $= -2.1V$

•  $V_E = V_B + V_{BE} = -2.1V + 0.7V = -1.4V$

$I_E = \frac{V_E}{R_E} = \frac{-1.4V}{2.2k\Omega} = -636\mu A$

•  $I_C = I_E = \boxed{-636\mu A}$

$V_C = V_{CC} - I_C R_C = -6V - (-636\mu A)(1.8k\Omega) = -4.86V$

$V_{CE} = V_C - V_E = -4.86V - (-1.4) = -3.46V$

$\boxed{-636\mu A, -3.46V}$   
 $\boxed{-1.27mA, 5.94}$

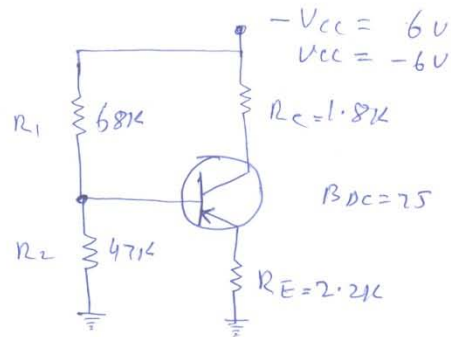
b. Power gain in db is i.e.  $G_{db} = 10 \log_{10} \frac{P_o}{P_{in}} = 10 \log_{10} \frac{500W}{10kW}$

$= 10 \log_{10} \frac{1}{20} = -10 \log_{10} 20$

•  $\text{Voltage gain} = 20 \log_{10} \frac{V_o}{V_i} = 20 \log_{10} \frac{\sqrt{PR}}{1mV}$   $-10(1.301) = \boxed{-13.01dB}$

$= 20 \log_{10} \sqrt{(500W)(20\Omega)}$

$= 20 \log_{10} \frac{100}{1mV} = 20 \log_{10} \frac{1}{10} = -20 \log_{10} 10 = \boxed{-20dB}$



will not change with  $V_{CC}$ . It depends upon  $I_B$  &  $\beta_{DC}$

$V_{RC}$

- Now voltage drop across  $R_C$  is

$$V_{RC} = I_C R_C = (42.3 \text{ mA})(1 \text{ k}\Omega) = \boxed{42.3 \text{ V}}$$

$V_{CE(\text{max})}$

- Now Find the value of  $V_{CC}$  when  $V_{CE} = V_{CE(\text{max})}$  which is  $20 \text{ V}$

$$V_{RC} = V_{CC} - V_{CE}$$

$$\text{So } V_{CC(\text{max})} = V_{CE(\text{max})} + V_{RC}$$

$$= 20 \text{ V} + 42.3 \text{ V} = \boxed{62.3 \text{ V}}$$

$V_{CC}$  can be increased up to  $62.3 \text{ V}$  before  $V_{CE(\text{max})}$  is exceeded.

$P_D$

- $P_D = (V_{CE(\text{max})}) I_C = (20 \text{ V})(42.3 \text{ mA}) = \boxed{846 \text{ mW}}$

As  $P_{D(\text{max})} = 1000 \text{ mW}$  so it is not exceeded when

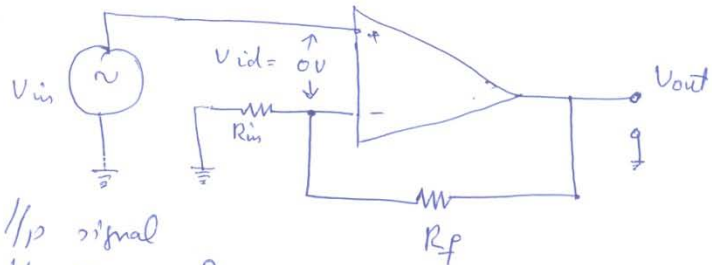
$$V_{CC} = 62.3 \text{ V}$$

So  $V_{CE(\text{max})} = 20 \text{ V}$  is the limiting rating

- If transistor is turned off then  $V_{CE(\text{max})}$  will exceed as the whole supply voltage  $V_{CC}$  will drop across transistor.

# Question No-4

Non-Inverting Amp close loop gain



- It's non inverting as the  $V_{in}$  signal drives the non-inverting  $V_{in}$  terminal.
- $V_{in}$  signal is applied to inverting  $V_{in}$  terminal through FB network.
- $R_f$  &  $R_{in}$  form  $-ve$  FB path and are voltage divider which reduces  $V_{out}$

FB voltage  $V_f = \left( \frac{R_i}{R_i + R_f} \right) V_{out}$   $\left| \begin{array}{l} \frac{R_i}{R_i + R_f} \text{ is attenuated} \\ \text{FB network} \end{array} \right.$

$V_f = B V_{out}$  — (1)  
The difference of  $V_{in}$  and  $V_f$  voltage i.e.  $V_{in} - V_f$  is applied which is the cause of  $V_{out}$  voltage.

$$V_{out} = A_{OL} (V_{in} - V_f)$$

$$V_{out} = A_{OL} (V_{in} - B V_{out})$$

$$V_{out} = A_{OL} V_{in} - A_{OL} B V_{out}$$

$$A_{OL} V_{in} = V_{out} + A_{OL} B V_{out}$$

$$V_{in} = V_{out} (1 + A_{OL} B)$$

overall voltage gain of Amp =  $V_{out}/V_{in}$

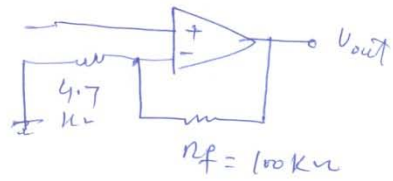
$$A_{CL(NI)} = 1 + \frac{R_f}{R_{in}}$$

$$\frac{V_{out}}{V_{in}} = \frac{A_{OL}}{1 + A_{OL} B} \quad \left| \begin{array}{l} A_{OL} B \gg 1 \end{array} \right.$$

$$\frac{V_{out}}{V_{in}} = \frac{A_{OL}}{A_{OL} B} = \frac{1}{B} \quad \text{So it's the reciprocal of FB attenu}$$

$$A_{CL(NI)} = \frac{R_i + R_f}{R_i} = \left( 1 + \frac{R_f}{R_{in}} \right)$$



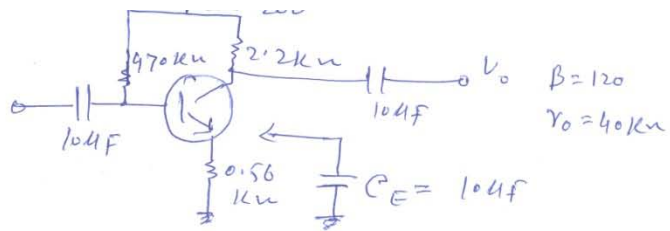


$$A_v (cl) = 1 + \frac{R_f}{R_i}$$

$$= 1 + \frac{100 \text{ k}\Omega}{4.7 \text{ k}\Omega} = 22.3 \text{ voltage}$$


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### Question-5



a. DC:

$r'_e$

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + (B+1)R_E} = \frac{20V - 0.7V}{470k\Omega + (121)0.56k\Omega} = 35.89\mu A$$

$$I_E = (B+1)I_B = (121)(35.89\mu A) = 4.34mA$$

and

$$r'_e = \frac{26mV}{I_E} = \frac{26mV}{4.34mA} = \boxed{5.99\Omega}$$

$1/p \approx$

$$\frac{25mV}{4.34} = 5.76\Omega$$

Test the condition  $r_0 \geq 10(R_C + R_E)$

$$40k\Omega \geq 10(2.2k\Omega + 0.56k\Omega)$$

"condition satisfied"  $40k\Omega \geq 10(2.76k\Omega) = 27.6k\Omega$

So

$$Z_{base} = \beta(r'_e + R_E) = 120(5.99\Omega + 560\Omega)$$

$$= \boxed{67.92k\Omega}$$

$$Z_{in} = R_B \parallel Z_b = 470k\Omega \parallel 67.92k\Omega$$

$$\frac{470 \times 67.92}{470 + 67.92} = \boxed{59.34k\Omega}$$

$o/p \approx$

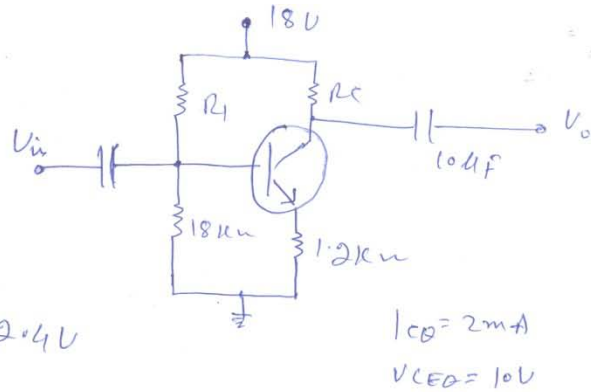
$$Z_o = R_C = 2.2k\Omega$$

voltage gain

$r_0 \geq 10R_C$  is satisfied so

$$A_v = \frac{V_o}{V_{in}} = -\frac{\beta R_C}{Z_b} = \frac{(120)(2.2k\Omega)}{67.92k\Omega} = \boxed{-3.89}$$

B



$$V_E = I_E R_E \cong I_C R_E$$

$$= (2 \text{ mA})(1.2 \text{ k}\Omega) = 2.4 \text{ V}$$

$$V_B = V_{BE} + V_E = 0.7 \text{ V} + 2.4 \text{ V} = 3.1 \text{ V}$$

$$V_B = \frac{R_2 V_{CC}}{R_1 + R_2}$$

$$\cancel{3.1 \text{ V}} \cong \frac{(18 \text{ k}\Omega)(18 \text{ V})}{R_1 + 18 \text{ k}\Omega} = 3.1 \text{ V}$$

$$3.1 \text{ V} = 3.1 \text{ V}(R_1) + (3.1 \text{ V})(18 \text{ k}\Omega)$$

$$3.1 \text{ V} = 3.1 R_1 + 55.8 \text{ k}\Omega$$

$$R_1 = \frac{3.1 - 55.8}{3.1} = \frac{2.682 \text{ k}\Omega}{3.1} = \boxed{86.52 \text{ k}\Omega}$$

$$R_C = \frac{V_{RC}}{I_C} = \frac{V_{CC} - V_C}{I_C}$$

$$V_C = V_{CE} + V_E$$

$$10 \text{ V} + 2.4 \text{ V} = 12.4 \text{ V}$$

$$R_C = \frac{18 \text{ V} - 12.4 \text{ V}}{2 \text{ mA}} = \boxed{2.8 \text{ k}\Omega}$$