Assignment No. 3

Subj: Electronic cot and Druces.

clas: BESE 15(A&B)

Instr: L+ cal Salcem (R)

Date 6th may 2011 To be submitted by

15 may 2011

Question No.1:

a. Derive a point expressions for the following transistor biasing, Assume NPN transistor type.

· Emitter Bias

· Collector Feed Back Bias

· Base Bias

b. Find Q-point and maximum peak value of base current for the cretwe region of an NPN tramsistor with following cct elements and parameters.

Bre = 300 UBB = 100

RB = 47 K-2 VCC = 20V

Re = 330 -2

Also sketch load line.

Question No-2.

- a. Draw Transistor Ac equivalent model using r-penameter
- b. Establish the relationship of h-parameter and y-parameters for Lac and Bac
- c. what is the effect of Emitter By pars capacitos on voltage gain in CE configuration, Give example.
- d. prove with the help of example

 that voltage gain (Av) without by pass

 capacitor in CF configurations is asherely

 effected:

Questim 10.3

- a. Explain the operation of N. Channel basic E-MOSFET.
- b. Draw an-N. Channel JFET tramsfer Characteritic cure from JFET Drain Characteritic cure.
- c. what are the Types of MOSFET discursed in the class and observed symbol for each Type.

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