

Not	
in	out
1	0
0	1

And		
a	b	out
1	1	1
1	0	0
0	1	0
0	0	0

Or		
a	b	out
1	1	1
1	0	1
0	1	1
0	0	0

XOR		
a	b	out
1	1	0
1	0	1
0	1	1
0	0	0

Mux			
a	b	sel	out
1	1	1	1
1	0	1	0
0	1	1	1
0	0	1	0
1	1	0	1
1	0	0	1
0	1	0	0
0	0	0	0

dMux			
in	sel	out a	out b
1	1	0	1
1	0	1	0
0	1	0	0
0	0	0	0