Not			Mux			
in	out		a	b	sel	out
1	0		1	1	1	1
0	1		1	0	1	0
			0	1	1	1
And			0	0	1	0
а	b	out	1	1	0	1
1	1	1	1	0	0	1
1	0	0	0	1	0	0
0	1	0	0	0	0	0
0	0	0				
Or						
Or a	b	out	dMux			
	b 1	out 1	dMux in	sel	out a	out b
a				sel 1	out a 0	out b 1
a 1	1	1	in			
a 1 1	1 0	1 1	in 1	1	0	1
a 1 1 0	1 0 1	1 1 1	in 1 1	1 0	0 1	1 0
a 1 1 0	1 0 1	1 1 1	in 1 1 0	1 0 1	0 1 0	1 0 0
a 1 1 0 0	1 0 1	1 1 1	in 1 1 0	1 0 1	0 1 0	1 0 0
a 1 1 0 0	1 0 1 0	1 1 1 0	in 1 1 0	1 0 1	0 1 0	1 0 0
a 1 1 0 0 XOR a	1 0 1 0	1 1 1 0	in 1 1 0	1 0 1	0 1 0	1 0 0
a 1 1 0 0 XOR a 1	1 0 1 0	1 1 1 0	in 1 1 0	1 0 1	0 1 0	1 0 0