

## **Rohan Agrawal**

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### **EDUCATION:**

**2003 – 2004**     **Masters in System-On-Chip Engineering**  
**University of Edinburgh, UK**

**1999 – 2003**     **Bachelors in Electronics & Instrumentation Engineering**  
**Shri Govindram Seksariya Institute of Technology & Science (SGSITS), Indore, India**  
Graduated in the top 10% of the class  
Awarded a First Class Degree for outstanding academic performance

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### **BUSINESS EXPERIENCE:**

**Company:** Intel Japan K K Tokyo, Japan

**Period:** Nov 2008 till date

**Designation:** Customer Enabling Manager

- World's largest semiconductor chip maker Intel Tokyo handles sales and marketing, solution services, e-Business
- Achieved "Division Recognition Award" for enabling innovative web-portal, providing efficient and smart way for global customer support
- Articulate the business processes needed for efficiency enhancement and smooth enablement for platform launch across the globe
- Achieved record score of CEP (Customer Excellence Program) in Intel's history for 2011
- Played vital role in project management and coordination for three generations of Intel platforms (2010, 2011 & 2012 launch) in Japan
- Represented and lead the team of AEs across US, Israel, Taiwan and Japan
- Quality enhancement while reducing production time & cost via effective vendor engagement
- Achieved record reduction in cost as well as production time with excellent vendor management

**Company:** Nihon Synopsis K K, Tokyo, Japan

**Period:** Mar 2007 to Aug 2008

**Designation:** Application Consultant

- Synopsis is one of the largest companies in the Electronic Design Automation industry.
- Supported Japanese customers for Front end simulators
- Played vital role in company's historical deal with one of the major Japanese clients and supported the design migration activities
- Conducted customer trainings and project coordination between teams in US, India & Japan

**Company:** Softbridge Solutions Pvt. Ltd., Tokyo, Japan

**Period:** Sep 2005 to Mar 2007

**Designation:** VLSI Engineer

- Softbridge has three business divisions focused on education, software development and mobile-media services
- Worked on WiMAX project by one the major Japanese client
- Received training for Japanese language and IT-culture (JLPT-2)

**Company:** Semi-Square Consultants Pvt. Ltd. Bangalore, India

**Period:** Mar 2005 to Aug 2005

**Designation:** ASIC Design & Verification Engineer

- Start-up company to serve logic design, simulation and synthesis services
- Implemented PCI Express bus-data link layer via RTL coding

**Company:** SOS Design Solutions, Livingston, Scotland, UK

**Period:** Mar 2004 to Mar 2005

**Designation:** ASIC Design Engineer

- Start-up company to develop re-usable IPs for projects like Software Radio
  - Test the efficiency of reconfigurable arrays for Software radio implementation
  - Software modules written in C language were mapped using RTL using cell level specifications
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### **ADDITIONAL INFORMATION:**

- Social Networking Head for Vedic Culture Centre, Japan
- Event in-charge: arranged five major fund raising events in Japan for philanthropic activities
- Founder and owner of Gym and Ping-Pong club in Intel Japan
- Founder of NPO – Hindi Sabha Japan, promoting various platforms for Hindi language learners in Japan
- President (Sep-2002) of Students Society of Elex & Instr Department, SGSITS

**Languages:**     Fluent English, Native Hindi, Japanese (Business level – read/write/spoken)

**Nationality:**     Indian (With Japanese Work Visa)

**Date of Birth:**     19<sup>th</sup> April 1982