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B.Tech - Electronics and Communication Engineering
K L University
Guntur

PROFILE

Excited about electronics and willing to build up projects that can change, adapt not only in the field of electronics but also in regard to computers.

CAREER OBJECTIVE

I'm excited about exploring challenging opportunities in my current field of study. My goal is to expand my knowledge and skills through a master's program, aiming for excellence in my chosen field. Over the next few years, I envision attaining a respected position in both my academic journey and career. Pursuing a master's degree is my next step to gain advanced expertise, enrich my understanding, and make meaningful contributions to the academic community.

EDUCATION:-

Degree	Institute/Board	CGPA/Percentage	Year
B.Tech	K L University,Guntur	9.16 (Current)	2020-2024
Intermediate	Board of Intermediate Education Andhra Pradesh	8.83	2018-2020
Secondary	Central board of Secondary Education	9.5	2018

KEY COURSES TAKEN:-

- TCAD (Technology computer aided design)
- VLSI Design
- C Programming
- Internet Of Things
- Digital Logic and Processor

TECHNICAL SKILLS:

- TCAD TOOL
- Internet Of Things (IOT Modules)
- Xilinx Vivado
- Cisco Packet Tracer
- C-language (Intermediate)
- Python (Intermediate)

EXPERIENCE:

- Got certified from NI Lab view and completed NI clad EXAM as part of certification course in 2nd year.
- Got trained from TESSOLVE in VLSI DOMAIN as part of internship in 2nd year.
- Got trained from TESSOLVE in VLSI DESIGN AND VERFICATON as part of certification course in 3rd year.
- Got trained from TESSOLVE VERIFICATION USING SYSTEM VERILOG as part of certification course in 3rd year.
- Got trained from 1STOP in VLSI DESIGN as part of internship in 3nd year

PROJECTS:

1. Designing a (NANO SCALE TRANSISTOR)

- In this project, a short-gate tunnelling-field-effect transistor(SG-TFET) structure has been investigated for the dielectrically modulated biosensing application in comparison with full-gate tunnelling-field-effect-transistor structure of similar dimensions. This project is done using SILVACO TCAD TOOL.

2. Designing a UART (Universal Asynchronous Receiver and Transmitter) using VERILOG

- This project aims to create a compact UART module using Verilog for asynchronous serial communication. Key features include configurable baud rates, start/stop bit handling, error detection, and FIFO buffering. The project provides hands-on experience in Verilog programming and is suitable for those interested in practical digital design and FPGA applications.

3. 16 Bit RISC (Reduced Instruction Set Computing) using VERILOG

- This project involves designing a 16-bit Reduced Instruction Set Computing (RISC) processor using Verilog. The focus is on creating a compact and efficient processor architecture with a limited set of instructions. Key components include an ALU, control unit, and registers. The project provides practical experience in digital design and Verilog programming, making it ideal for those interested in processor development and FPGA applications.

4. Arduino Home Automation

- Create a smart Home Automation System using Arduino Uno. This project enables remote control and monitoring of household devices, offering convenience and efficiency. Experience the simplicity of Arduino in building a connected and automated living space.

5. Train Collision Avoidance with Arduino

- Developed a train collision avoidance system using Arduino. The system uses special sensors for precise train position detection, wireless communication for coordination, and an Arduino-controlled braking system to prevent collisions. The project aims to enhance train safety by predicting and avoiding potential accidents, making it a practical solution for railway applications.

6. Real-Time Face Recognition with Python and OpenCV

- Built a quick and accurate real-time face recognition system using Python and OpenCV. Utilized smart tools for efficient identification in live videos, making it suitable for security and user authentication applications. The project showcases the simplicity and reliability of Python and OpenCV for those interested in exploring facial recognition technology.

PUBLICATIONS:

- Gate-All-Around FET based 6T SRAM Design Using a Device-Circuit Co-Optimization Framework. (IJERT)
- S ANUDEEP ;etc all. International Conference On Distributed Computing And Electrical Circuits And Electronics, "Convolutional Neural Network for Printed circuit Board Verification" , Communicated(IEEE).

EXTRACURRICULAR ACTIVITIES:

- Core member for Designing wing in ECE student body.
- Coordinator in National Level Techno Fest Conducted by KLU 2022.
- Coordinator in National Level Techno Fest Conducted by KLU 2023.
- Design Lead of IEEE NANOTECHNOLOGY SB(KL university).

Hobbies

- In my free time, I love to express my creativity through graphic design using (Photoshop, After Effects), unwind with some gaming, and immerse myself in the world of music to find joy and inspiration.

LANGUAGES

- English: Full Working Proficiency
- Hindi: Limited Working Proficiency
- Telugu: Full Working Proficiency

STRENGTHS:

- Good at Problem Solving
- Good Listening Skills
- Good Communication Skills
- Good Team working Skills

DECLARATION

I hereby declare that the details and information given above are complete and true to the best of my knowledge.