

Jitendra Sai Karthik Jinka

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📅 04/10/2003 🇮🇳 India

OBJECTIVE

To be associated with a progressive organization, which provide me dynamic work sphere to extract my inherent skills as a professional and develop my aptitude for the further growth of the organization.

SKILLS

Embedded Systems

Java Programming

Verilog

HDL programming

PCB Artwork

Arduino, ESP 32, Nodemcu
Developing Boards

TOOLS

Xilinx Vivado Tool, I have a sound knowledge about the tool and I synthesized the basic logic gates and some Functional module like Adders etc., and mainly practiced some basic level projects in the tool.

Model Sim, This tool is introduced in our academics, Where I practiced some HDL for Logic gates and some combinational circuits.

Dev-C++ Tool, I have a sound knowledge in this tool where I used this to practice the C Programming.

Python IDLE & Jupiter Notebook, I am having the basic knowledge on both of these tools and currently working on python programming for skill enhancement.

Cadence Tool, I have basic knowledge and I am working more to enhance my skills in this tool.

EDUCATION

Bachelor of Technology (ECE), K L University

2021 - 2024

In my 2years of B-Tech life me and my friends as a group participated a 30hrs Build-A-Thon, also organized our Department fest Zrotriya and also demonstrated my project in other college Expo. Also balanced my academics with CGPA: 9.31

Diploma (ECE), Government Polytechnic Vijayawada

2018 - 2021

As my Diploma as ECE student I actively participate in clubs and organizations related to my field of study. As an electronic student me with my group participate in technical competitions and organized some campaigns.

Secondary Schooling, K C High School

2017 - 2018

CGPA: 9.5

CERTIFICATES

Dakshin Bharath Hindi Prachar Sabha

Praveena Uthardha

VLSI Design & Verification

Tessolve Level- 2

Deep Learning

IBM (E-Training) Level-1

Machine Learning

Tessolve Level- 1

Cisco Networking Academy

Network & Automation

PROJECTS

Smart Bot (Car), Restrict' s, through sensing

12/2021 -

It was Designed to detects the Accidents occurrence based on vibrations and quick passage of information about the location of incident.

01/2022

Home Automation, Bluetooth Communication

10/2021 -

Implemented to made Smarter usage of surrounding Appliances.

11/2021

Smart Parking Unit, Verilog Simulation

05/2022 -

Car parking Management system designed by using the Verilog commands and Simulated in Xilinx Vivado.

06/2022

ACHIEVEMENTS

National Karate Championship, Goju-Ryu (Style)

06/2014

Got a Gold Medal in National Championship Battle in karate. Also participated State level Tournaments with leading results.

Chess, Andhra Chess Federation

2015

Won the Intermediate level chess tournament.

DECLARATION

I hereby declare that all the information furnished above is true to the best of my belief.

Jitendra Sai Karthik Jinka
Vijayawada