**ANILKUMAR MANTHOJU**  
Phone: 7842273099,8919761811  
Email: anilkumar4f6@gmail.com

**Career Objective:**

To obtain an Embedded System Engineer position with an Electronics company, where my electrical and programming skills together can help in the design and development of various devices.

**Summary of Skills:**

* Strong experience in embedded systems product development
* Experience on low level driver development for microcontrollers
* Excellent knowledge of electrical embedded systems and microprocessor
* Skilled in reading and interpreting blueprints and schematics
* Ability to develop software and perform debugging
* Excellent skills in C, and developing test specifications and plans
* Good knowledge with the designing, testing, and construction of design
* Adopt in working with the latest lab equipment
* Hands on experience on Bluetooth, **SPI**, **UART, 1553,I2C,ETHERNET,CAN**
* Proficiency in tools of Keil, CC Studio, CCES, ISE, Vivado SDK, Rhapsody, Tasking, Cantata, Canape and DaVinci Configurator.
* Experience in working with 8/16/32 bit microcontrollers and ARM Processors
* Expertise in working with **GPIO, ADC, Timers, PWM and** **Watchdog**
* Handled devices: **8051, TMS570Ls3137, TMS320F28335, ADSP21489, LPC1788, MSP430F5659, Aurix tricore Microcontroller, Spartan 6, Artix 7 and Zynq.**
* Handled Memory Devices: NAND Flash, SDRAM, MRAM and EEPROM
* Good knowledge in understanding of Algorithms and System Integration

**Work Experience:**

Senior Embedded Engineer  
Cyient Ltd, Hyderabad  
Aug 2022 – Present

* Working as **BSW Developer** at ZF client for steer by wire projects (Mercedes Benz, Volvo and Renault)
* AUTOSAR Compliant embedded software development
* Implement software functions and peform bench tests and unit tests to verify them
* Perform static Analysis of source code using Polyspace
* Participate in peer review of source code
* Support software test plan creation and review test results
* Delegate and supervise tasks within in the project team.
* Software Component Design & Development: **Rhapsody**
* Measuring/Testing tools : **Tasking and Canape**
* Automated module/ Unit testing tools : **CANtata**
* Experience version control tool: **PTC** **Integrity**.

Embedded System Engineer  
Apollo Micro Systems Ltd, Hyderabad  
May 2017 – July 2022

* Analyze and develop system and software requirements according to customer's specifications
* Create and maintain software design, and associated documentation
* Use standing coding practices and create software package
* Integrate software in devices and perform unit and integration testing
* Develop effective embedded system solutions for existing and new devices
* Assist in software verification and solving software issues
* Created digital circuit design reading customer's requirements
* Developed test and validation plan for system testing

**Project Details:**

**Title: Five Processor Board**

**Team size:** 3 members

**Client**: BEL, Bangalore

**Job role:** Test Engineer

**Job responsibilities**: As per specified ATP of client requirement tested module level and system level. In this project five DSP processors are interlinked with each other and one Altera FPGA.

**Title: FPGA Simulator JIG UNIT**

**Team size:** 3 members

**Client**: L & T, Pune

**Job role:** Firmware Engineer

**Job responsibilities**: As per specified ATP of client requirement developed software in ISE SDK. For Health check of PCB’s i.e. every module driver is developed according to that the condition is checked. This project is used to test the Agni 5 launcher controller like ADC, DAC, DIs, Dos and UARTs. The communication is Ethernet (10 Mbps).

**Title: PAN AND TILT Controller**

**Team size:** 3 members

**Client**: BEL, Bangalore

**Job role:** Firmware Engineer

**Job responsibilities**: This project is built to spec and helped in design level. Software is developed to test all the peripherals of the system. The project consists of two boards’ controller card and relay card. In Controller card DIs, Dos, SSI and UARTs are available. For communication Ethernet is there.

**Title: EVP Signal Processing**

**Team size:** 4 members

**Client**: CSIO, Chandigarh

**Job role:** Firmware Engineer

**Job responsibilities**: In this Project Microcontroller and FPGA (Zynq-702) is used. Software developed for Microcontroller (MSP430F5659) in CC Studio.

**Title: IDAS**

**Team size:** 4 members

**Client**: NSTL, Vizag

**Job role:** Team Leader

**Job responsibilities**: In this Project involved in Design and development according to the user requirements. In this project Microcontroller and FPGA is used. Microcontroller is Master and FPGA is slave. This is mainly for Data Acquisition system. This project consists of ADCs, DIs, Dos and Memory devices. 1553, UART, USB and Ethernet is for communication purpose. Software developed for the Microcontroller (TMS570LS3137) in CC Studio.

**Title: GCC CARD**

**Team size:** 3 members

**Client**: IRDE, Dehradun

**Job role:** Firmware Developer

**Job responsibilities**: In this Project involved in Development of firmware for ADSP21489 according to the user requirements. In this project DSP and FPGA is used. DSP is Master and FPGA is slave. This project consists of ADCs, DIs, DOs and Memory devices. 1553, UART, SPI, I2C and Ethernet is for communication purpose. Software developed for the DSP (ADSP21489) in CCES (Cross Core Embedded Studio).

**Education:**

* Bachelor's Degree in Electronics and Communication Engineering with 72.5%  
  Jayamukhi Institution and Technological Sciences, Narsampet, Warangal  
  2015
* Intermediate in MPC with 92%   
  SVV Junior College, Warangal  
  2011
* Matriculation with 92%   
  Siddhartha High School, Chennaraopet, Warangal  
  2009