N-Channel 60-V (D-S) Single and Quad MOSFETs

PRODUCT SUMMARY				
Part Number	**$V_{(BR)DSS}$ Min (V)**	**$r_{DS(on)}$ Max (Ω)**	**$V_{GS(th)}$ (V)**	**I_D (A)**
2N6660	60	3 @ V_{GS} = 10 V	0.8 to 2	1.1
VQ1004J/P		3.5 @ V_{GS} = 10 V	0.8 to 2.5	0.46

FEATURES

- Low On-Resistance: 1.3 Ω
- Low Threshold: 1.7 V
- Low Input Capacitance: 35 pF
- Fast Switching Speed: 8 ns
- Low Input and Output Leakage

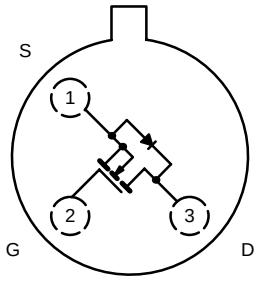
BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays

TO-205AD
(TO-39)



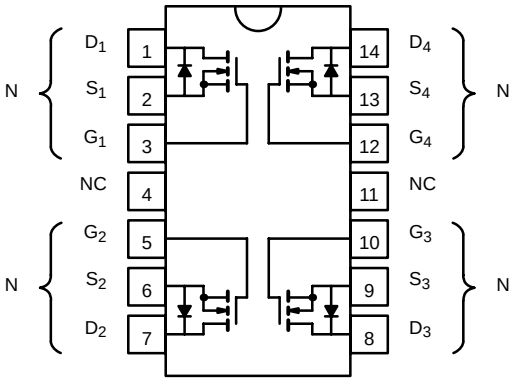
Top View
2N6660

Device Marking
Side View

2N6660
"S" *fllxxyy*

"S" = Siliconix Logo
f = Factory Code
ll = Lot Traceability
xxyy = Date Code

Dual-In-Line



Top View
Plastic: VQ1004J
Sidebraze: VQ1004P

Device Marking
Top View

VQ1004J
"S" *fllxxyy*

VQ1004P
"S" *fllxxyy*

"S" = Siliconix Logo
f = Factory Code
ll = Lot Traceability
xxyy = Date Code

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C UNLESS OTHERWISE NOTED)

Parameter		Symbol	2N6660	Single		Total Quad	Unit
				VQ1004J	VQ1004P	VQ1004J/P	
Drain-Source Voltage		V_{DS}	60	60	60		V
Gate-Source Voltage		V_{GS}	±20	±30	±20		V
Continuous Drain Current (T_J = 150°C)	T_C= 25°C	I_D	1.1	0.46	±0.46		A
	T_C= 100°C		0.8	0.26	0.26		A
Pulsed Drain Current[a]		I_{DM}	3	2	2		A
Power Dissipation	T_C= 25°C	P_D	6.25	1.3	1.3	2	W
	T_C= 100°C		2.5	0.52	0.52	0.8	W
Thermal Resistance, Junction-to-Ambient[b]		R_{thJA}	170	0.96	0.96	62.5	°C/W
Thermal Resistance, Junction-to-Case		R_{thJC}	20				°C/W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	–55 to 150				°C

Notes
a. Pulse width limited by maximum junction temperature.
b. This parameter not registered with JEDEC.

SPECIFICATIONS (T_A = 25°C UNLESS OTHERWISE NOTED)

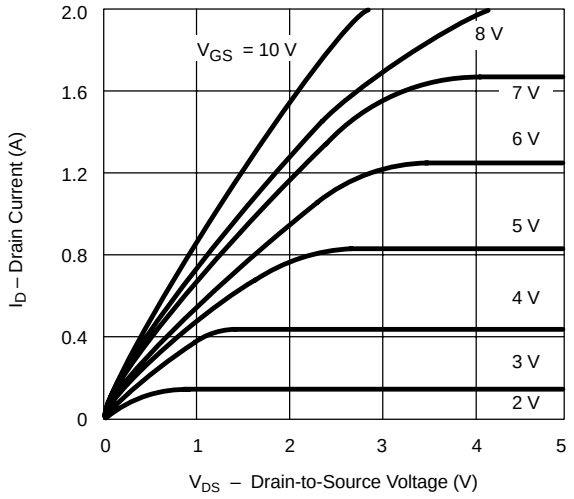
Parameter	Symbol	Test Conditions	Typ[a]	Limits 2N6660 Min	Limits 2N6660 Max	Limits VQ1004J/P Min	Limits VQ1004J/P Max	Unit
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0 V, I_D = 10 μA	75	60		60		V
Gate-Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 1 mA	1.7	0.8	2	0.8	2.5	V
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±15 V			±100		±100	nA
		T_C = 125°C			±500		±500	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 60 V, V_{GS} = 0 V			10			μA
		V_{DS} = 35 V, V_{GS} = 0 V						μA
		V_{DS} = 48 V, V_{GS} = 0 V					1	μA
		T_C = 125°C			500		500	μA
		V_{DS} = 28 V, V_{GS} = 0 V						μA
		T_C = 125°C						μA
On-State Drain Current[b]	$I_{D(on)}$	V_{DS} = 10 V, V_{GS} = 10 V	3	1.5		1.5		A
Drain-Source On-Resistance[b]	$r_{DS(on)}$	V_{GS} = 5 V, I_D = 0.3 A[d]	2		5		5	Ω
		V_{GS} = 10 V, I_D = 1 A	1.3		3		3.5	Ω
		T_C = 125°C[d]	2.4		4.2		4.9	Ω
Forward Transconductance[b]	g_{fs}	V_{DS} = 10 V, I_D = 0.5 A	350	170		170		mS
Common Source Output Conductance[b]	g_{os}	V_{DS} = 10 V, I_D = 0.1 A	1					mS
Diode Forward Voltage	V_{SD}	I_S = 0.99 A, V_{GS} = 0 V	0.8					V
Dynamic								
Input Capacitance	C_{iss}	V_{DS} = 24 V, V_{GS} = 0 V f = 1 MHz	35		50		60	pF
Output Capacitance	C_{oss}		25		40		50	pF
Reverse Transfer Capacitance	C_{rss}		7		10		10	pF
Drain-Source Capacitance	C_{ds}		30		40			pF
Switching[c]								
Turn-On Time	t_{ON}	V_{DD} = 25 V, R_L = 23 Ω I_D ≅ 1 A, V_{GEN} = 10 V R_G = 25 Ω	8		10		10	ns
Turn-Off Time	t_{OFF}		8.5		10		10	ns

Notes
a. For DESIGN AID ONLY, not subject to production testing.
b. Pulse test: PW ≤ 80 μs duty cycle ≤ 1%.
c. Switching time is essentially independent of operating temperature.
d. This parameter not registered with JEDEC on 2N6660.

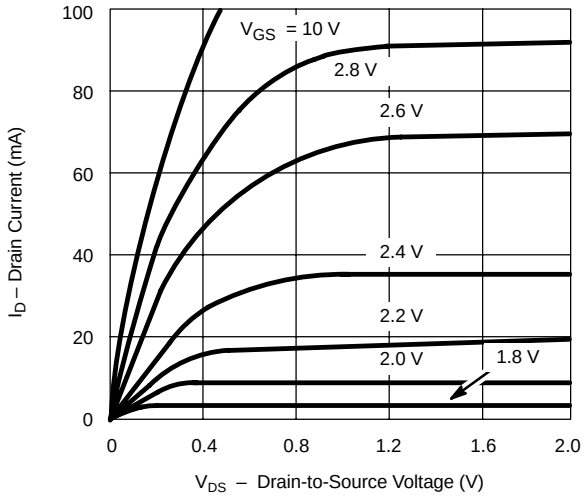
VNDQ06

TYPICAL CHARACTERISTICS (T_A = 25°C UNLESS OTHERWISE NOTED)

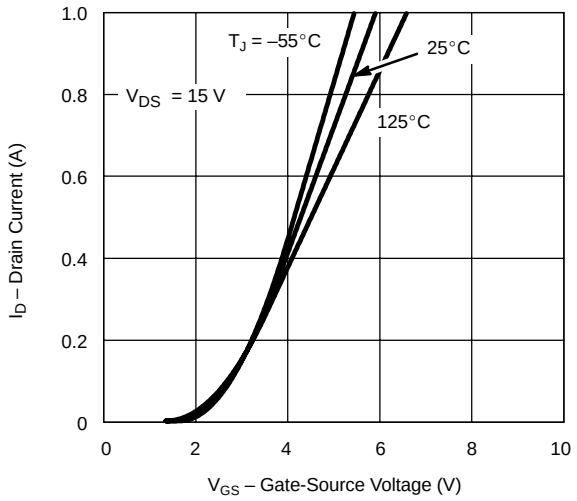
Ohmic Region Characteristics



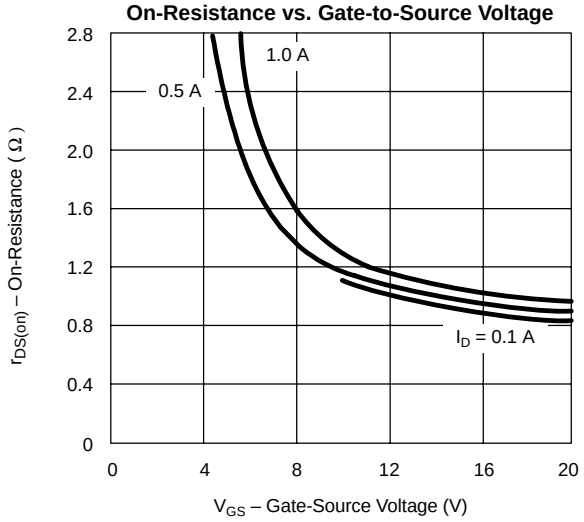
Output Characteristics for Low Gate Drive



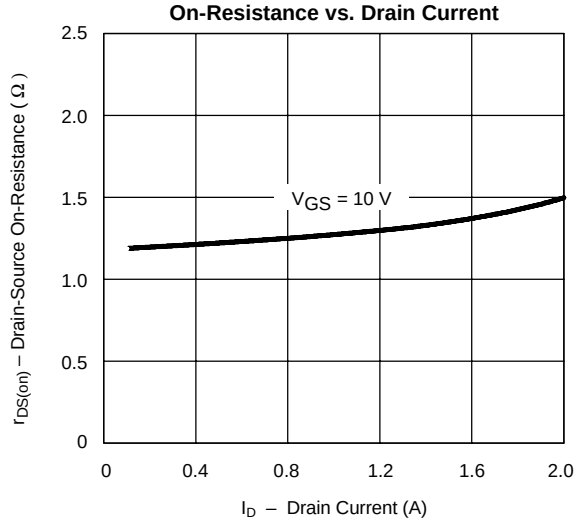
Transfer Characteristics



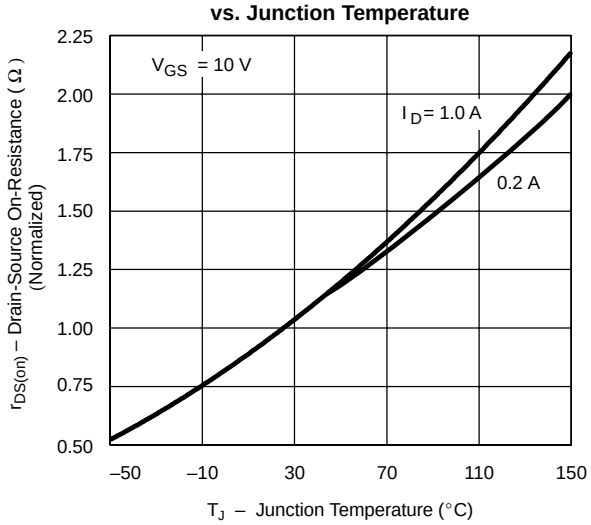
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Drain Current

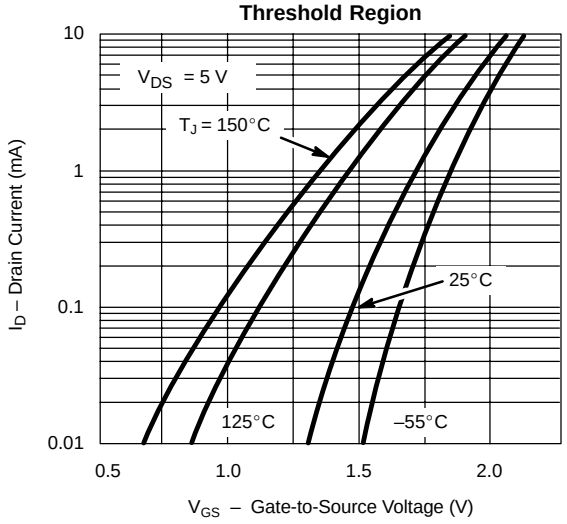


Normalized On-Resistance vs. Junction Temperature

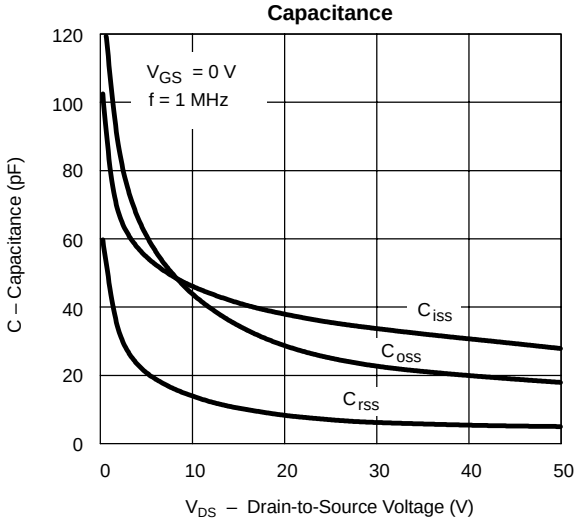


TYPICAL CHARACTERISTICS (T_A = 25°C UNLESS OTHERWISE NOTED)

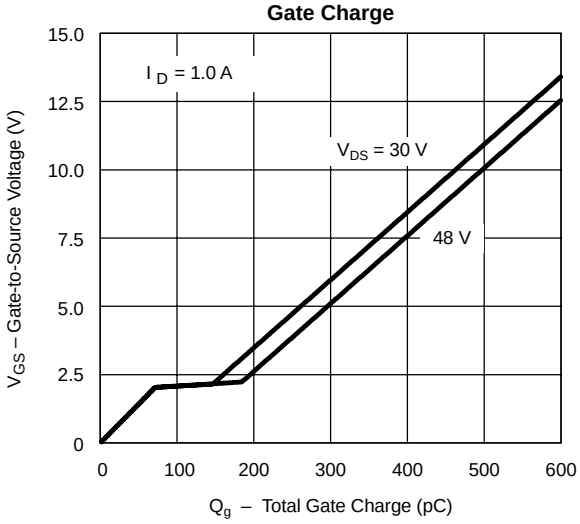
Threshold Region



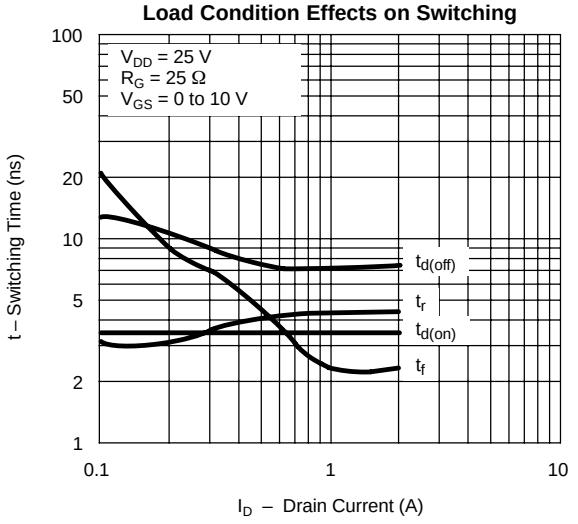
Capacitance



Gate Charge



Load Condition Effects on Switching



Normalized Effective Transient Thermal Impedance, Junction-to-Case (TO-205AD)