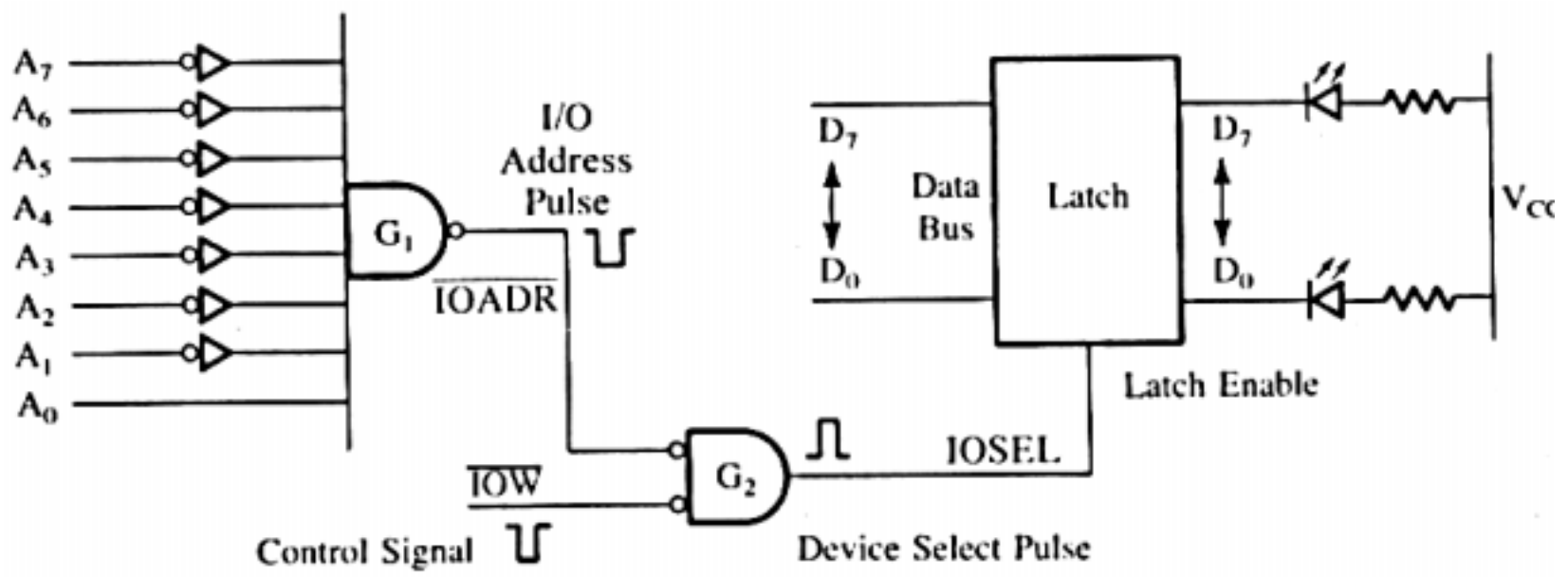
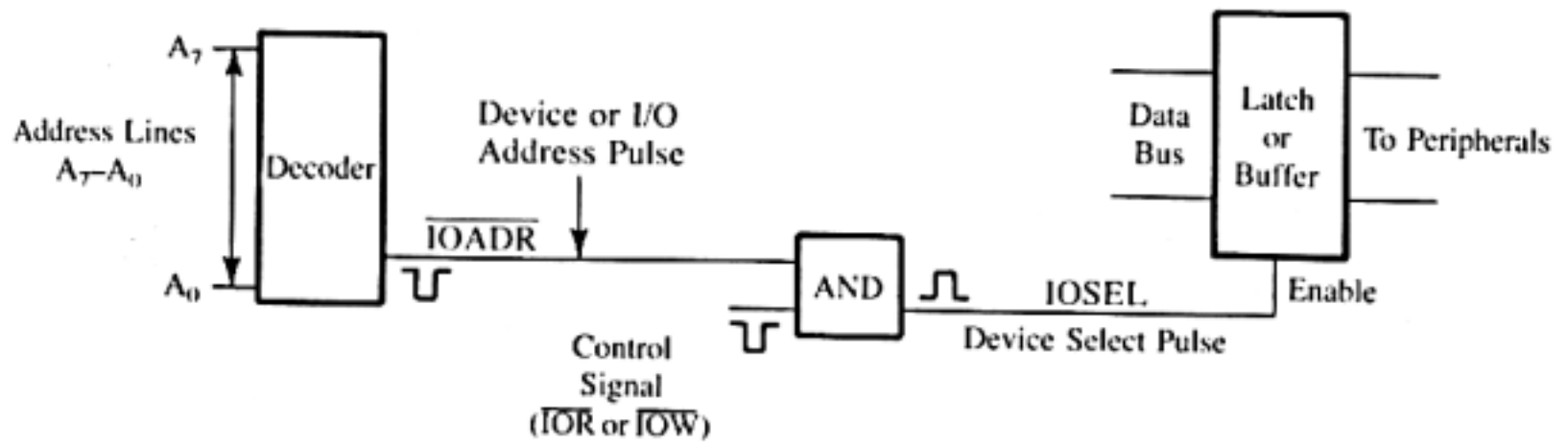
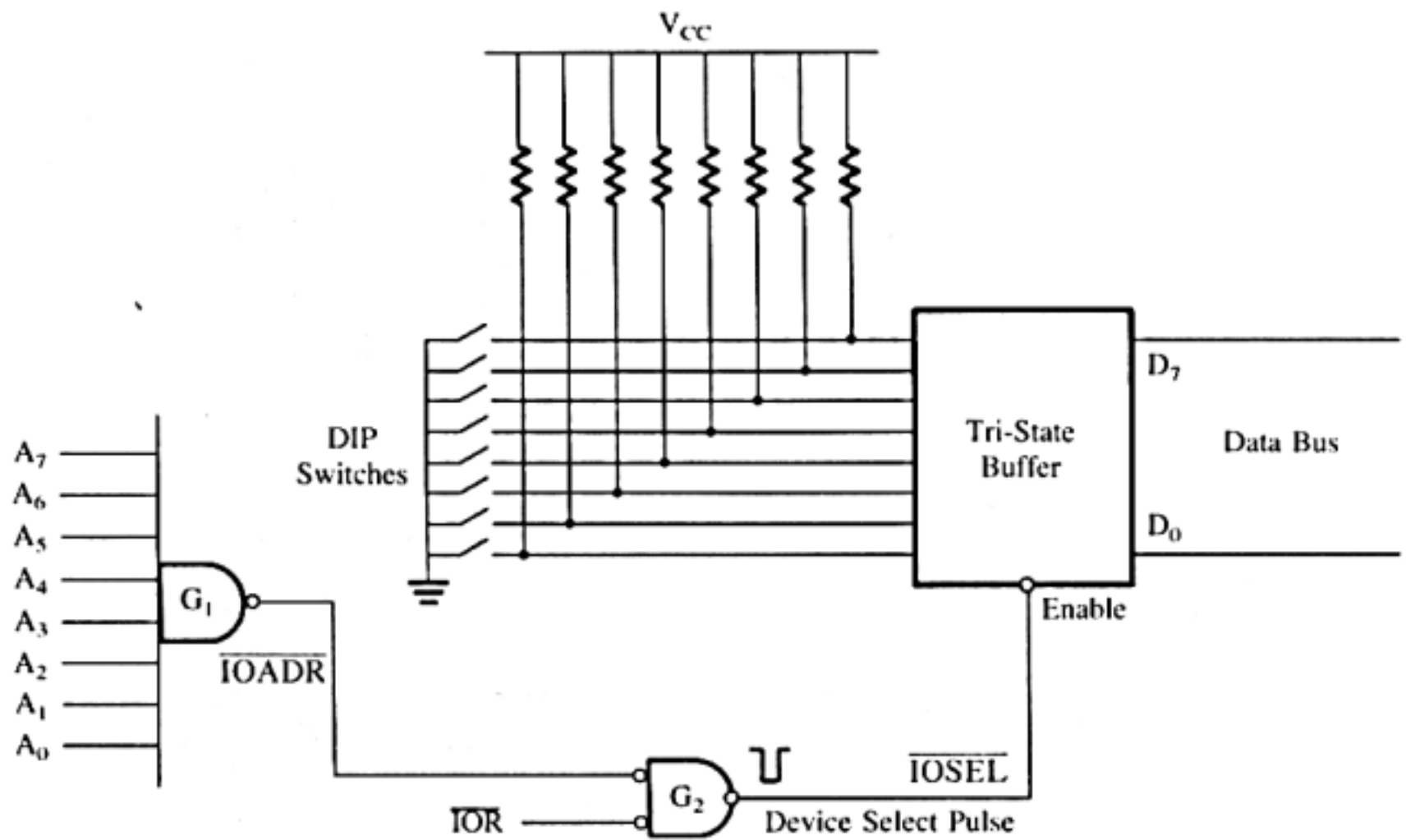


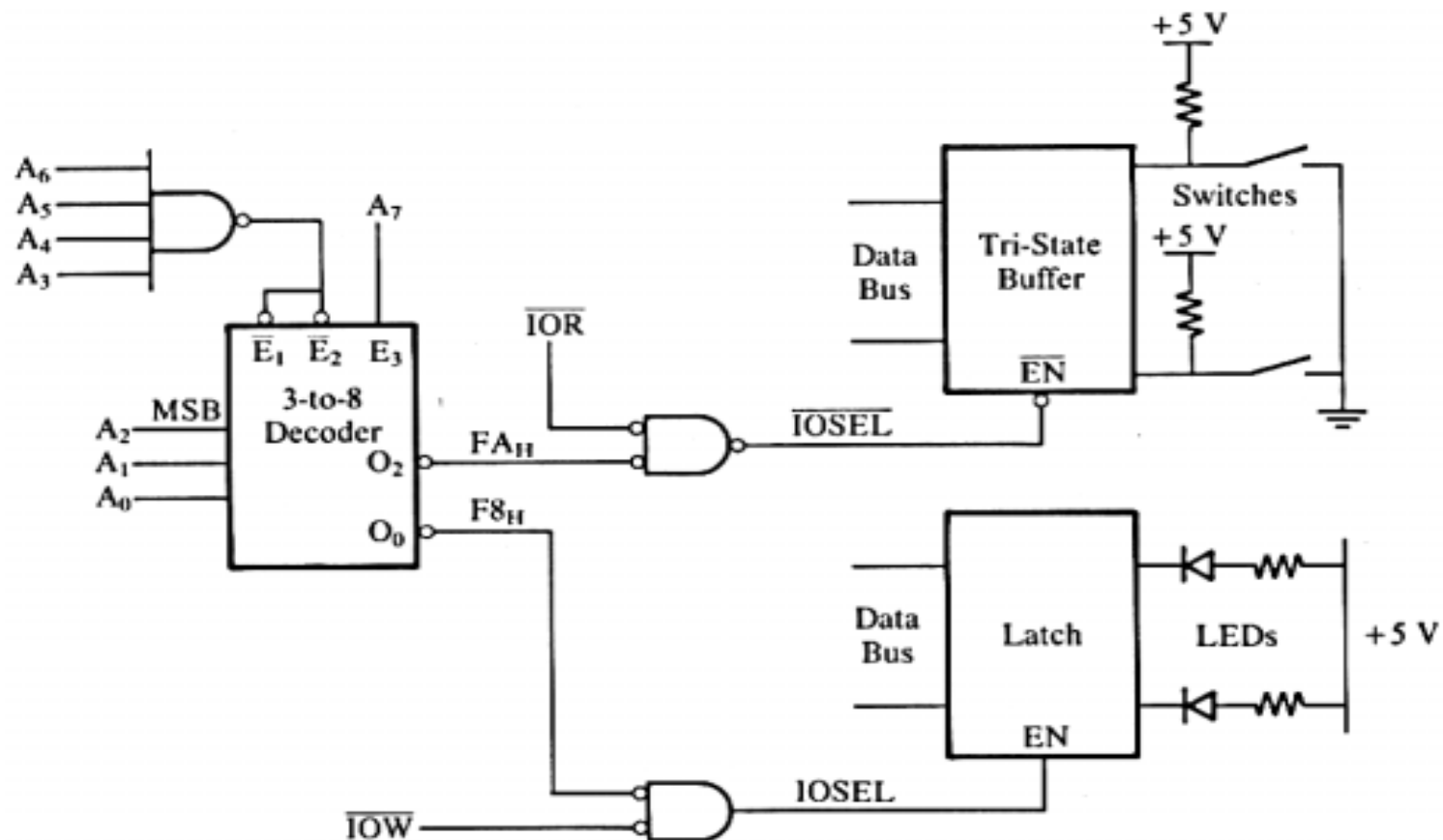
*Demultiplexed Bus

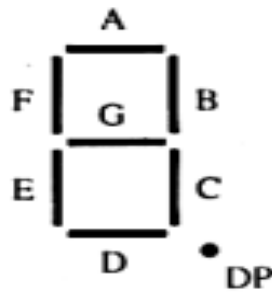
Timing for Execution of the Instruction: STA 8000H



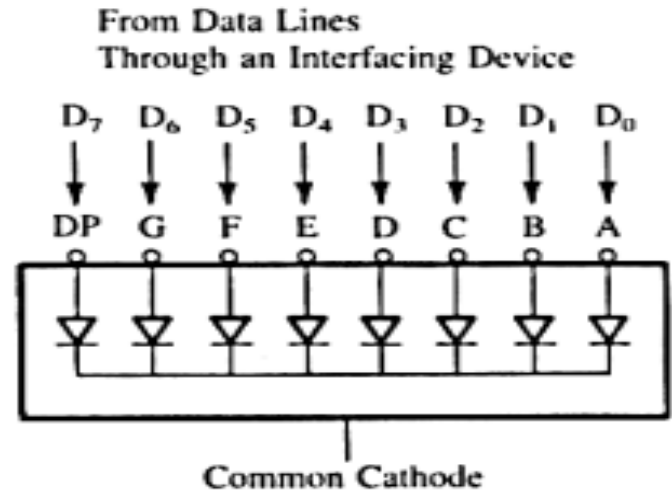
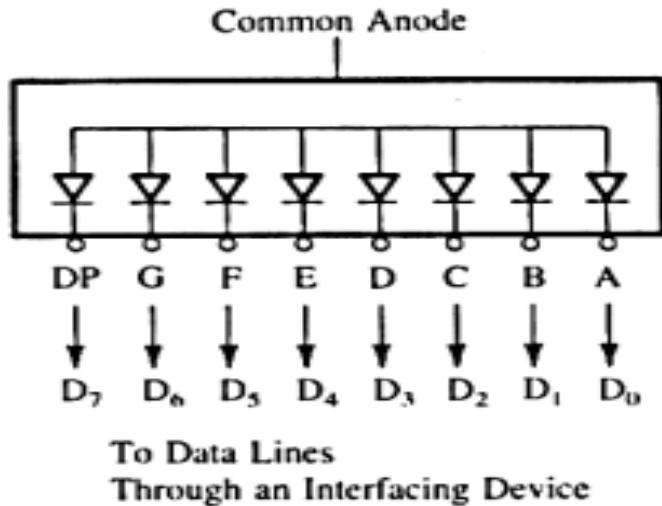


A ₇	A ₆	A ₅	A ₄	A ₃	A ₂	A ₁	A ₀	
1	1	1	1	1	0	0	0	= F8H
↓					↓			
Decoder Enable					Input			



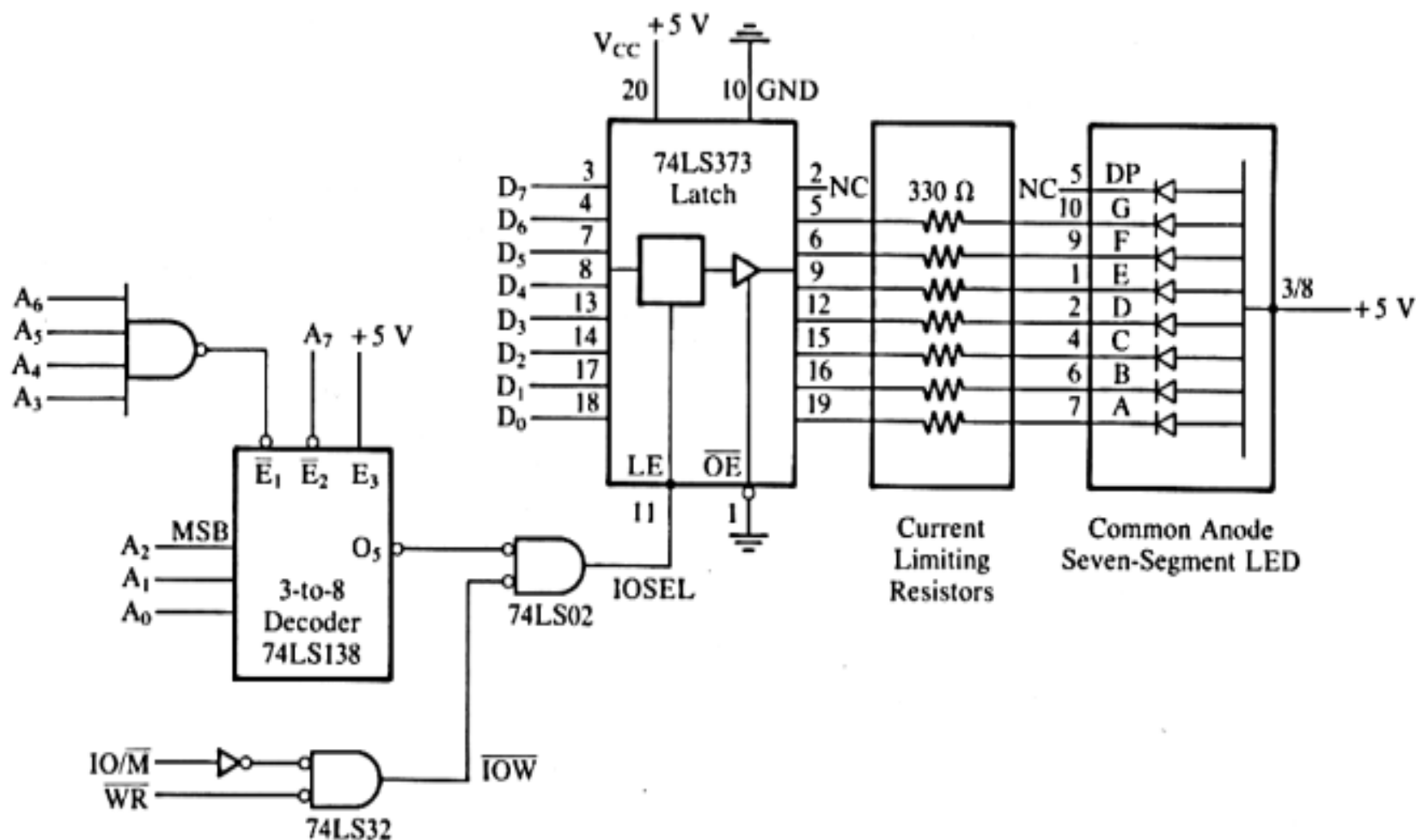


(a)



To display digit 7 (common anode)

Data Lines	D ₇	D ₆	D ₅	D ₄	D ₃	D ₂	D ₁	D ₀	
Bits	X	1	1	1	1	0	0	0	= 78H
Segments	NC	G	F	E	D	C	B	A	



```

MVI A,78H
OUT F5H
HLT

```

```

;Load seven-segment code in the accumulator
;Display digit 7 at port F5H
;End

```