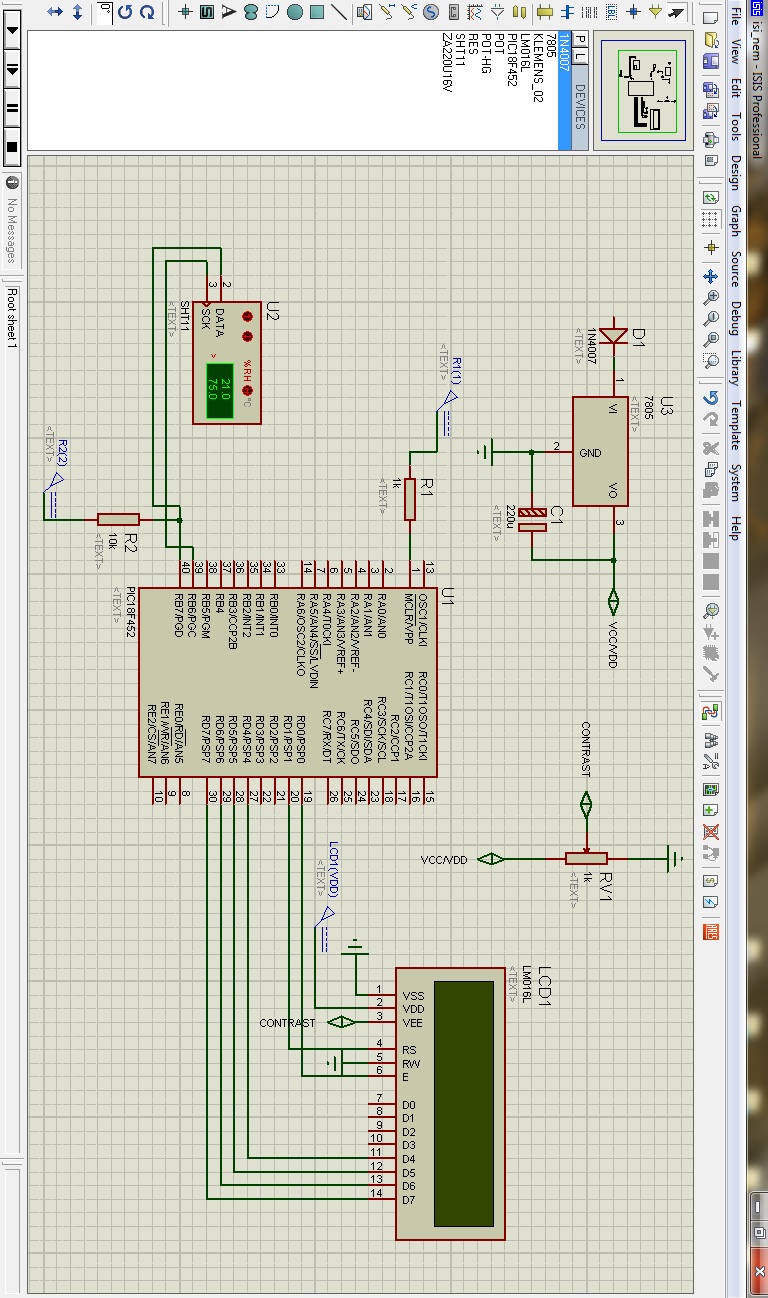
**EK-1 (Devrenin Proteus Çizimi)**



**EK-2 (Kaynak Kod)**

#include <18F452.h>

#include "SHT11.c"

#include "new\_lcd.c"

#FUSES NOWDT //No Watch Dog Timer

#FUSES WDT128 //Watch Dog Timer uses 1:128 Postscale

#FUSES XT //Crystal osc

#FUSES NOPROTECT //Code not protected from reading

#FUSES NOOSCSEN //Oscillator switching is disabled

#FUSES NOBROWNOUT //No brownout reset

#FUSES BORV20 //Brownout reset at 2.0V

#FUSES NOPUT //No Power Up Timer

#FUSES NOSTVREN //Stack full/underflow will not cause reset

#FUSES NODEBUG //No Debug mode for ICD

#FUSES NOLVP //No low voltage prgming

#FUSES NOWRT //Program memory not write protected

#FUSES NOWRTD //Data EEPROM not write protected

#FUSES NOWRTB //Boot block not write protected

#FUSES NOWRTC //configuration not registers write protected

#FUSES NOCPD //No EE protection

#FUSES NOCPB //No Boot Block code protection

#FUSES NOEBTR //Memory not protected from table reads

#FUSES NOEBTRB //Boot block not protected from table reads

#device adc=8

#use delay(clock=4000000)

#define sht\_data\_pin PIN\_b7

#define sht\_clk\_pin PIN\_b6

float temp,humid,sicaklik,nem;

void main()

{

setup\_adc\_ports(NO\_ANALOGS);

setup\_adc(ADC\_CLOCK\_DIV\_2);

setup\_psp(PSP\_DISABLED);

setup\_spi(SPI\_SS\_DISABLED);

setup\_wdt(WDT\_OFF);

setup\_timer\_0(RTCC\_INTERNAL);

setup\_timer\_1(T1\_DISABLED);

setup\_timer\_2(T2\_DISABLED,0,1);

setup\_ccp1(CCP\_OFF);

lcd\_init();

delay\_ms(100);

sht\_init();

delay\_ms(100);

while(1){

sht\_rd (temp, humid);

sicaklik=(float)temp;

nem=(float)humid;

lcd\_gotoxy(1,1);

printf(lcd\_putc,"SICAKLIK %2.2f C",sicaklik);

lcd\_gotoxy(1,2);

printf(lcd\_putc,"NEM %2.2f RH",nem);

delay\_ms(200);

}

}