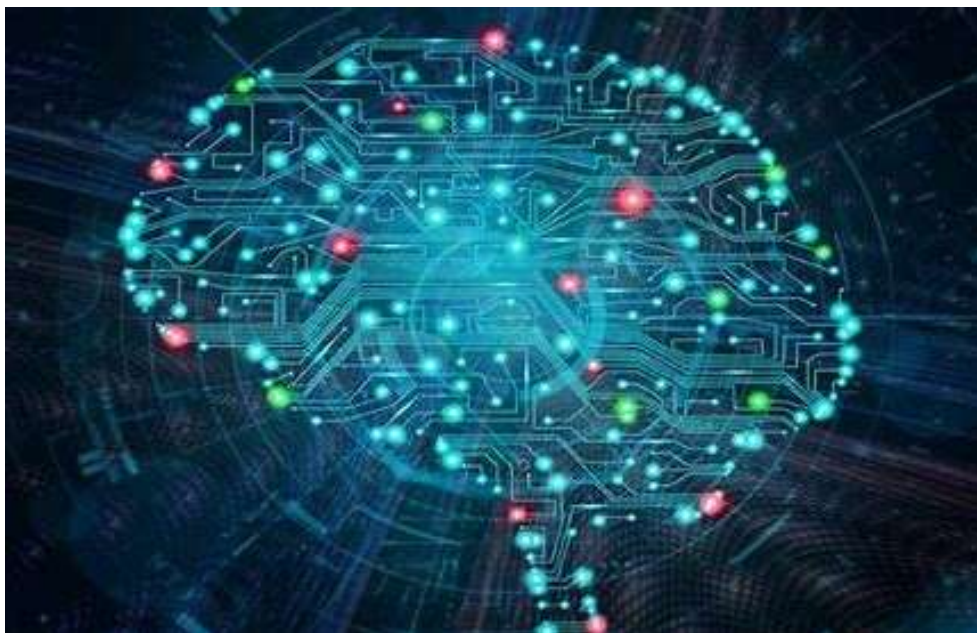




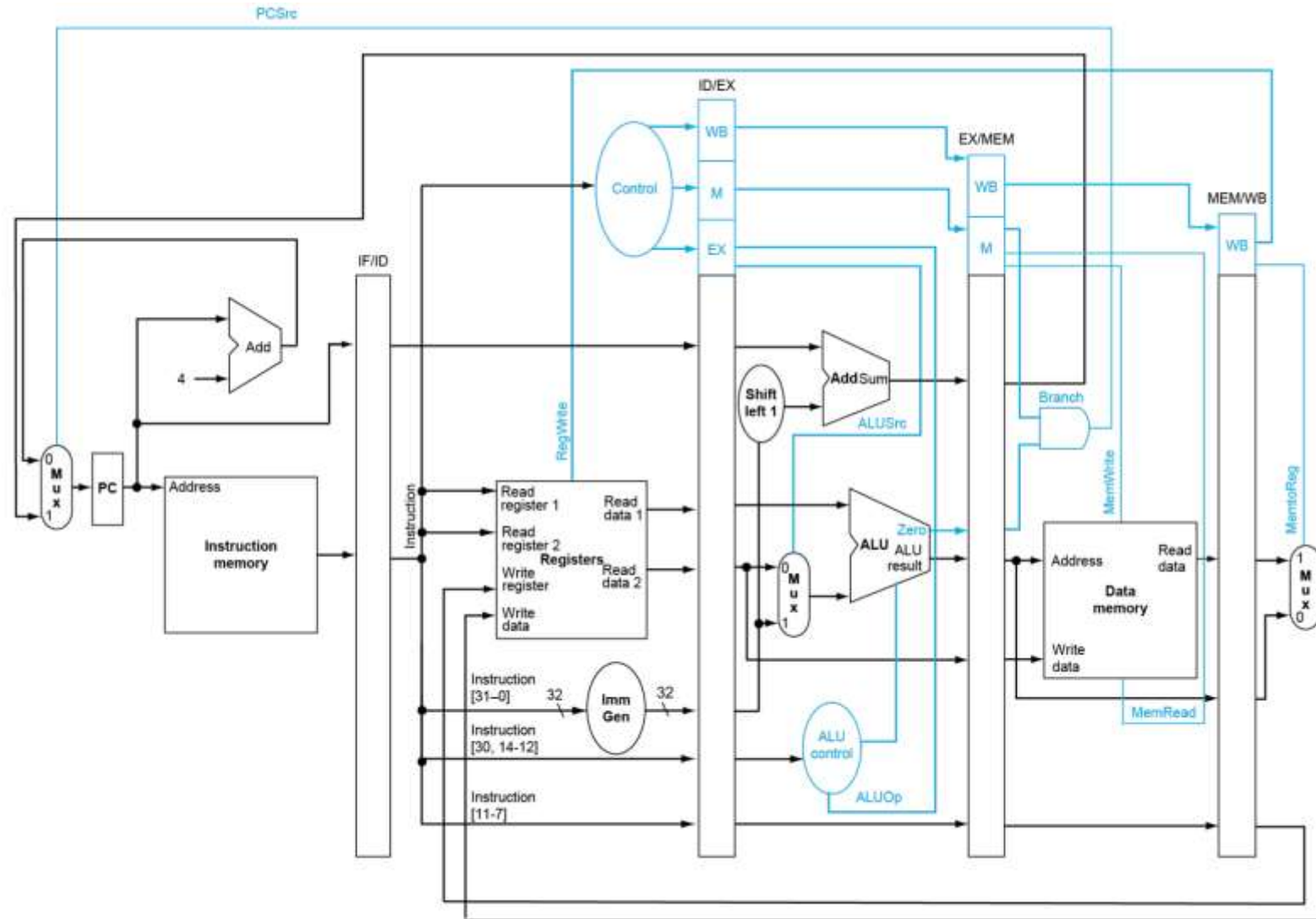
# RC5



**ECE3700J Intro to  
Computer Organization**

**2023.10.24**

# Pipeline



**Add x14, x5, x6**

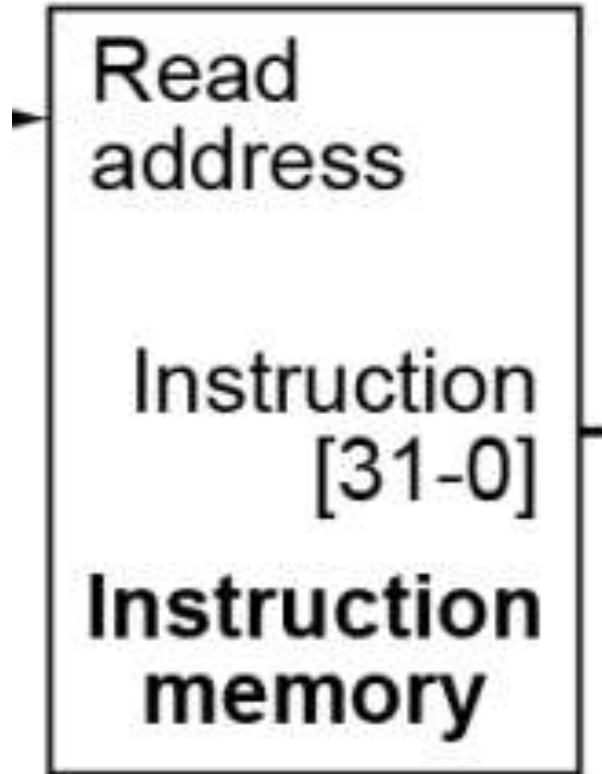
**Lw x13, 24(x20)**

**Add x12, x6, x7**

**Sub x11, x6, x7**

**Lw x10, 20(x20)**

# Pipeline



**Add x14, x5, x6**

**Lw x13, 24(x20)**

**Add x12, x6, x7**

**Sub x11, x6, x7**

**Lw x10, 20(x20)**

# Pipeline

**Add x14, x5, x6**

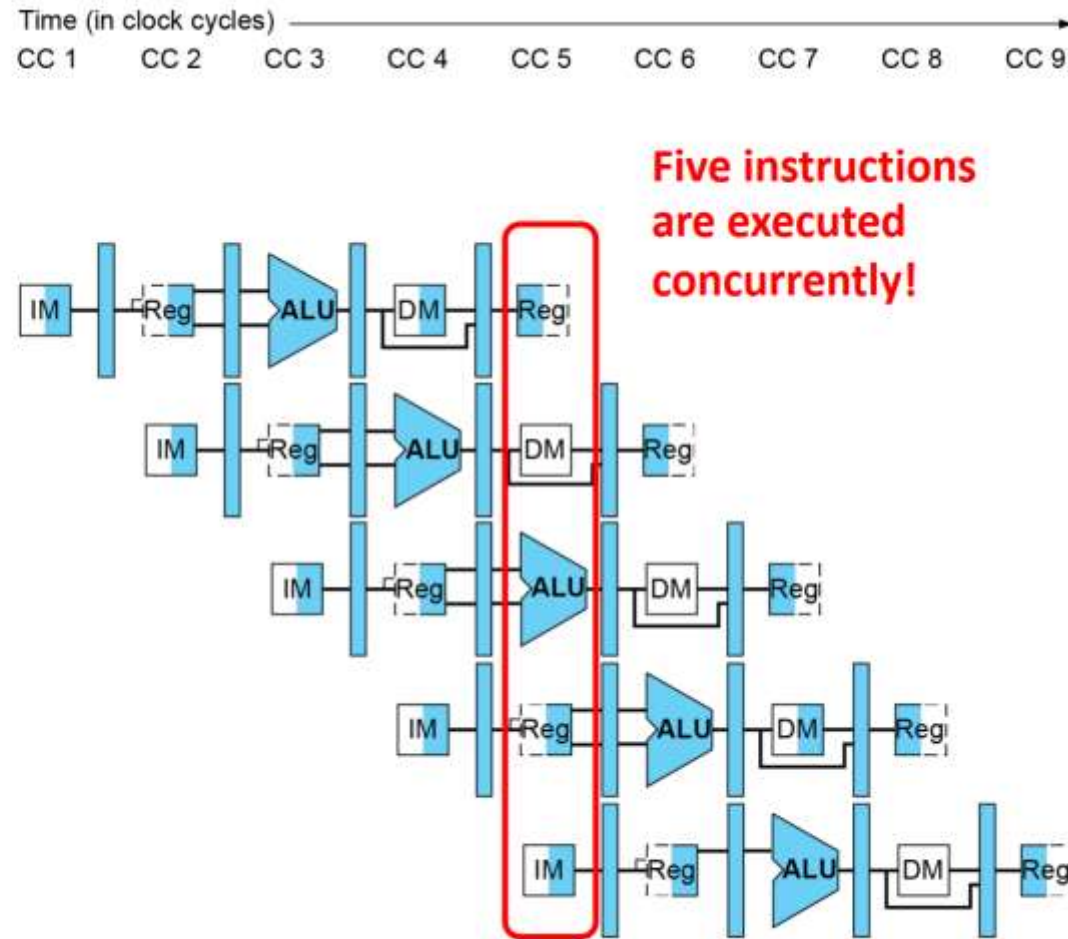
**Lw x13, 24(x20)**

**Add x12, x6, x7**

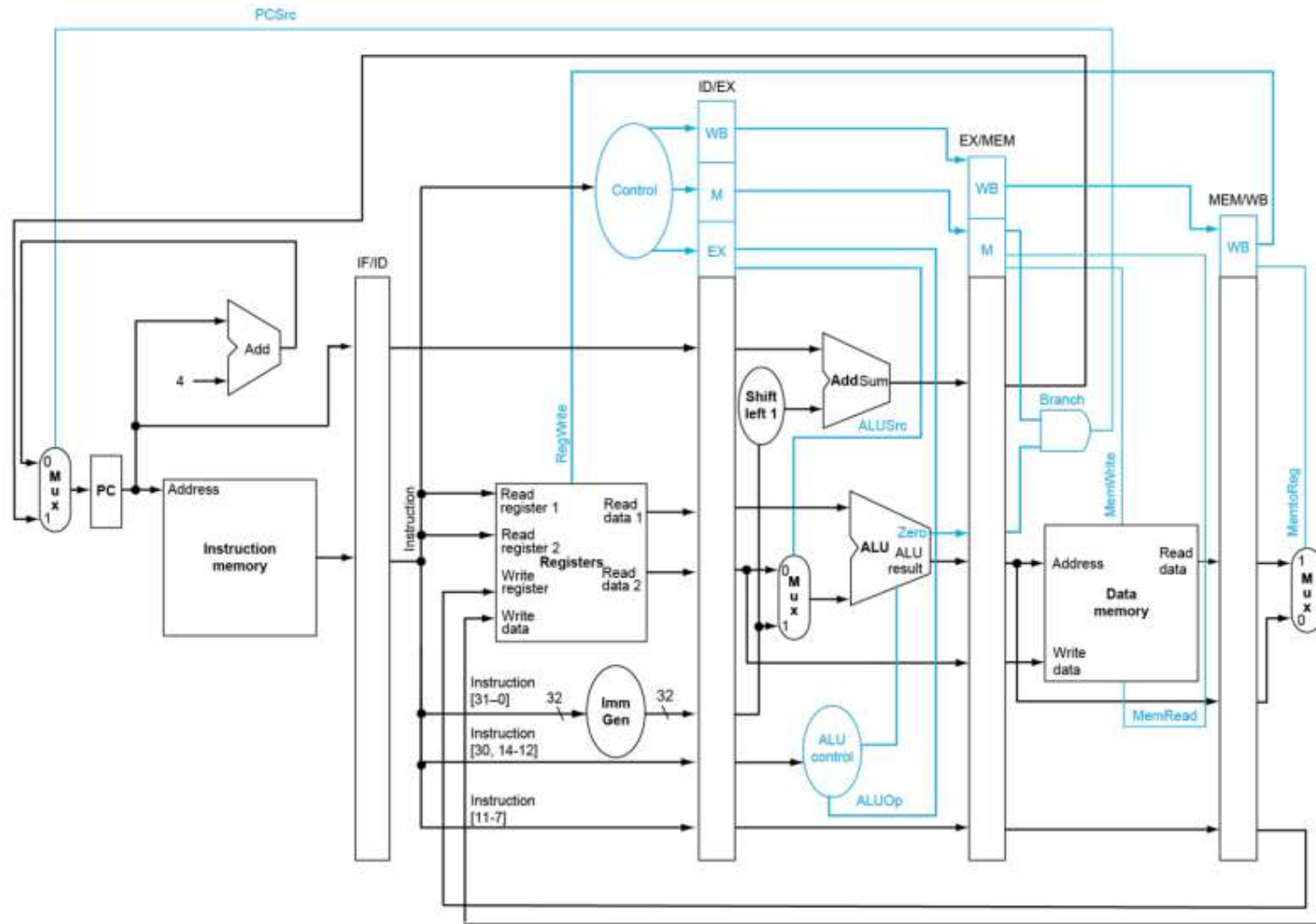
**Sub x11, x6, x7**

**Lw x10, 20(x20)**

Program  
execution  
order  
(in instructions)



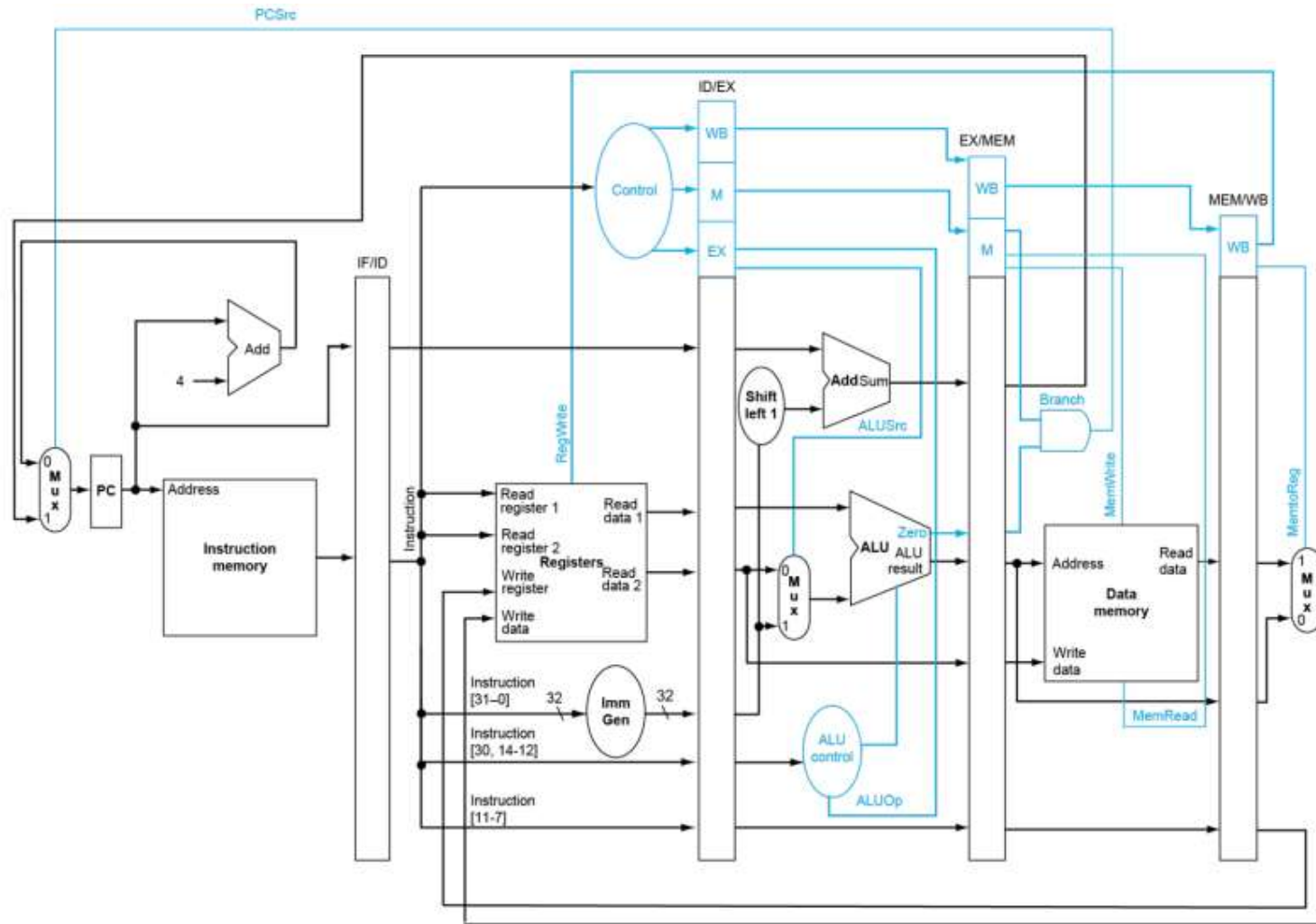
# Pipeline



PC = 0x00

Add x14, x5, x6

# Pipeline

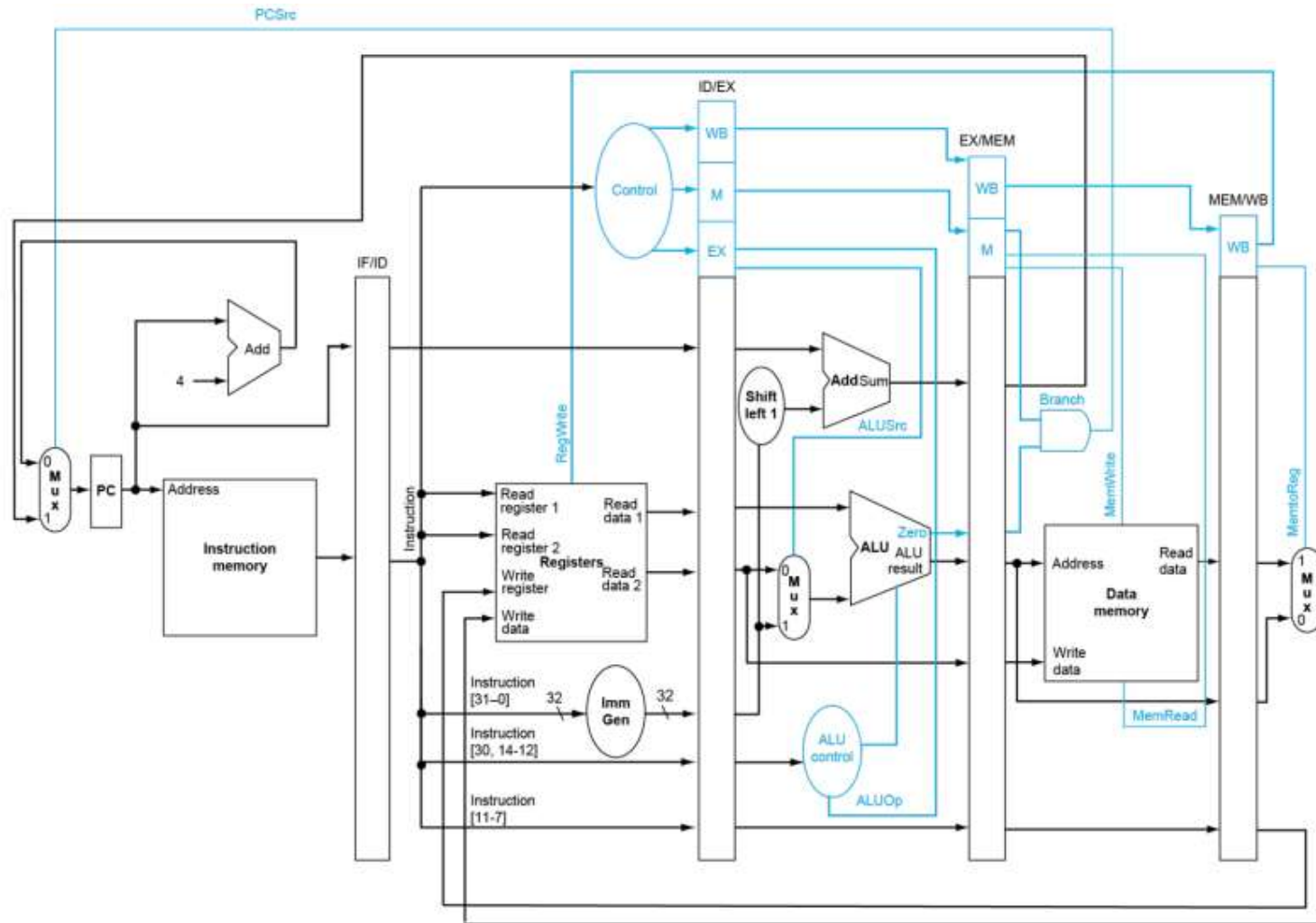


PC = 0x04

Lw x13, 24(x20)    Add x14, x5, x6



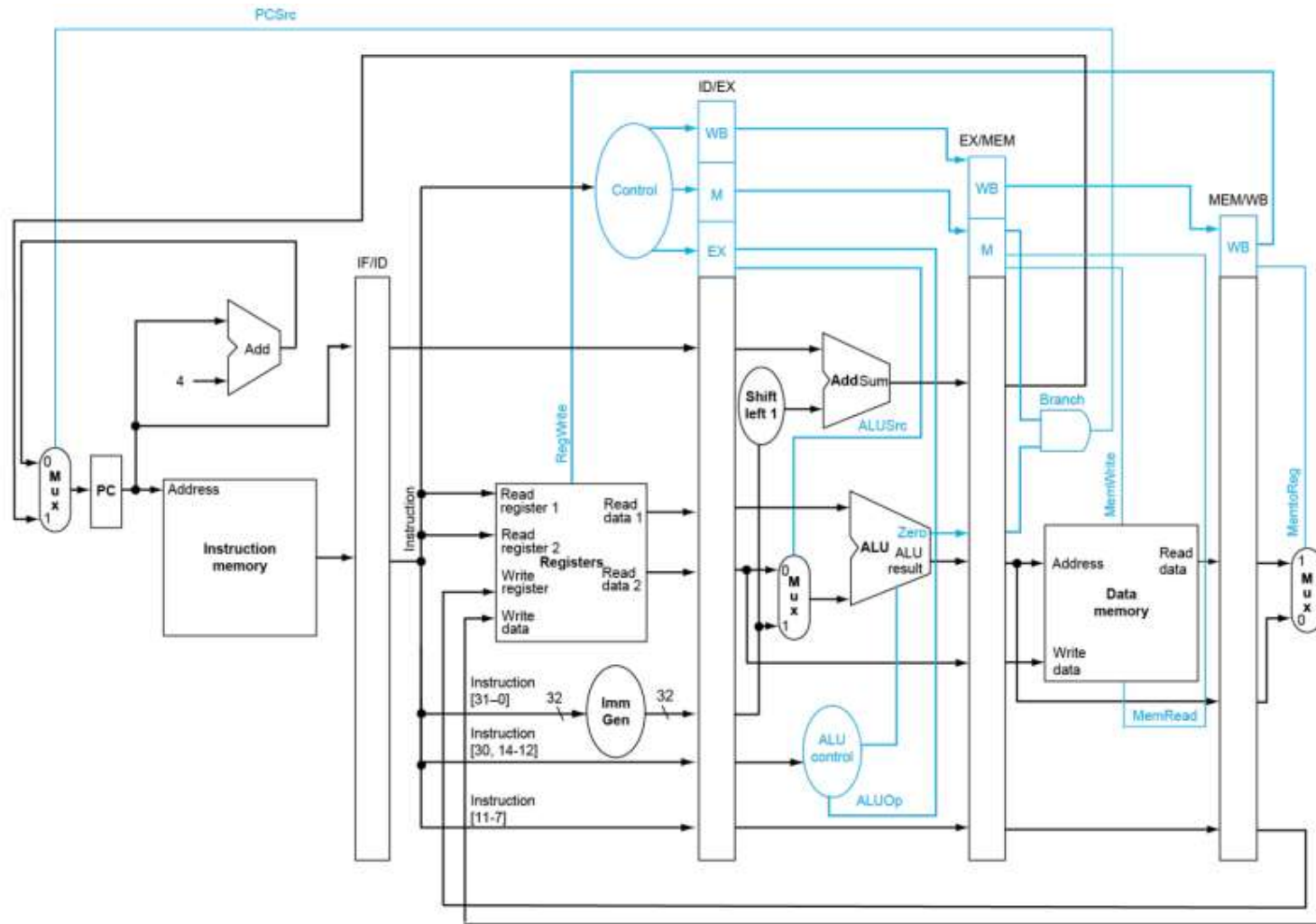
# Pipeline



PC = 0x08

Add x12, x6, x7    Lw x13, 24(x20)    Add x14, x5, x6

# Pipeline

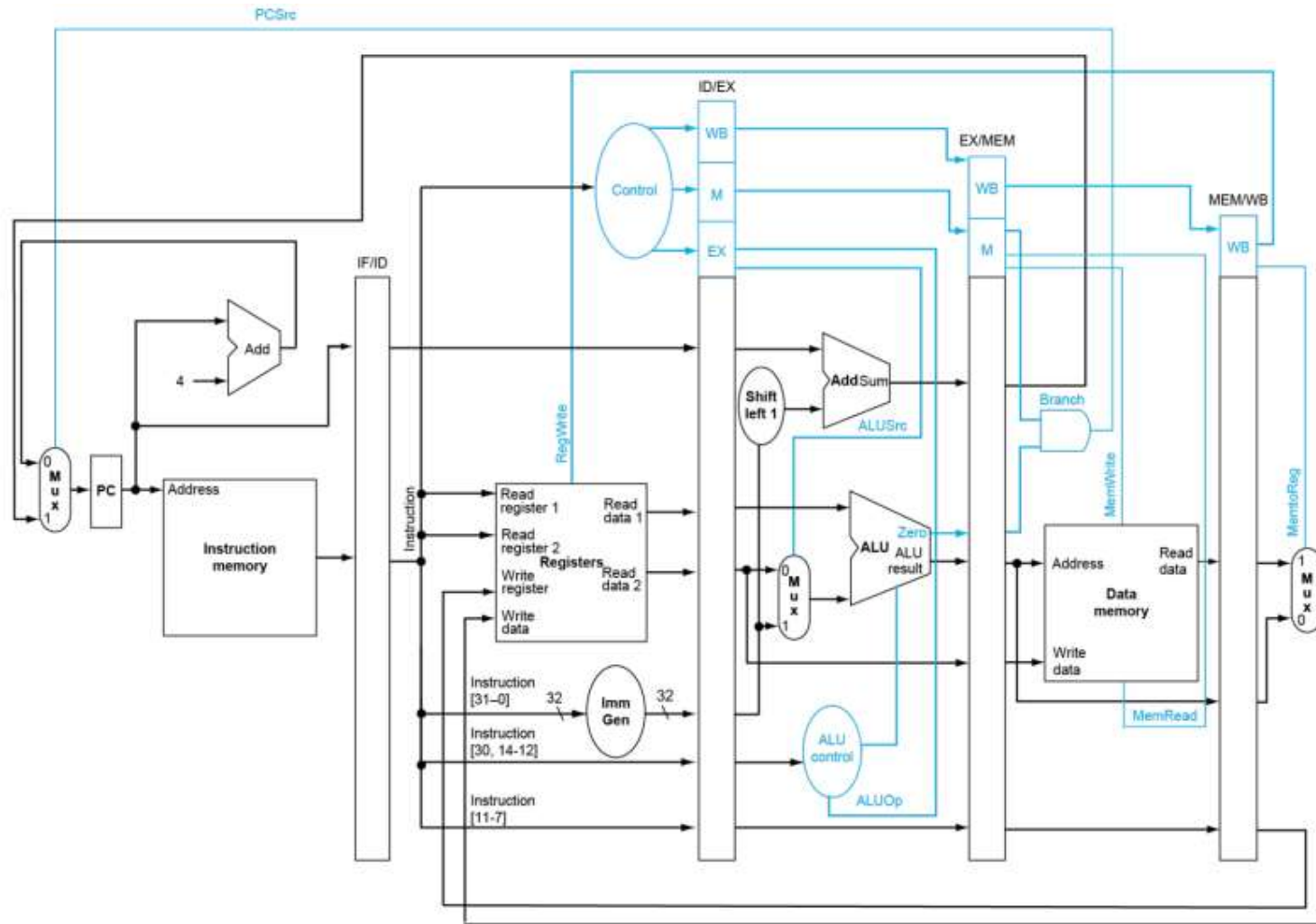


PC = 0x0C

Sub x11, x6, x7    Add x12, x6, x7    Lw x13, 24(x20)    Add x14, x5, x6



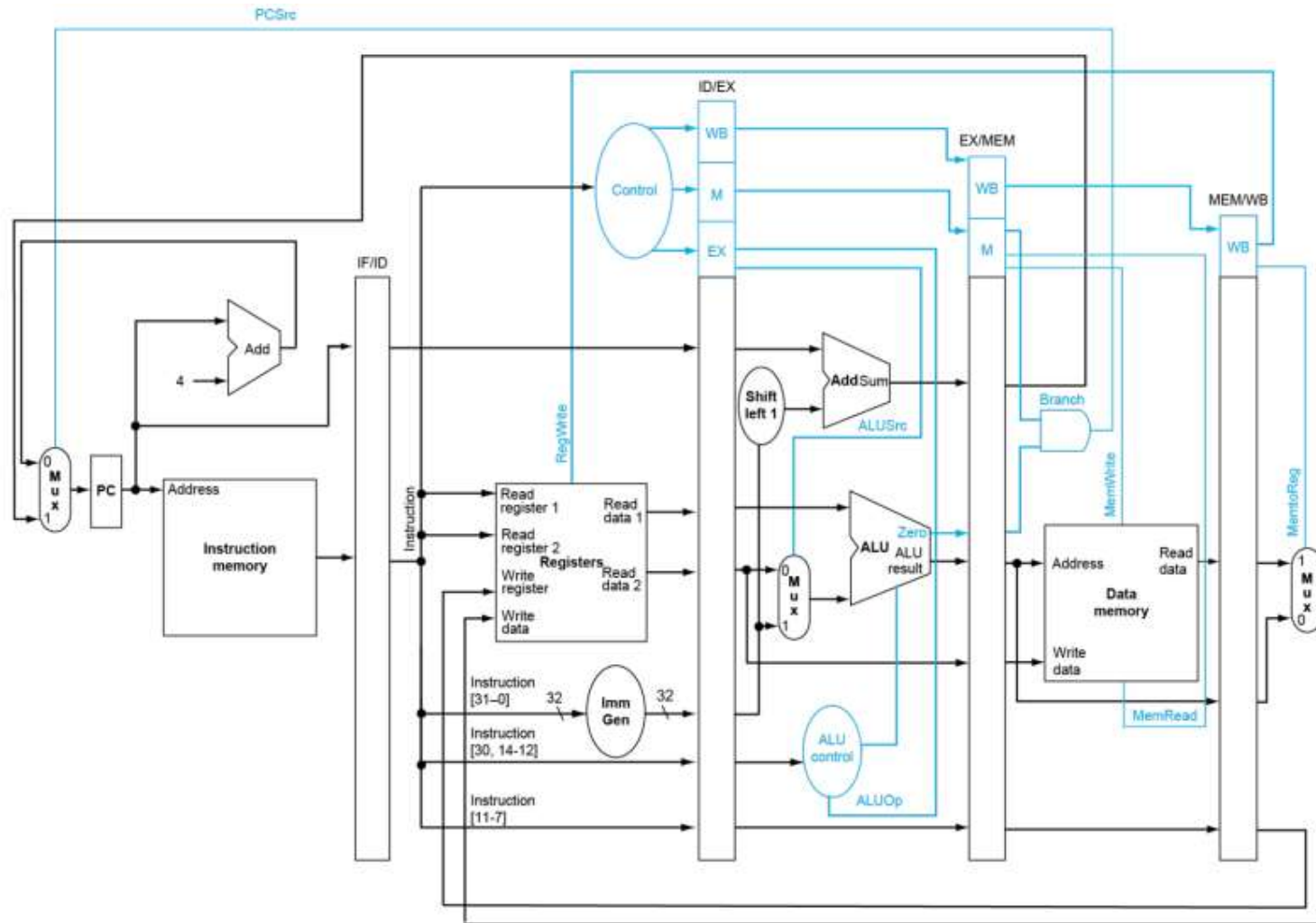
# Pipeline



PC = 0x10

Lw x10, 20(x20)    Sub x11, x6, x7    Add x12, x6, x7    Lw x13, 24(x20)    Add x14, x5, x6

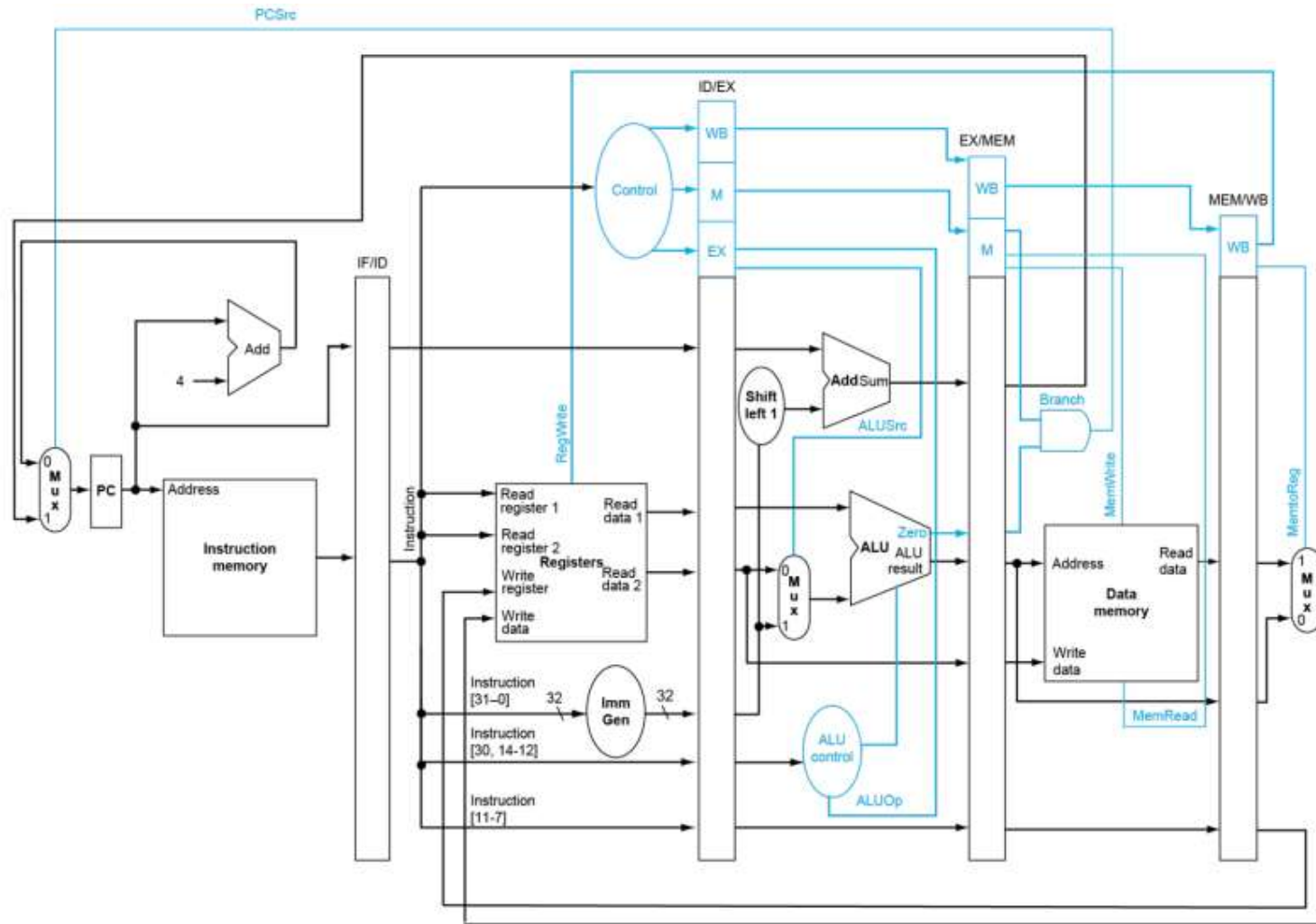
# Pipeline



PC = 0x14

Lw x10, 20(x20)    Sub x11, x6, x7    Add x12, x6, x7    Lw x13, 24(x20)

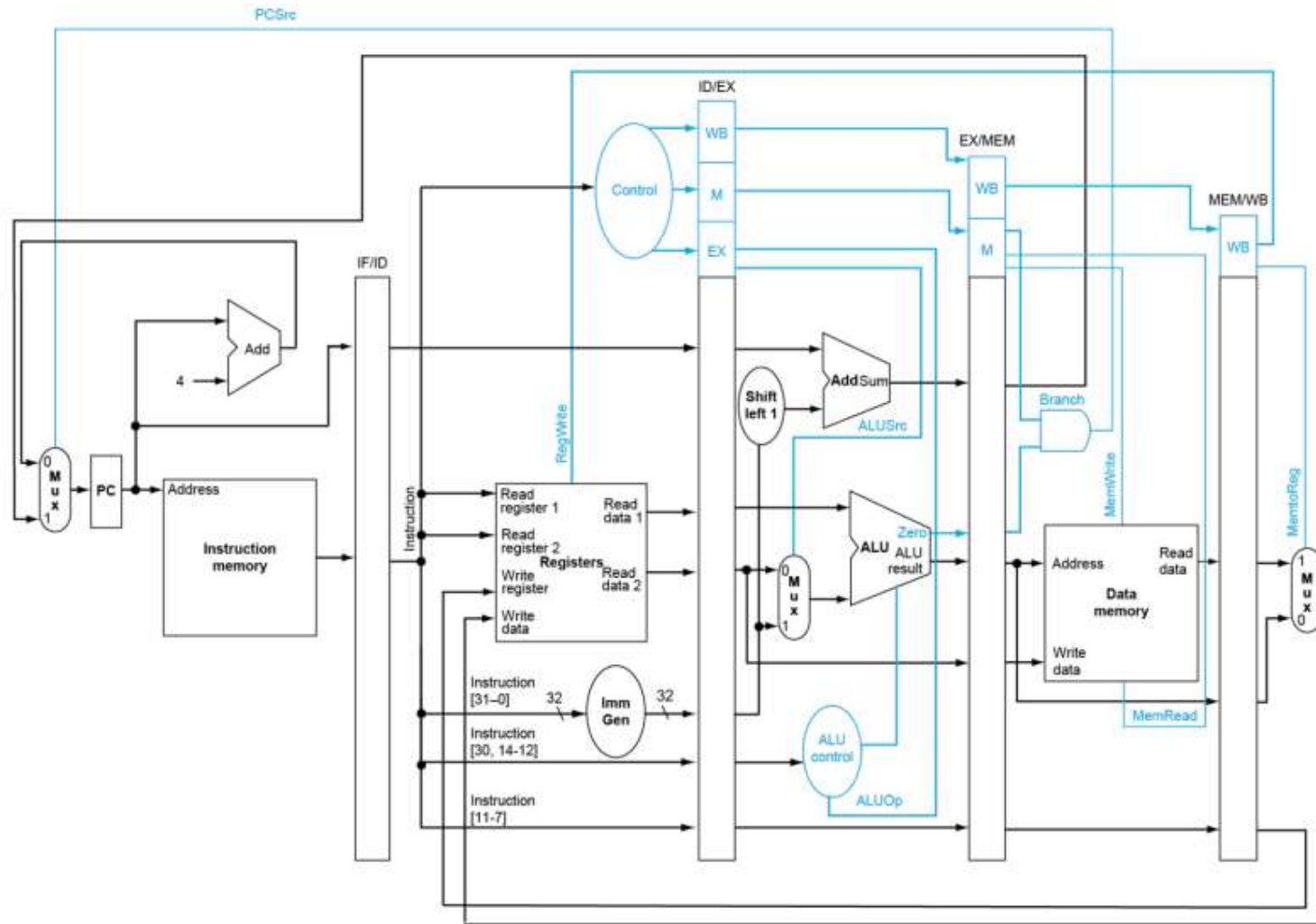
# Pipeline



PC = 0x18

Lw x10, 20(x20) Sub x11, x6, x7 Add x12, x6, x7

# Pipeline

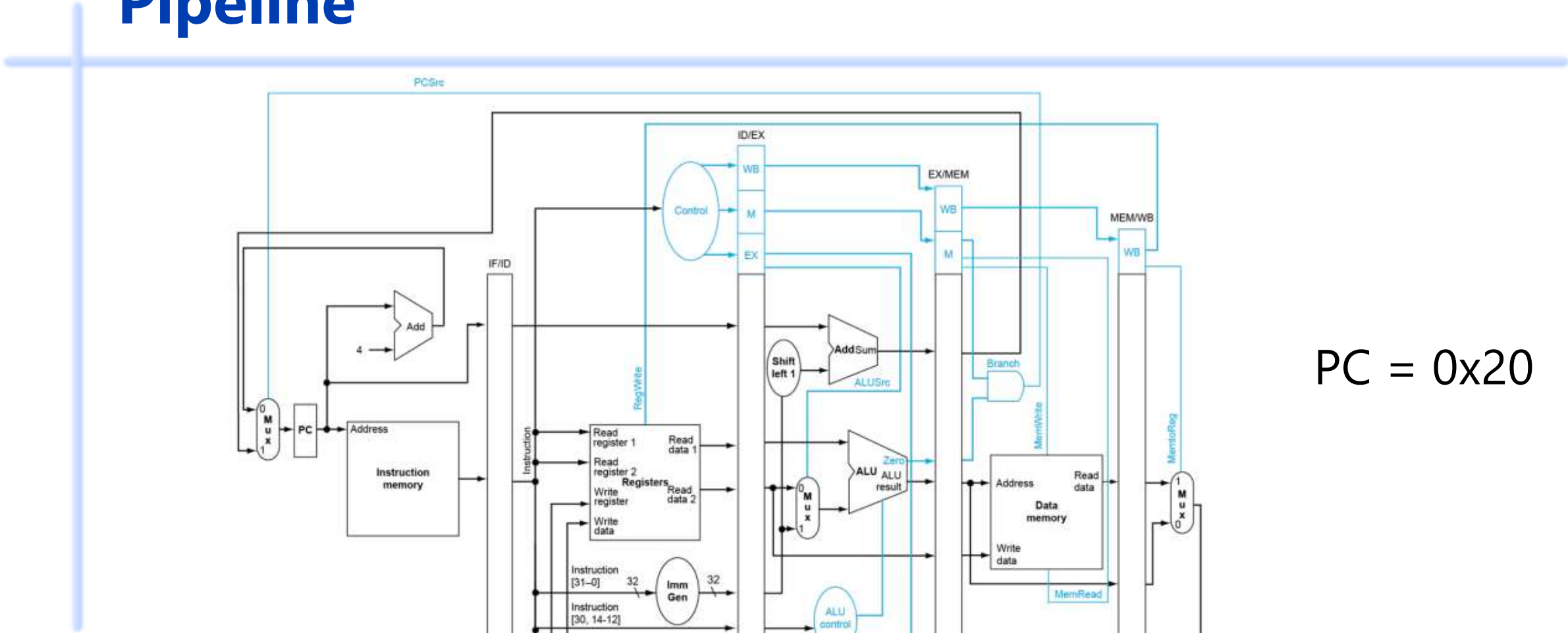


PC = 0x1C

Lw x10, 20(x20) Sub x11, x6, x7

# Pipeline

PC = 0x20



# Pipeline

PC = 0x20

# Pipeline

PC = 0x20



**That's all for  
today's RC**