

CURRICULUM VITAE



Personal:

Name : **Dr.D.Nirmal M.E, Ph.D.,**

Date of Birth : 20.01.1984

Sex : Male

Nationality : INDIAN

Office Address : Associate Professor, Department of Electronics and Communication Engineering Karunya university.

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Academic History:

Ph.D Faculty of Information and Communication Engineering, Highly Commended.

(specialisation in Nano Device Modelling)
Anna University, Chennai- 2012

Mater of Engineering

(Specialization in VLSI Design)
Karunya University, India- 2007

Bachelor of Engineering

(Specialisation in Electrical and Electronics)
Anna University, Chennai-2005

Post Graduate Diploma in Higher Education

Indira Gandhi National Open University, India- 2013

Employment Record:

Type of Employment- **Associate Professor**

Place-Karunya University(Electronics and communication Engineering Department)

Period- July 2013- Till Date.

Type of Employment- **Assistant Professor**

Place-Karunya University(Electronics and communication Engineering Department)

Period- June 2009- June 2013

Type of Employment- **Lecturer**

Place-Karunya University(Electronics and communication Engineering Department)

Period- June 2007- May2009

Type of Employment- **Teaching Research Associate**

Place-Karunya University(Electronics and communication Engineering Department)

Period- June 2006 - May 2007

Research Experience:

Position- **Teaching and Research.**

Place – VLSI center of excellence, Karunya University.

Period – July 2007 to Till Date.

Position- **Teaching Research Associate.**

Place – VLSI center of excellence, Karunya University.

Period – June 2006 to June 2007.

Administrative Experience:

Position- **Department Library In-charge.**

Place- Electronics and communication engineering, Karunya university.

Period- June 2007 to 2013.

Campus Life Experience:

Position-**Joint Chief Warden.**

Place- Mens Hostel, Karunya University.

Period- January 2013- Till Date

Position-**Warden.**

Place- Mens Hostel, Karunya University.

Period- June 2007 to December 2010

Experience- 3 years and 6 months.

Achievements in Campus Life:

Acted as a mentor for many Students in Academics and Professional Life and they are placed in very high position in the society.

De addition excises been done to many students for life transformation in the University Campus.

Projects Handled:

Bachelor of Engineering-19(Electronics and Communication Engineering)

Master of Engineering-18(VLSI Design)
1(Nanotechnology)

Honors & Awards:

IEEE Electron Device Coimbatore Chapter Chair.

Shir.P.K.Das Memorial **Best Faculty Award** in the Year 2013 by Nehru College of Educational and Charitable Trust.

Best Research Paper Award with high impact factor from Karunya University in the year 2012.

Received **Shiksha Rattan Puraskar Award** 2012 from Indian International friend ship Society.

Distinction in Master of Engineering (VLSI DESIGN)

Professional Society Activities:

- Life Member of ISTE(Indian Society for Technical Education)
- Member of IEEE (Institute of Electrical and Electronics Engineering)
- Member of VSI (VLSI Society Of India)
- Life Member of SSI(Semiconductor Society of India)
- Member of IETE (The Institution of Electronic and Telecommunication Engineers)
- Life Member of IAENG(International Association for engineers)

- Member of IACSIT(International Association Of Computer Science And Information Technology)
- Member of ACCENT (Association of Computer Communication Education for National Triumph Social and Welfare Society)

Professional Society Achievements:

- IETE student forum is inaugurated in the Karunya University Campus for the benefit of the Students under my Leadership.
- IETE student forum of Karunya University is very active, having organized several events including Life changing campus life program under my leadership for the benefit of the Students on 27 August, 2012 at Karunya University campus.

Subjects Handled:

- VLSI Design.
- CMOS VLSI Technology.
- Nanoelectronics.
- Solid State Device Modeling.
- Microelectronics.
- Solid State Electronics.
- Microwave and Optical Engineering.

Research Interests:

MOS insulators: Study of MOS insulators, including high-k dielectrics

Novel MOS devices for ULSI applications: Transistors with high-k dielectrics

Synthetic Experience:

Experience with Scientific Equipments:

Techniques used extensively

- X-Ray Diffraction
- Impedance Spectroscopy
- SEM
- Raman spectroscopy
- UV Visible.

Material worked with

-Zirconium di oxide.

Technology Tools:

-TCAD(Technology computer aided design simulation tool)
-sdevice
-inspect
-tecplot
-Mentor graphics
-Tanner tool
-Synopsys.

Computer Language Skills:

-VHDL
-Verilog
-C++
-C Language

Research Experience:

Review Assignments for Scientific Journals:

- 1) IEEE Electron Device Letters.
- 2) International journal of Microelectronics(MJS)Elsevier.
- 3) International Journal Of Microelectronics Reliability Elsevier.
- 4) International journal of Superlattices and Microstructures Elsevier.
- 5) International Journal of Security (IJS).
- 6) International journal computation intelligence and information security(IJCIIS).

Editorial Member for Journal:

Editor- Microelectronics Journal(Elsevier)

Editorial Board member of Journal of NanoScience , NanoEngineering & Applications (JoNSNEA).

Editorial Board member of Journal of Semiconductor Devices and Circuits.

Editorial Board member of Journal of Solid State Electronics and Devices.

Editorial Board Member of International Journal of Advanced Computer Research(IJACR)

Editorial Board Member of International Journal of Scientific Engineering and Technology(IJSET)

International Advisory committee:

- 1) **4th IEEE International Advanced Computing Conference - IACC 2014.** It will be held during Feb 21-22, 2014 at ITM University Gurgaon- Technical program committee member- <http://iacc2014.com/technical-program-committee.php>
- 2) **2015 International Conference on Nanoscience and Nanotechnology Toronto** Canada during 17-18 2015. International Advisory Committee- <http://www.waset.org/conference/2015/06/toronto/ICNN/committees>
- 3) **First International Conference on Advance Computing and Communication, ICACC-2013** will be held during March 16-17 2013, in Ranchi, India- Technical programm committee member- <http://www.theaccents.org/icacc-conference/prog.html>
- 4) **International Conference on Electronics and Communication Systems.** It will be held 13 th and 14th of February 2014 in Karpagam college of Engineering, India-Advisory committee member- <http://www.icecs2014.com/5.html>.

University Collaboration:

Collaborative research work is going on with Mumbai University Nanoscience

and Nanotechnology Centre from January 2013.

International/National Universities, College Visited For Research

Activity:

- Nanyang Technical University, Singapore.
- IIT Bombay, India.
- IIT Chennai, India.
- Manipal University, India.
- Anna University, India.
- Indian Institute of Science, Bangalore, India.
- SRM University, India.
- Bharathiar University, India.
- P.S.G Tech, Coimbatore, India.
- S.K.P Engineering college, Chennai, India.
- Noorl Islam University, India.

Research Publications in International Journals:

- Binola k Jebalin, Shobha Rekh, Prajoon, Godwin raj, N.Mohankumar and **D.Nirmal** “Unique model of polarization engineered AlGaIn/GaN Based HEMTs for high power applications” Superlattices and Microstructures 78(2015)210-223.(**Impact factor: 1.979**)
- **D.Nirmal**, P.VijaKumar, K.shruthi, Binola K jebalin and Mohan Kumar “ A review of Nanoscale channel and Gate Engineered FINFETS for VLSI mixed signal applications using Zirconium – di- oxide dielectrics” Journal of Engineering sciences and Technology Review Volume7(2) 2014, 119-124.
- I.Flavia Princess Nesamani, V.Lakshmi Prabha,Aswathy Paul and **Nirmal .D**, Synthesis and Dielectric Studies of Monoclinic Nanosized Zirconia”, International journal of Advances in Condensed Matter Physics. (**Hindawi**)Volume 2014. <http://dx.doi.org/10.1155/2014/828492> (**Impact Factor: 1.175**).

- **Nirmal .D**, Vijayakumar .P, Shruthi and Mohan Kumar .N “Nanoscale Channel Engineered Double Gate MOSFET for Mixed Signal Applications using High-k Dielectric”, International journal of circuit theory and applications. (**Wiley**)Volume 41, Issue 6, pp 608-618,2013. (**Impact Factor: 1.759**).
- **Nirmal .D**, Vijayakumar .P, Patrick Chella Samuel , Binola k Jebalin, and Mohan Kumar .N “Subthreshold analysis of nanoscale FinFETs for ultra low power application using high-k materials”, International Journal of Electronics.(**Taylor and Francis**) Volume 100, Issue 6,pp 803-817,2013. (**Impact factor: 0.440**).
- **Nirmal .D**, Vijayakumar .P, Divya .D , Binola k Jebalin, and Mohan Kumar .N “Subthreshold Performance of Gate Engineered FinFET Devices and circuit with High-k Dielectrics”, Microelectronics Reliability(**Elsevier**) 53 (2013) 499–504. (**Impact factor: 1.167**).
- Surya A , **Nirmal D** et al “CMOS Based Instrumentation Amplifier For Neural Recording System” IJCIC Volume5, Issue 1,pp 51-55,2013.
- **Nirmal. D** , Nalini .B and Cyril Robinson Azariah “ Fabrication and Characteristics of Flexible Thin Film Depletion Mode Field Effect Transistor (FET) using High-κ Dielectric Nano Zirconia” International Journal of Emerging Trends in Engineering and Development, Issue 3, Vol.2 pp.295-299. (March 2013).
- **Nirmal .D** and Vijayakumar .P “Fin Field Effect Transistors Performance in Analog and RF for High-k Dielectrics” Defence Science Journal, Vol. 61, No. 3, pp. 235-240, May 2011. (**Impact factor:0.304**)
- **Nirmal .D**, Shruti .K, Divya Mary Thomas, Patrick Chella Samuel, Vijayakumar .P and Mohan Kumar .N “Impact of Channel Engineering on FINFETs using High-k dielectrics” International Journal of Micro and Nano Electronics, Circuits and Systems, 3(1), pp. 7-11, 2011.
- **Nirmal .D**, Nalini .B and Vijayakumar .P “Nano sized High K Dielectric Material for FINFET”, Integrated Ferroelectrics(**Taylor and Francis**), Volume 121, Issue 1, pp. 31 – 35, 2010.(**Impact Factor: 0.264**)

- **Nirmal .D**, Vijayakumar .P and Sam Jebaraj “NAND Gate Using FINFET for Nanoscale technology”, International Journal of Engineering Science and Technology Vol.2 (5), pp. 1351-1358, 2010.
- **Nirmal .D** and Vijayakumar .P “Gate Engineering on the Analog Performance of DM-DG MOSFETs with High K Dielectrics”, International Journal of Advanced Science and Technology Vol. 25, pp. 1-6, December, 2010.

Research Publications in International / National conferences:

International conference:

- J. Ajayan, D. Nirmal, D. Sivaranjini, S. Sivasankari and M. Manikandan " High Performance Low Leakage power Full adder Circuit Design Using Rate Sensing Keeper" Proceedings of International Conference on Electronics and Communication Systems-ICECS-2014, Karpagam College of Engineering. Coimbatore, Tamilnadu, Feb.13,14. pp.55-59.
- J. Ajayan, D. Nirmal, S. Sivasankari, D. Sivaranjini and M. Manikandan " High speed Low Power Full adder Circuit Design Using Current Comparison Domino" proceedings of Second International Conference on Device Circuits and Systems-ICDCS-2014. Karunya University, Coimbatore, Tamilnadu, pp.10-14, March-6-8, 2014.
- Prajoon.P, **D.Nirmal**, Aldona Mathew, Abin George“Enhancement Mode GaN Based HEMT UsingPolarization Engineering Technique” proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIECS’14, Karpagam College of Enginnering,Coimbatore,Tamil Nadu, March 2014 pp 208-211.
- A.Persiya, **D.Nirmal**, C.T.Sruthi “Investigation on the optical and structural properties of Thermally Evaporated tin selenide thin films for Phase Change Memory application” proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIECS’14, Karpagam College of Enginnering,Coimbatore,Tamil Nadu, March 2014 pp 482-484.
- T.D.Subash, Dr.T.Gnanasekaran, J. Jagannathan, **Dr. D. Nirmal** “Intend and portrayal of InSb QWFET for RF application” proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIECS’14, Karpagam College of Enginnering,Coimbatore,Tamil Nadu, March 2014 pp 787-790.

- Abin George, **D.Nirmal**, Prajoon.P, Aldona Mathew “Design and Simulation of Schottky-Source/Drain GaN/AlGa_N HEMTs for Breakdown Voltage Improvement” proceedings of IEEE International Conference on Electronics and Communication System ICECS’14, Karpagam College of Engineering, Coimbatore, Tamil Nadu, February 2014 pp 187-189.
- Aldona Mathew, **D.Nirmal**, Prajoon.P, Abin George “Modelling of HEMT for High Power Switching Application using Polarization Engineering Technique” proceedings of IEEE International Conference on Electronics and Communication System ICECS’14, Karpagam College of Engineering, Coimbatore, Tamil Nadu, February 2014 pp 149-152.
- Surya, **D.Nirmal**, J.Beulah, D.Rubavathy, Daphne and J.David “ CMOS Based Instrumentation Amplifier For Neural Recording System” in First International Conference on Communication , Computation and Control held in Christian College of Engineering and technology from 11th- 13th April 2013.
- Annie, **D.Nirmal**, Sajitha Soman, Praba , Kingsly “Analysis of Gate Engineered SOI MOSFET FOR VLSI Applications” proceedings of IEEE International Multi Conference on Automation, Computing, Control, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. pp 498 - 501 Palai, Kottayam, March 2013.
- Praba, **D.Nirmal**, Sajitha Soman, Annie , Kingsly “Design and Simulation of GaN/AlGa_N HEMTs with Low Leakage Current and High ON/OFF Current Ratio” proceedings of IEEE International Multi Conference on Automation, Computing, Control, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. PP 490 - 493 Palai, Kottayam, March 2013.
- Sajitha Soman, **D.Nirmal**, Praba, Annie , Kingsly “Analysis of Drain Current in Short Channel Drain Extended Tripartite Gate FinFETs” proceedings of IEEE International Multi Conference on Automation, Computing, Control, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. PP 494 - 497 Palai, Kottayam, March 2013.
- **D.Nirmal**, Vijayakumar, Doreen Joy, Binola k Jebalin, and N.Mohankumar “Nanoscale Tri Gate MOSFET for Ultra Low Power Applications Using High-k

Dielectrics ” 5th IEEE International Nanoelectronics Conference, IEEE INEC 2013 held in Singapore, Page 12-16, on 2nd – 4th January 2013

- **D.Nirmal**, ShevinBobinVarughese, DoreenJoy, FlaviaPrincess, and P.Vijayakumar “Design and simulation of ALGa_N/Ga_N HFET” 2012 *International Conference on Devices , Circuits and Systems (ICDCS) Proceedings*, Karunya university Page. 199-201, 2012.
- **D.Nirmal**, DoreenJoy, ShevinBobinVarughese, FlaviaPrincess, and P.Vijayakumar “Low power analysis of triple gate MOSFET” 2012 *International Conference on Devices , Circuits and Systems (ICDCS) Proceedings*, Karunya university Page. 126-129, 2012.
- FlaviaPrincess, P.Sindhu, M.Manikandan, V.LakshmiPrabha and **D.Nirmal** “Mixed mode simulation of SRAM FINFETs” 2012 *International Conference on Devices , Circuits and Systems (ICDCS) Proceedings*, Karunya university Page. 280-283, 2012.
- **D. Nirmal**, P.Vijaya Kumar, Patrick Chella Samuel, Shruti K, Divya Mary Thomas & N. Mohankumar “Analysis Of Dual Gate Mosfets Using High K Dielectrics” 2011 3rd *IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings*, page VI:22-25.
- **D. Nirmal**, P.Vijaya Kumar, Shruti K, Divya Mary Thomas, Patrick Chella Samuel & N. Mohankumar “Analysis Of Single Halo Double Gate Mosfets Using High-K Dielectrics” 2011 3rd *IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings*, page VI:26-30.
- **D. Nirmal**, P.Vijaya Kumar, Divya Mary Thomas, Shruti K, Patrick Chella Samuel & N. Mohankumar “Impact of Gate Engineering on Double Gate MOSFETs using High-k Dielectrics” 2011 3rd *IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings*, page VI:31-34.
- **D. Nirmal**, Divya Mary Thomas, Shruti K, Patrick Chella Samuel, Vijaya Kumar & N. Mohankumar “ Analysis of Dual Material FinFETs using High-k Dielectrics” International Conference on Communication and Signal processing ICCOS 2011 Karunya University, Coimbatore-641 114. 17-18, March 2011

- **D.Nirmal**, Nalini, Vijayakumar “Nanosized High K Dielectric Material For Finfet” *ICE(International conference for Electro ceramics)* , Delhi University, India, 13-17, Dec 2009.
- **D.Nirmal**, Vijayakumar, Rekha, Soumya “Impact Of High-K Dielectrics On Device Performance Of SOI FinFETS” *ISCO(4th international conference on intelligent systems and control)*, karpagam college of engineering, India, 4th and 5th , Feb 2010.
- **D.Nirmal**, Vijayakumar, Rekha “High-K Dielectrics On FinFETs Device Performance Using TCAD” *International Conference on Emerging Trends in Engineering Technologies ICETS*, Noorul Islam university,India, 25 & 26, March 2010.
- **D.Nirmal**, Vijayakumar, Soumya “Device Design And Optimization Of Nanoscale FinFETs Using TCAD” *International Conference on Emerging Trends in Engineering Technologies ICETS*, Noorul Islam university,India, 25 & 26, March 2010.

National conference:

- **Nirmal**, Vijayakumar et.al. “ ANALYSIS OF ANALOG PARAMETERS USING MULTI GATE FET” *3rd National Conference on Signal Processing, Communications and VLSI Design (NCSCV '11)* Anna university of Technology coimbatore.6th & 7th May 2011. pp. 35-39.
- **Nirmal**, Vijayakumar, Samjebaraj, Christopher, Arockia, Sunil “FinFETs For Future Nano Scale Technology” *National Conference on the theme 'Emerging trends in RF and Signal Processing FISAT* , Kerala,India, 25-27, March 2010,pp. 161-165.
- **Nirmal**, Vijayakumar, Samjebaraj, Christopher, Arockia, Sunil “CMOS logic design with Double Gate Transistors” *National Conference on VLSI,Image processing and Wireless communication ARASU Engineering College*, India 20th March 2010,pp.1-7.

- **Nirmal**, Vijay “Implementation of Low Power Circuits Using FinFETs” *National Conference on Microwave and Optical Communication*, Alagappa Chrttiar College Of Engineering And Technology, India,2009.
- **Nirmal**, Jackuline Moni “Floorplanning Based on Btree* and simulated Annealing” *National conference on Modeling, Analysis and Simulation of Computer and Telecommunication systems* Government College Of Engineering Salem, India,2007.

Research Projects:

Project proposal on “Design and Fabrication of HeteroStructure InP/GaN based devices for High Frequency Applications” is submitted to Department of Science and Technology start-up research Grant(Young Scientists Award) has been in the final stage of approval.(24 Lakhs)

Grant Fetched:

Grand-in-aid for organizing **International conference on Devices, Circuits and Systems** from DRDO for the year 2012 with Rs 40,000.

Grand-in-aid for organizing **National workshop on Electronics system Design and Manufacturing (ESDM)** from DIT for the year 2012 with Rs 30,000.

International Conference Resource Person :

Session Chair in the **IEEE 9th International conference on Intelligent Systems and Control**” Organized by Karpagam college of Engineering, India on 9th and 10th Janyary 2015.

Session Chair in the **IEEE SSCS Sponsored Conference On “Solid State Circuits”** Organized by SaintGits college of Engineering, India on 29th August 2014.

I have served as a organizing secretary of 2nd **International conference on Devices, Circuits and Systems** sponsored by **IEEE EDS INDIA** and **IETE INDIA** March 6th to 8th 2014.

Session Chair in the **International Conference Innovations in Information, Embedded and Communication systems** Organized by Karpagam college of

Engineering, India on 13th and 14th of March 2014.

Session Chair in the **International Conference on Devices circuits and systems** Organized by Karunya University, India on 6th to 8th March, 2014.

Session Chair in the **International Conference on Electronics and Communication Systems** Organized by Karpagam college of Engineering, India on 13th and 14th of February 2014.

I have served as a organizing secretary of **International conference on Devices, Circuits and Systems** sponsored by **IEEE EDS INDIA** and **IETE INDIA** March 15th and 16th 2012.

Session Chair in the **International Conference on Innovations in Intelligent Instrumentation , Optimization and Signal** Organized by Karunya University, India on 1st March 2013.

Session Chair in the **IEEE International Multi Automation, Computing, Control, Communication and Compressed Sensing** Organized by St.Joseph's College of Engineering and Technology, Kerala, India on 22th and 23th March 2013.

Session Chair in the 2013 **IEEE International Conference on Emerging Trends in Computing, Communication and Nanotechnology** Organized by Infant Jesus College of Engineering, India on 25th and 26th March 2013.

National Conference Resource Person :

Session Chair in the Fourth National Conference on Communication, Computation, Control and Automation,CCCA2014 Organized by Sri Ramakrishna Institute of Technology, India on 28th and 29th March 2014.

Workshop Organized:

- Organized two day workshop on “**National Workshop on circuit simulation on TANNER EDA Tool**” in Karunya University, 25th and 26th March 2011.
- Organized one day DIT sponsored workshop on “**Electronic System Design Manufacturing**” in Karunya University, 14th October 2012.

International / National Resource Person:

- Keynote address in the **IEEE 9th International conference on Intelligent Systems and Control On “Advanced high power semiconductor devices and its application “Organized by Karpagam college of Engineering, India on 9th and 10th January 2015.**
- Delivered a plenary talk in the **IEEE SSCS Sponsored Conference On “Solid State Circuits”** Organized by SaintGits college of Engineering, India on 29th August 2014.
- Resource person for IEEE India EDS chapter sponsored workshop on “**Nano Electronics Device Modelling**” in Mahendra Engineering College on 14th July 2014.
- Delivered a Talk on “**Nanoelectronics in recent trends**” in Reva ITM,Bangalore India on 24th March 2014.
- Delivered a Special Guest lecture on “**VLSI Design**” in Pavai College of Technology, India on 28th January 2014.
- Delivered a Guest lecture in IETE Student forum inauguration on “**Nanoelectronics in Communication systems**” in Karpagam college of Engineering, India on 30th September 2013.
- Delivered a special lecture in One day seminar on “**Advanced Research Methodologies in VLSI System**” in Cape Institute of Technology , India on 12th August 2013.
- Delivered a Keynote Talk in **2013 IEEE International Conference on Emerging Trends in Computing, Communication and Nanotechnology** Organized by Infant Jesus College of Engineering, India on 25th and 26th March 2013.
- Resource person in “**Workshop for Writing Journal Papers**” at Karunya University, Coimbatore on 6th March 2013.
- Guest Lecture on “**Emerging Trends in NanoElectronics for VLSI Applications**” in Paavai College Of Engineering, Namakkal on 12th February 2013.
- Technical Talk On “**Emerging Engineering Technologies Using FinFET With High-K Dielectrics**” in the IEEE INDIA EDS chapter workshop on FinFET Circuit Design-An Alternate for CMOS Design conducted in Muthayammal Engineering College, Rasipuram, India 2012.

- Guest lecture in National workshop on **“Low power VLSI methodologies”** organized by Hindustan Institute Of Technology, Coimbatore conducted on 24th April 2010.
- Resource person for National Workshop **“Find Your way with Wonder Chip: VLSI”** organized by Karunya University conducted from 19th of September 2009 to 21st of September 2009.
- Delivered a Invited lecture on the topic **“INTRODUCTION TO SYNOPSYS”** in Karunya University.

International Courses / Seminars Attended:

- Participated in pre-conference workshop in “International Conference on signal processing, Image processing and pattern Recognition” on Lab View held on 6th February 2013 at Karunya University.
- Attended the International workshop and IEEE EDS Mini-colloquia on “The past & future of Integrated Circuits Technologies” held during November 4-5, 2009 at S.K.P Engineering college, Tiruvannamalai.
- Advanced International Workshop on “NANOSCIENCE AND TECHNOLOGY” Organized by Anna University Chennai on 2009.

National Courses / Seminars Attended:

- Attended a faculty development program on “ Team building and Leadership skill” Organized by Dept of ECE Karunya University on 6th September 2014.
- Attended a workshop on “COMSOL Multiphysics Modeling” organized by Electronics and Instrumentation Engineering Department , Karunya University on February 14 , 2014.
- Attended the one day “Workshop on Project Proposal Writing” in Karunya University, Coimbatore, February 13th 2013.
- Attended the one day National Workshop on “Recent trends in RF Design” in Anna University of Technology Coimbatore, 09th October 2010.
- Underwent the one day training program in center for VLSI & Nanotechnology in S.K.P Engineering College, Tiruvannamalai, 24 September 2010.
- Attended the 3rd INUP (Indian Nanoelectronics User Program) training program on Nanofabrication Technologies, IISC, Bangalore, 15-16 April 2010.
- Attended the One day Hands-on-training program on “VLSI Back-end tools”

organized by Kumaraguru college of Technology on 20th February 2010.

- Attended the workshop on “ASIC Design using SYNOPSYS” Organized by Karunya University collaboration with D’gipro Design Automation And Marketing Pvt.Ltd., on 2009.
- Attended a “Research Workshop On Nanochemistry” Organized by Karunya University under KRWG scheme on January 28 &29, 2009.
- Work shop cum seminar on “RESEND TRENDS IN VLSI DESIGN & VERIFICATION-AN INDUSTRY PERSPECTIVE” at Manipal Centre For Information Science, Manipal University in 2008.
- Attended the one week “ PEDAGOGIC TRANING” in Karunya University in 2007.
- AICTE sponsored National seminar on “EMERGING TRENDS IN VLSI DEVICES USING EMBEDDED SYSTEMS” conducted by NICE Kumaracoil in 2007
- Underwent one week in-plant training in “Madurai Power Corporation Pvt.Ltd” in 2003.

Reference:

- 1) Dr. Chandan Kumar Sarkar
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I hereby declare that all the information presented above are true and correct to the best of my knowledge and belief.

(D NIRMAL)