

**Dr. D. NIRMAL**

Associate Professor & Head of the Department,
Electronics and Communication Engineering,
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ABOUT MYSELF

Serving Humanity through Teaching , Research, Collaboration and Leadership with passion. Has a good record with 13+ years of experience in Electronics and communication Engineering.

RESEARCH INTERESTS

Nanoelectronics, 1D / 2D Materials, Graphene, Carbon nanotubes, GaN Technology, Nanoscale device design and modelling, Device and circuit simulation- GSI, Sensors.

EXPERIENCE

Associate Professor, Electronics and communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India , July 2013- Till Date

Assistant Professor, Electronics and communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, June 2009- June 2013

Lecturer, Electronics and communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India , June 2007- May 2009

RESEARCH PROFILES

1	Google Scholar Profile	https://scholar.google.co.in/citations?user=IdAzwA8AAAAJ&hl=en
2	Scopus Profile	https://www.scopus.com/authid/detail.uri?authorId=36816416200
3	LinkedIn Profile	https://in.linkedin.com/in/dr-d-nirmal-m-e-ph-d-smieee-lmiete-miste-558a3963
4	ResearchGate Profile	https://www.researchgate.net/profile/Dnirmal_PhD
5	ORCID Profile	https://orcid.org/0000-0002-9751-2816
6	Publons - Web of Science Profile	https://publons.com/researcher/1407130/dnirmal-mephd/

PATENTS

1	A Multilayer GATE MOSFET for Reducing Short Channel Effect, Indian Patent Application No:20204109570– Filed in May 2020.
2	A TRIBOELECTRIC NANOGENERATOR, Indian Patent Application No: 202041027095 A– Filed in July 2020.

RESEARCH PROJECTS ONGOING

#	Title of the project	Funding Agency	Amount	Year
1	Design and Fabrication of Cascode Driver Circuit using GaNHEMT on Si for space applications	Indian Space Research Organization, Govt. of India	33.44 lakh	2019-2022
2	Modelling And Simulation of Tunnel FETS for Low Power Applications	Department of Electronics and Information Technology, Govt. of India	25 lakh	2016-2021
3	Modernization of RF laboratory with MIMO-SDR platform	All India Council for Technical Education	12.43 Lakhs	2019-2021

RESEARCH PROJECTS COMPLETED

#	Title of the project	Funding Agency	Amount	Year
1	A compact modelling of GaN Based FP-HEMT Device for High power microwave Applications	Defense Research and Development Organization, Govt. of India	21.98 lakh	2017-2019
2	Development Of Bio-Compatible Cellulosic Printed Circuit Board For Flexible Electronics	Tamil Nadu State Council for Science And Technology	0.1 lakh	2017-2018
	Growth of GaN Thin Film On Silicon Using PLD Method For High Electron Mobility Transistor	Karunya University	0.25 lakh	2017

SUMMARY OF INTERNATIONAL JOURNAL/ CONFERENCE/ BOOKPUBLICATIONS – (Given in Annexure)

Year	International Journal Publications	International Conference Publications	National Conference publications	Book/Chapter
2009 - 2020	55	38	5	5

FELLOWSHIPS, AWARDS AND ACADEMIC ACHIEVEMENTS

1	IEI Young Engineers Award- Electronics and Telecommunication Engineering- 2017.
2	Karunya university Achievers Award 2016-2017 for obtaining funded project from DRDO.
3	IEEE Madras Section Professional Achievement in the year 2016.
4	IEEE Electron Device Coimbatore Founding Chapter Chair (2015-2020).
5	Best Researcher Award from Karunya University in the year 2014.
6	Shir.P.K.Das Memorial Best Faculty Award in the Year 2013 by Nehru College of Educational and Charitable Trust.
7	Best Research Paper Award with high impact factor from Karunya University in the year 2012.
8	Received Shiksha Rattan Puraskar Award 2012 from Indian International friend ship Society.
9	Distinction in Master of Engineering (VLSI DESIGN)

TEACHING INTERESTS

VLSI Design, CMOS VLSI Technology, Nano electronics, Solid State Device Modeling, Microelectronics, Solid State Electronics, Electron Devices, Microwave and Optical Engineering.

RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD	4	5
Master of Engineering	30+	

EDUCATION

Ph.D., Faculty of Information and Communication Engineering, Highly Commended.
(Specialisation in Nano Device Modelling), Anna University, Chennai- 2012, India

MEng VLSI Design, 2007[First Class with Distinction] , Karunya University, India

BEng Electrical and Electronics, 2005, Anna University, Chennai, India

Post Graduate Diploma in Higher Education Indira Gandhi National Open University, 2015, India

POSITIONS OF RESPONSIBILITY

- **Teaching and Research**, VLSI Laboratory and Centre for research in Semiconductor Devices, Karunya University, Period – July 2007 to Till Date.
- **Co-ordinator Post Graduate programme**, Place- Department of Electrical Technology (EEE,ECE,E&I and Media) Karunya University, June 2016- June 2018.
- **IEEE Student branch counsellor**, Karunya University, June 2016- till date.
- **Department Library In-charge**, Electronics and communication Engineering, Karunya University, June 2007 to 2013.
- **Joint Chief Warden**, Men's Hostel, Karunya University, January 2013- Till Date
- **Warden**, Men's Hostel, Karunya University, June 2007 to December 2010

PROFESSIONAL SOCIETY ACTIVITIES

- Life Member of ISTE(Indian Society for Technical Education)LM-56881
- Senior Member of IEEE (Institute of Electrical and Electronics Engineering)SM-92838928.
- Life Member of SSI(Semiconductor Society of India)
- Member of Institute of Engineers (India)M-1544705.
- Member of IETE (The Institution of Electronic and Telecommunication Engineers) M-215257
- Life Member of IAENG(International Association for engineers)
- Member of IACSIT(International Association Of Computer Science And Information Technology)
- Member of ACCENT (Association of Computer Communication Education for National Triumph Social and Welfare Society)

REVIEW ASSIGNMENTS FOR SCIENTIFIC JOURNALS/ SOCIETIES

- IEEE Electron Device Letters.
- International journal of Microelectronics (MJS) Elsevier.
- International Journal of Microelectronics Reliability Elsevier.
- International journal of Superlattices and Microstructures Elsevier.
- All IEEE Young Engineer's Humanitarian Challenge 2016- Jury member.

EDITORIAL MEMBER FOR JOURNAL

- Editor- Microelectronics Journal(Elsevier)
- Editorial Board member – International journal of Electronics and communications (Elsevier)Till- January 2020.
- Guest Editor - IEEE Trans on Nanotechnology.

INTERNATIONAL ADVISORY COMMITTEE:Several.

COLLABORATION:

- ISRO(Indian Space research organization), Government of India- Dr.Manish K Hooda
- DRDO(Defense research and Development Organization), Ministry of Defense- Dr. SubashChandar.
- Taiwan semiconductor research Institute - Wen-KuanYeh , Director General.
- University of Sharjah, College of Engineering- Anwar Hasan Mohammed Jarndal.
- The University of Texas Rio Grande Valley (UTRGV)- Hasina F Huq PhD

TITLE: (Attached in Annexure)

- International conference resource person: 25
- National conference resource person : 2
- Workshop organized : 5
- International / national resource person: 47
- International courses/seminar attended: 9
- National courses / seminars attended: 27

RESEARCH CONSULTANCY

Consultancy work is carried out for DRDO in Technology Computer aided tool(TCAD) in 2017.

GRANT FETCHED

- Grand-in-aid for organizing **International conference on Devices, Circuits and Systems** from DRDO for the year 2012 with Rs 40,000.
- Grand-in-aid for organizing **National workshop on Electronics system Design and Manufacturing(ESDM)** from DIT for the year 2012 with Rs 30,000.

REFERENCE

Dr. Chandan Kumar Sarkar

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BRIEF BIO-SKETCH IN PARAGRAPH FORMAT

Dr. D. Nirmal has obtained his B.E. degree in Electrical and Electronics Engineering from Anna University, and M.Tech in VLSI Design from Karunya Institute of Technology and Sciences. He has completed Ph.D in Information and Communication Engineering from Anna University in the year 2013. He is presently working as an Associate Professor and Head in the department of Electronics and Communication engineering. His research interests includes Nanoelectronics, 1D/2D Materials, Carbon nanotubes, GaN Technology, Device and Circuit Simulation – GSL, Sensors, Nanoscale device design and modelling. He is a founding chair of IEEE EDS Coimbatore chapter. He has completed a project funded by DRDO, Government of India and currently handling a funded project from ISRO, Department of Space and Ministry of Electronics and Information Technology, India.

He has published more than 100 research papers. He is also a Senior IEEE member. He has delivered many lectures in National and International Level conferences/programs.

ADDRESS FOR COMMUNICATION

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INTERNATIONAL JOURNALS PUBLICATIONS

2020	
55	D.Godfrey, D.Nirmal , D. Godwinraj L. Arivazhagan, N.Mohankumar, J.Tzou and W.K.Yeh " Enhancement of DC and Breakdown Performance on Single to Multi-Step Gate FP Using GaN-HEMT for High Power Applications", <i>Silicon</i> , DOI: 10.1007/s12633-020-00503-4 (2020). (Impact Factor:1.126)
54	A.S. Augustine Fletcher, D. Nirmal , L. Arivazhagan and J. Ajayan , " An Intensive Study on Assorted Substrates Suitable for High JFOM AlGaIn/GaN HEMT", <i>Silicon</i> , DOI: 10.1007/s12633-020-00549-4 (2020). (Impact Factor:1.126)
53	J.Ajayan, D.Nirmal , P.Mohankumar, M.Saravanan, M.Jagadesh and L.Arivazhagan, "A review of photovoltaic performance of organic/inorganic solar cells for future renewable and sustainable energy technologies" <i>Superlattices and Microstructures</i> , https://doi.org/ 10.1016/j.spmi.2020.106549 , (2020) pp 1-53 (Impact Factor:2.385).
52	K.Husna Hamza and D. Nirmal , "A review of GaNHEMT broadband power amplifiers", <i>International Journal of Electronics and Communications (AEU)</i> , Vol 116, (2020) DOI:10.1016/j.aeue.2019.153040 (Impact Factor:2.853)
51	D.Godfrey, D.Nirmal , L. Arivazhagan, R.Ratheskannan, P.Issac Nelson, S.Rajesh, B.Vidhya and N.Mohankumar "A novel ZnPc nanorod derived piezoelectric nanogenerator for energy harvesting " <i>Physica E Low-Dimensional Systems and Nanostructures Vol.118 (2020) (Impact Factor:3.176)</i> .
50	S.Angen, J. Grace jency and D.Nirmal , "A wearable energy storage capacitor using graphene oxide and magnesium oxide as electrodes" <i>Physica E Low-Dimensional Systems and Nanostructures 115 (2020) (Impact Factor:3.176)</i> .
49	A.S. Augustine Fletcher, D. Nirmal , L. Arivazhagan, J. Ajayan and Varghese, A, "Enhancement of Johnson figure of merit in III-V HEMT combined with discrete field plate and AlGaIn blocking layer", <i>International Journal of RF and Microwave Computer-Aided Engineering</i> , vol 30, Issue 2(2020). (Impact Factor:1.472)
2019	
48	Rani, C.S.H., Bagan, K.B., D.Nirmal , and Roach, R.S., " Enhancement of Performance in TFET by Reducing High-K Dielectric Length and Drain Electrode Thickness" <i>Silicon</i> , doi:10.1007/s12633-019-00328-w (2019) (Impact Factor:1.210).
47	M.Manikandan, D.Nirmal , J.Ajayan, P.Mohankumar, P.Prajoon and L.Arivazhagan "A review of blue light emitting diodes for future solid state lighting and visible light communication applications", <i>Superlattices and Microstructures</i> 136 (2019). https://doi.org/ 10.1016/j.spmi.2019.106294 , (Impact Factor:2.385).
46	J.Ajayan, D.Nirmal , Dheena Kurian, P.Mohankumar, L.Arivazhagan , A.S. Augustine Fletcher , T.D.Subash and M.Saravanan "Investigation of impact of gate underlap/overlap on the analog/RF performance of composite channel double gate MOSFETs" <i>Journal of Vacuum Science & Technology B</i> 37 , 062201 (2019); https://doi.org/10.1116/1.5116199 (Impact Factor:1.351).
45	J.Ajayan, D.Nirmal , P.Mohankumar, Dheena Kurian, A.S. Augustine Fletcher , L.Arivazhan and B.Santhoshkumar "GaAs metamorphic high electron mobility transistors for future deep space-biomedical-military and communication system applications: A review" <i>Microelectronics journal</i> , https://doi.org/10.1016/j.mejo.2019.104604 Vol 108, (2019) (Impact Factor:1.284).
44	J.Ajayan, D.Nirmal , P.Mohankumar, and L.Arivazhan, " Investigation of Impact of Passivation Materials on the DC/RF Performances of InP-HEMTs for Terahertz Sensing and Imaging" <i>Silicon</i> , doi.org/10.1007/s12633-019-00226-1 (2019) pp 1-6 (Impact Factor:1.210).
43	L. Arivazhagan , , D. Nirmal , D.Godfrey, J. Ajayan , P.Prajoon A.S. Augustine Fletcher, A.Amir Anton Jone and J.S.Raj Kumar, "Improved RF and DC performance in AlGaIn/GaNHEMT by P-type doping in GaN buffer for millimetre-wave applications", <i>International Journal of Electronics and Communications (AEU)</i> , Vol 108, (2019) Pg 189-194. (Impact Factor:2.853)
42	A.S. Augustine Fletcher, D. Nirmal , J. Ajayan and L. Arivazhagan, "Analysis of AlGaIn/GaNHEMT using discrete field plate technique for high power and high frequency applications", <i>International Journal of Electronics and Communications (AEU)</i> , Vol 99, (2019) Pg 325-330. (Impact Factor:2.853)
41	Suresh Subramanian, B. Sundarambal and D. Nirmal , "Investigation on Simulation-Based Specific Absorption Rate in Ultra-Wideband Antenna for Breast Cancer Detection", <i>IEEE Sensors Journal</i> , Vol 18 No.24, 20 Dec 2018, (Impact Factor:2.617)
40	D. Gracia, D. Nirmal and D. Jackuline Moni, "Impact of Leakage Current in Germanium Channel based DMDGTFT using Drain-gate underlap technique", <i>International Journal of Electronics and Communications (AEU)</i> , Vol 96, (2019) Pg 164-169. (Impact Factor:2.853)
39	P. Vanitha, T.S. Arun Samuel and D. Nirmal , "A New 2D Mathematical Modeling of Surrounding Gate

	Triple material Tunnel FET using Halo Engineering for Enhanced Drain Current", International Journal of Electronics and Communications (AEU), Vol 99, (2019) Pg 34-39. (Impact Factor: 2.115)
2018	
38	J.Ajayan, T.Ravichandran, P.Mohankumar, P.Prajoon, J.CharlesPravin and D.Nirmal, " Investigation of DC and RF Performance of Novel MOSHEMT on Silicon Substrate for Future Submillimeter Wave Applications" <i>Semiconductors</i> , vol. 52,No.16,(2018) pp 1191-1997 (Impact Factor:0.672) .
37	R.Ratheskumar,P.Isaac Nelson, S.Rajesh, T.Ponmudiselvi, A.Mohan , B.Vidhya, D.Nirmal and Arivazhan " Curtailed recombination rate and fast carrier transport in ZnPc/Ga As/ Zn Pc Stacked hybrid structure " <i>Optical Materials</i> , vol. 85,(2018) pp 287-294. (Impact Factor:2.320) .
36	D.Gracia , D.Nirmal and D.JackulineMoni, " Impact of leakage current in germanium channel based DMDGTFET using drain- gate underlap technique" " <i>International Journal of Electronics and Communications (AEÜ)</i> ", vol.96,(2018) pp 164-169. (Impact Factor:2.115) .
35	J. Ajayan , D. Nirmal , T.Revichandran, P.Mohankumar, P. Prajoon, L. Arivazhagan , Chandran Kumar Sarkar " InP high electron mobility transistors for submillimeter wave and terahertz frequency applications: A review " <i>International Journal of Electronics and Communications (AEÜ)</i> ", vol.94,(2018) pp 199-214. (Impact Factor:2.115) .
34	J. Hengsteler, P. Prajoon, D. Nirmal , " Analysis of High Efficiency InGaN Multiple-Quantum-Well Light-Emitting-Diodes Using InGaN Step-Graded Barriers" <i>Journal of Nanoelectronics and Optoelectronics</i> , vol.13,(2018) pp939-943. (Impact Factor:1.019) .
33	J. Ajayan ,T.Revichandran, P.Mohankumar, P. Prajoon, J. Charles Pravin, D. Nirmal , " Investigation of breakdown performance in $L_g = 20$ nm novel asymmetric InPHEMTs for future high-speed high-power applications" <i>Journal of Computational Electronics</i> , vol.17,(2018) pp265-272. (Impact Factor:1.431) .
32	J. Ajayan ,T.Revichandran, P.Mohankumar, P. Prajoon, J. Charles Pravin, D. Nirmal , " Investigation of DC-RF and breakdown behaviour in $L_g = 20$ nm novel asymmetric GaAsMHEMTs for future submillimetre wave applications" <i>International Journal of Electronics and Communications (AEÜ)</i> , vol.84,(2018) pp 387-393. (Impact Factor:2.115) .
31	J. charlespravin, p. prajoon, flavia princess nesamania, p. senthilkumar, D. Nirmal , and G.sriresh," Nanoscale High-k Dielectrics for Junctionless Nanowire Transistor for Drain Current Analysis" " <i>Journal of ELECTRONIC MATERIALS</i> " https://doi.org/10.1007/s11664-018-6075-2 (Impact factor:1.676)
30	P. Prajoon , M. AnujaMenokey , J. Charles Pravin , J. Ajayan , S. Rajesh , D. Nirmal , " Investigation of efficiency enhancement in InGaNMQW LED with compositionally step graded GaN/InAlN/GaN multi-layer barrier" " <i>Superlattices and Microstructures</i> DOI:10.1016/j.spmi.2018.02.008 (Impact factor:2.123) .
2017	
29	D. Nirmal , L. Arivazhagan, A.S.Augustine Fletcher, J. Ajayan, P. Prajoon " Current collapse modeling in AlGaIn/GaNHEMT using small signal equivalent circuit for high power application" " <i>Superlattices and Microstructures</i> https://doi.org/10.1016/j.spmi.2017.12.027 (Impact factor:2.123)
28	J. Ajayan , D. Nirmal , P. Prajoon and J. Charles Pravin"Analysis of nanometer-scale InGaAs/InAs/InGaAs composite channel MOSFETs using high-K dielectrics for high speed applications" <i>International Journal of Electronics and Communications (AEÜ)</i> , vol.79,(2017)pp 151-157 (Impact Factor:1.147) .
27	A.S. Augustine Fletcher, and D.Nirmal , " A survey of Gallium Nitride HEMT for RF and highpower applications" " <i>Superlattices and Microstructures</i> DOI: 10.1016/j.spmi.2017.05.042 (Impact factor:2.123)
26	P. Murugapandian, S.Ravimaran,J.William, J Ajayan and D.Nirmal , " DC and microwave characteristics of 20 nm T-gate InAlN/GaN high electron mobility transistor for high power RF applications" " <i>Superlattices and Microstructures</i> DOI: 10.1016/j.spmi.2017.05.060 (Impact factor:2.123)
25	D.Gracia , D.Nirmal and A. NishaJusteena, " Investigation of Ge based Double Gate Dual Metal Tunnel FET Novel Architecture using Various Hetero dielectric Materials" " <i>Superlattices and Microstructures</i> DOI: 10.1016/j.spmi.2017.04.045 (Impact factor:2.123)
24	J. Ajayan and D. Nirmal , "20 nm In _{0.75} Ga _{0.25} As channel-based HEMTs on Inp/GaAs substrates for future THz applications" <i>Journal of semiconductors</i> , vol.38,No.4(2017)pp 1-6.
23	Charles Pravin J, D.Nirmal , Prajoon P , N.Mohankumar and Ajayan J, "Investigation of 6T SRAM memory circuit using high-k dielectrics based nano scale junctionless transistor" " <i>Superlattices and Microstructures</i> DOI: 10.1016/j.spmi.2017.03.012 (Impact factor:2.117)
2016	
22	J. Ajayan and D. Nirmal , "20 nm high performance enhancement mode InPHEMT with heavily doped S/D regions for future THz applications " <i>Superlattices and Microstructures</i> DOI: 10.1016/j.spmi.2016.10.011 (Impact factor:2.117)

21	P.Prajoon, D. Nirmal , AnujaMenokey and J.Charlespravin, "Temperature dependant efficiency droop analysis of In GaNMQW light emitting diode with modified ABC model.", <i>J Comput Electron</i> , Vol 16 (2016) pp 1511–1520. (Impact factor – 1.104).
20	J. Ajayan and D. Nirmal , "20-nm enhancement-mode metamorphic GaAsHEMT with highly doped InGaAs source/drain regions for high frequency applications" <i>International Journal of Electronics</i> DOI: 10.1080/00207217.2016.1218066 (Impact factor:0.729)
19	J. Ajayan and D. Nirmal , "20-nm T-gate composite channel enhancement-mode metamorphic HEMT on GaAs substrates for future THz applications" <i>J ComputElectron</i> Vol 16(2016), pp 1291–1296. (Impact factor:1.104)
18	Charles Pravin J, D.Nirmal ,Prajoon P and AnujaMenokey M., "A New Drain Current Model for Dual Metal Junctionless Transistor for Enhanced Digital Circuit Performance" <i>IEEE Trans. Electron Devices</i> ,VOL. 63, NO. 9(2016) pp 3782-3789. (Impact Factor – 2.207).
17	Prajoon P, D. Nirmal , AnujaMenokey M, J Charles Pravin "Efficiency Enhancement of InGaNMQW LED Using Compositionally Step Graded InGaN Barrier on SiC Substrate" <i>IEEE J. Display Technology</i> , DOI: 10.1109/JDT.2016.2570814, (2016)1117 - 1121. (Impact Factor – 1.925).
16	P.Prajoon, D. Nirmal , AnujaMenokey and J.Charlespravin, "A Modified ABC Model in InGaNMQW LED Using Compositionally Step Graded Alternating Barrier for Efficiency Improvement", <i>Superlattices and Microstructures</i> , 96 (2016) 155-163. (Impact factor – 2.097).
15	J. Charles Pravin, D. Nirmal , P. Prajoon and J. Ajayan, "Implementation of nanoscale circuits using dual metal gate engineered Nanowire MOSFET with high-k dielectrics for low power applications" <i>Physica E</i> 83 (2016) 95–100. (Impact Factor: 2.00).
2015	
14	J. Ajayan and D. Nirmal , "A review of InP/InAlAs/InGaAs based transistors for high frequency applications" <i>Superlattices and Microstructures</i> 86 (2015) 1–19. (Impact factor: 2.097)
13	Binola k Jebalin, ShobhaRekh, Prajoon, N.Mohankumar and D.Nirmal , "The influence of high-k passivation layer on breakdown voltage of schottkyAlGaIn/GaNHEMTs" <i>Microelectronics Journal. Vol. 46 (12), (2015), 1387–1391. (Impact factor: 0.836).</i>
12	Binola k Jebalin, ShobhaRekh, Prajoon, N.Mohankumar and D.Nirmal , "Unique model of polarization engineered AlGaIn/GaN Based HEMTs for high power applications" <i>Superlattices and Microstructures</i> 78(2015)210–223. (Impact factor: 2.097).
11	B. Padmanaban, R. Ramesh, D. Nirmal and S. Sathiyamoorthy, "Numerical modeling of triple material gate stack gate all-around (TMGSGAA) MOSFET considering quantum mechanical effects" <i>Superlattices and Microstructures</i> 82 (2015) 40–54. (Impact factor: 2.097)
2014	
10	D. Nirmal , P. Vijayakumar , Shruthi and N.Mohan Kumar, " A review of Nanoscale channel and Gate Engineered FINFETS for VLSI mixed signal applications using Zirconium – di- oxide dielectrics" <i>Journal of Engineering sciences and Technology Review</i> Volume7(2) 2014, 119-124.
9	I.Flavia Princess Nesamani, V.LakshmiPrabha,Aswathy Paul and D.Nirmal , "Synthesis and Dielectric Studies of Monoclinic Nanosized Zirconia", <i>International journal of Advances in Condensed Matter Physics</i> , Vol. 2014. http://dx.doi.org/10.1155/2014/828492 (Impact Factor: 1.175).
2013	
10	D. Nirmal , P. Vijayakumar , Shruthi and N.Mohan Kumar, "Nanoscale Channel Engineered Double Gate MOSFET for Mixed Signal Applications using High-k Dielectric", <i>International journal of circuit theory and applications</i> , Volume 41, Issue 6, pp 608-618,2013. (Impact Factor: 1.759).
9	D. Nirmal , P. Vijayakumar , D. Divya and N.Mohan Kumar , "Subthreshold Performance of Gate Engineered FinFET Devices and circuit with High-k Dielectrics", <i>Microelectronics Reliability</i> , 53 (2013) 499–504. (Impact factor: 1.167).
8	Surya A , D. Nirmal , "CMOS Based Instrumentation Amplifier For Neural Recording System" <i>IJCIC</i> Volume5, Issue 1,pp 51-55,2013.
7	D. Nirmal , P. Vijayakumar , Patrick Chella Samuel and N.Mohan Kumar, "Subthreshold analysis of nanoscaleFinFETs for ultra-low power application using high-k materials", <i>International Journal of Electronics</i> , Vol. 100, Issue 6,pp 803-817,2013. (Impact factor: 0.440).
6	Cyril Robinson Azariah, B.Nalini and D.Nirmal , "Fabrication and Characteristics of Flexible Thin Film Depletion Mode Field Effect Transistor (FET) using High-κ Dielectric Nano Zirconia" <i>International Journal of Emerging Trends in Engineering and Development</i> , Issue 3, Vol.2 pp.295-299. (March 2013).

2011	
5	D.Nirmal and P.Vijayakumar, "Fin Field Effect Transistors Performance in Analog and RF for High-k Dielectrics" Defence Science Journal, Vol. 61, No. 3, pp. 235-240, May 2011. (Impact factor: 0.304)
4	D.Nirmal , Shruti .K, Divya Mary Thomas, Patrick Chella Samuel, P.Vijayakumar and N.MohanKumar, "Impact of Channel Engineering on FINFETs using High-k dielectrics" International Journal of Micro and Nano Electronics, Circuits and Systems, 3(1), pp. 7-11, 2011.
2010	
3	D. Nirmal , B.Nalini and P. Vijayakumar, "Nano sized High K Dielectric Material for FINFET", Integrated Ferroelectrics, Vol. 121, Issue 1, pp. 31-35, 2010. (Impact Factor: 0.264)
2	D.Nirmal , P. Vijayakumar and Sam Jebaraj, "NAND Gate Using FINFET for Nanoscale technology", International Journal of Engineering Science and Technology Vol.2 (5), pp. 1351-1358, 2010.
1	D.Nirmal and P.Vijayakumar, "Gate Engineering on the Analog Performance of DM-DG MOSFETs with High K Dielectrics", International Journal of Advanced Science and Technology Vol. 25, pp. 1-6, December, 2010.

INTERNATIONAL CONFERENCE PUBLICATIONS

2020	
38	L. Arivazhagan, Anwar Jarndal, SubhashChander, Godfrey D, Raj Kumar J S, S Bhagyalakshmi, Pavan Kumar Reddy and D.Nirmal , "Self-Heating Analysis of GaN-HEMT for Various Ambient Temperature and Substrate Thickness" IEEE Conference Proceedings on 2020 5th International Conference on Devices, Circuits and Systems, ICDCS20, Coimbatore, India, 5 th -6 th March 2020.
37	Husna Hamza K, D.Nirmal and L. Arivazhagan, "Impact of AlGa _N Back Barrier in AlGa _N /Ga _N HEMT on Ga _N substrate" IEEE Conference Proceedings on 2020 5th International Conference on Devices, Circuits and Systems, ICDCS20, Coimbatore, India, 5 th -6 th March 2020.
36	Godfrey D, D.Nirmal , L. Arivazhagan, Brigis Roy, Yu-Lin Chen, Tien-Han Yu, Wen-KuanYeh and Godwinraj D, "Investigation of AlGa _N /Ga _N HEMT Breakdown analysis with Source field plate length for High power applications" IEEE Conference Proceedings on 2020 5th International Conference on Devices, Circuits and Systems, ICDCS20, Coimbatore, India, 5 th -6 th March 2020.
2019	
35	Arivazhagan.L, D.Nirmal , Ajayan.J, Rajkumar.J.S, Godfry.D, Bhagya.S, "Modeling of self-heating for AlGa _N /Ga _N HEMT with thermal conductivity degradation effect" AIP Conference Proceedings 2nd International Conference on Material Science, Smart Structures and Applications, ICMSS 2019, Erode, India, Volume 2201, 17 December 2019.
34	Arivazhagan.L, D.Nirmal , Ajayan.J, Rajkumar.J.S, Godfry.D, Bhagya.S, "Enhancement of drain current in AlGa _N /Ga _N HEMT using Al _N passivation" AIP Conference Proceedings 2nd International Conference on Material Science, Smart Structures and Applications, ICMSS 2019, Erode, India, Volume 2201, 17 December 2019.
2018	
33	Moni.D.J, Anucia.A.J, D.Gracia, D.Nirmal , "Performance Analysis of GaSb/InAs Tunnel FET for Low Power Applications" IEEE International Conference on Devices, Circuits and Systems, ICDCS 2018; Karunya University, Coimbatore, pp- 335-338.
32	Pandit.P.P, Arivazhagan.L, D.Nirmal , Prajoon.P, Ajayan.J, Rajkumar.J.S, "DC Performance analysis of AlGa _N /Ga _N HEMT for future High power applications" IEEE International Conference on Devices, Circuits and Systems, ICDCS 2018; Karunya University, Coimbatore, pp- 313-318.
2017	
31	NishaJusteena.A, D.Nirmal , Gracia.D, "Design and Analysis of Tunnel FET using High K Dielectric Materials" IEEE International Conference on Innovations in Electrical, Electronics, Instrumentation and Media technology (ICIEEIMT'17), Karunya University, Coimbatore, pp-177-180, 4 March-2017.

30	Pratik.P.Pandi, Grace Jency, J,MonicBabu, Kishore Kumar, D.Nirmal ,"Fabrication of Ultra Flexible Super Capacitor using PVdf" IEEE International Conference on Innovations in Electrical,Electronics, Instrumentation and Media technology (ICIEEIMT'17), Karunya University, Coimbatore, pp-98-102, 4 March-2017.
29	SubashChander, Ajay, D.Nirmal , Mridula Gupta,"30nm Normally Off Enhancement Mode AlGa _N /Ga _N HEMT on SiC Substrate for Future High Speed Nanoscale Power applications" " IEEE International Conference on Innovations in Electrical,Electronics, Instrumentation and Media technology (ICIEEIMT'17), Karunya University, Coimbatore, pp-293-296, 4 March-2017.
2016	
28	AnujaMenokey, D Nirmal , Prajoon P, J Charles Pravin, "Green InGa _N /Ga _N LEDs with n-Ga _N Interlayer for efficiency droop improvement" International Conference on Devices, Circuits and Systems (ICDCS'16), Karunya University, Coimbatore, pp-216-219, 3 March-2016.
27	Charles Pravin., D.Nirmal , PrajoonP., Altrin Sharma., AnujaMenokey M "Impact of Gate Length on the Performance of a Junctionless Dual Metal Transistor with High-k dielectrics", International Conference on Devices, Circuits and Systems (ICDCS'16), Karunya University, Coimbatore, pp-291-294, 3 March-2016.
2015	
26	S sreeram, J Ajayan, K Vivek, D. Nirmal and V Rajesh " A high speed 256-bit carry look ahead adder design using 22 nm strained silicon technology" IEEE Sponsored 2nd international conference on electronics and communication system ICECS-2015, Karpagam College of Engineering, Coimbatore, Tamil Nadu,26-27 Feb. 2015 pp 174 – 179.
25	Jerrin K. Joy, D. Nirmal and P. Prajoon "Effect of quantum well thickness and molar concentration for obtaining different wavelength using AlGaAs/GaAs single quantum well LASER" IEEE sponsored 2nd international conference on electronics and communication systems (ICECS 2015), of IEEE International Conference on Electronics and Communication System ICECS'14, Karpagam College of Engineering, Coimbatore, Tamil Nadu Vol-3, (2015) pp:1738-174.
24	Surya A, D. Nirmal and Charles Pravin "Performance enhancement of Junctionless Gate transistor with high ON/OFF ratio" International Conference on Innovation in Information, embedded and communication systems (ICIIECS-2015), Karpagam College of Engineering, Coimbatore, Tamil Nadu 19 Mar - 20 Mar 2015 pp 1-4.
2014	
23	J. Ajayan, D. Nirmal , D. Sivaranjini, S. Sivasankari and M. Manikandan " High Performance Low Leakage power Full adder Circuit Design Using Rate Sensing Keeper" Proceedings of International Conference on Electronics and Communication Systems-ICECS-2014, Karpagam College of Engineering. Coimbatore, Tamilnadu, Feb.13, 14. pp.55-59.
22	J. Ajayan, D. Nirmal , S. Sivasankari, D. Sivaranjini and M. Manikandan " High speed Low Power Full adder Circuit Design Using Current Comparison Domino" proceedings of Second International Conference on Device Circuits and Systems-ICDCS-2014. Karunya University, Coimbatore, Tamilnadu, pp.10-14, March-6-8, 2014.
21	Prajoon.P, D. Nirmal , Aldona Mathew, AbinGeorge"Enhancement Mode Ga _N Based HEMTUsingPolarization Engineering Technique" proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIIECS'14, Karpagam College of Engineering, Coimbatore, Tamil Nadu, March 2014 pp 208-211.
20	A.Persiya, D. Nirmal ,C.T.Sruthi "Investigation on the optical and structural properties of Thermally Evaporated tin selenide thin films for Phase Change Memory application" proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIIECS'14, Karpagam College of Engineering, Coimbatore, Tamil Nadu, March 2014 pp 482-484.
19	T.D.Subash, Dr.T.Gnanasekaran, J. Jagannathan, D. Nirmal "Intend and portrayal of InSbQWFET for RF application" proceedings of International Conference on Innovations in Information Embedded and Communication Systems ICIIECS'14, Karpagam College of Engineering,Coimbatore,Tamil Nadu, March 2014 pp 787-790.
18	Abin George, D. Nirmal ,Prajoon.P,Aldona Mathew "Design and Simulation of Schottky-Source/Drain Ga _N /AlGa _N HEMTs for Breakdown Voltage Improvement" proceedings of IEEE International Conference on Electronics and Communication System ICECS'14,Karpagam College of Engineering, Coimbatore, Tamil Nadu, February 2014 pp 187-189.

17	Aldona Mathew, D. Nirmal , Prajoon.P, Abin George “Modelling of HEMT for High Power Switching Application using Polarization Engineering Technique” proceedings of IEEE International Conference on Electronics and Communication System ICECS’14, Karpagam College of Engineering, Coimbatore, TamilNadu, February 2014 pp 149-152.
2013	
16	Surya, D. Nirmal , J.Beulah, D.Rubavathy, Daphne and J.David “ CMOS Based Instrumentation Amplifier For Neural Recording System” in First International Conference on Communication , Computation and Control held in Christian College of Engineering and technology from 11th- 13th April 2013.
15	Annie, D. Nirmal , Sajitha Soman, Praba , Kingsly “Analysis of Gate Engineered SOIMOSFET FOR VLSI Applications” proceedings of IEEE International Muti Conference on Automation, Computing, Contral, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. pp 498 - 501 Palai, Kottayam, March 2013.
14	Praba, D. Nirmal , Sajitha Soman, Annie , Kingsly “Design and Simulation of GaN/AlGaNHEMTs with Low Leakage Current and High ON/OFF Current Ratio” proceedings of IEEE International Muti Conference on Automation, Computing, Contral, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. PP 490 - 493 Palai, Kottayam, March 2013.
13	Sajitha Soman, D. Nirmal , Praba, Annie , Kingsly “Analysis of Drain Current in Short Channel Drain Extented Tripal Gate FinFETs” proceedings of IEEE International Muti Conference on Automation, Computing, Contral, Communication and Compressed sensing , iMac4s-2013, St. Joseph’s College of Engineering and Technology. PP 494 - 497 Palai, Kottayam, March 2013.
12	D. Nirmal , Vijayakumar, Doreen Joy, Binola k Jebalin, and N.Mohankumar “Nanoscale Tri Gate MOSFET for Ultra Low Power Applications Using High-k Dielectrics ” 5th IEEE International Nanoelectronics Conference, IEEE INEC 2013 held in Singapore, Page 12-16, on 2nd – 4th January 2013
2012	
11	D. Nirmal , Shevin Bobin Varughese, Doreen Joy, Flavia Princess and P.Vijayakumar “Design and simulation of ALGaN/GaNHFET” 2012 International Conference on Devices, Circuits and Systems (ICDCS) Proceedings, Karunya university Page. 199-201, 2012.
10	D. Nirmal , Doreen Joy, Shevin Bobin Varughese, Flavia Princess, and P.Vijayakumar “Low power analysis of triple gate MOSFET” 2012 International Conference on Devices , Circuits and Systems (ICDCS) Proceedings, Karunya university Page. 126-129, 2012.
9	Flavia Princess, P.Sindhu, M.Manikandan, V.Lakshmi Prabha and D.Nirmal “Mixed mode simulation of SRAM FINFETs” 2012 International Conference on Devices , Circuits and Systems (ICDCS) Proceedings, Karunya university Page. 280-283, 2012.
2011	
8	D. Nirmal , P.Vijaya Kumar, Patrick Chella Samuel, Shruti K, Divya Mary Thomas & N. Mohankumar “Analysis Of Dual Gate Mosfets Using High K Dielectrics” 2011 3rd IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings, page VI:22-25.
7	D. Nirmal , P.Vijaya Kumar, Shruti K, Divya Mary Thomas, Patrick Chella Samuel & N. Mohankumar “Analysis Of Single Halo Double Gate Mosfets Using High-K Dielectrics” 2011 3rd IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings, page VI:26-30.
6	D. Nirmal , P.Vijaya Kumar, Divya Mary Thomas, Shruti K, Patrick Chella Samuel & N. Mohankumar “Impact of Gate Engineering on Double Gate MOSFETs using High-k Dielectrics” 2011 3rd IEEE International Conference on Electronics Computer Technology (ICECT 2011) Proceedings, page VI:31-34.
5	D. Nirmal , Divya Mary Thomas, Shruti K, Patrick Chella Samuel, Vijaya Kumar & N. Mohankumar “ Analysis of Dual Material FinFETs using High-k Dielectrics” International Conference on Communication and Signal processing ICCOS 2011 Karunya University, Coimbatore-641 114. 17-18, March 2011
2010	

4	D.Nirmal ,Vijayakumar, Rekha, Soumya “Impact Of High-K Dielectrics On Device Performance Of SOIFinFETS” <i>ISCO (4th international conference on intelligent systems and control)</i> , karpagam college of engineering, India, 4 th and 5 th , Feb 2010.
3	D.Nirmal ,Vijayakumar, Rekha “High-K Dielectrics On FinFETs Device Performance Using TCAD” <i>International Conference on Emerging Trends in Engineering Technologies ICETS</i> , Noorul Islam university,India, 25 & 26, March 2010.
2	D.Nirmal , Vijayakumar, Soumya “Device Design And Optimization Of NanoscaleFinFETs Using TCAD” <i>International Conference on Emerging Trends in Engineering Technologies ICETS</i> , Noorul Islam university, India, 25 & 26, March 2010.
2009	
1	D.Nirmal ,Nalini, Vijayakumar “Nanosized High K Dielectric Material For Finfet” <i>ICE (International conference for Electro ceramics)</i> , Delhi University, India, 13-17, Dec 2009.

NATIONAL CONFERENCE PUBLICATIONS

5	D. Nirmal , Vijayakumaret,al. “ ANALYSIS OF ANALOG PARAMETERS USING MULTI GATE FET” <i>3rd National Conference on Signal Processing, Communications and VLSI Design (NCSCV '11)</i> Anna university of Technology coimbatore.6th & 7th May 2011. pp. 35-39.
4	D. Nirmal ,Vijayakumar, Samjebaraj, Christopher, Arockia, Sunil “FinFETs For Future Nano Scale Technology” <i>National Conference on the theme 'Emerging trends in RF and Signal ProcessingFISAT</i> , Kerala,India, 25-27, March 2010,pp. 161-165.
3	D. Nirmal ,Vijayakumar, Samjebaraj, Christopher, Arockia, Sunil “CMOS logic design with Double Gate Transistors” <i>National Conference on VLSI,Image processing and Wireless communication ARASU Engineering College, India 20th March 2010,pp.1-7.</i>
2	D. Nirmal , Vijay “Implementation of Low Power Circuits Using FinFETs” <i>National Conference on Microwave and Optical Communication, AlagappaChrttiar College Of Engineering And Technology, India, 2009.</i>
1	D. Nirmal ,JackulineMoni “Floorplanning Based on Btree* and simulated Annealing” <i>National conference on Modeling, Analysis and Simulation of Computer and Telecommunication systems</i> Government College Of Engineering Salem, India, 2007.

BOOK PUBLICATION

5	Chapter Title: Performance Analysis of Dual Metal Double Gate Tunnel-FETs for Ultralow Power Applications Authors: D. Gracia and D. Nirmal Publication Title: Nanoelectronic Materials and Devices. Publisher: Springer ISBN: 978-981-10-7190-4
4	Chapter Title: InP-Based High-Electron-Mobility Transistors for High-Frequency Applications Authors: D. Nirmal and J. Ajayan Publication Title: Nanoelectronics Devices, Circuits and Systems Publisher: Elsevier ISBN: 9780128133538
3	Chapter Title: Modeling of Double gate MOSFETs Authors: D. Nirmal and J. Ajayan Publication Title: Nanoscale Devices Physics Modeling and their Application Publisher: CRC Press Taylor & Francis ISBN: 9781138060340
2	Chapter Title: A Fundamental Overview of High Electron Mobility Transistor and its Applications Authors: D. Nirmal and J. Ajayan Publication Title: Nanoscale Devices Physics Modeling and their Application Publisher: CRC Press Taylor & Francis ISBN: 9781138060340
1	BOOK Title: Handbook for III-V High Electron Mobility Transistor Technologies Editors: D. Nirmal and J. Ajayan

	Publisher: CRC Press Taylor & Francis ISBN: 9781138625273
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INTERNATIONAL CONFERENCE RESOURCE PERSON

27	Session Chair in the “ 2nd International conference on VLSI Devices, Circuits and Systems ” held in Meghnad saha Institute of Technology Kolkata, India 18 th and 19 th July 2020
26	Sharing in the “ VIRTUAL CONFERENCE ORGANIZERS’ PANEL ” held in IEEE Bangladesh Section, Bangladesh 1 st and 2 nd , July 2020
25	Session Chair in the “ IEEE Sponsored 4th International conference on Communication and Electronic systems ” held in PPG Institute of Technology, India 17 th , July 2019
24	Session Chair in the 2019 International Conference on Computer Communication and Informatics (ICCCI 2019) Organized by Sri Shakthi Institute of Engineering and Technology – Coimbatore India on 23 th -25 th January 2019.
23	Session Chair in the “ 3rd IEEE International conference on Communication and Electronic systems ” held in PPG Institute of Technology India 15 th and 16 th October 2018.
22	Session Chair in the 2018 International Conference on Computer Communication and Informatics (ICCCI 2017) Organized by Sri Shakthi Institute of Engineering and Technology – Coimbatore, India on 5 th January 2018.
21	Session Chair in the “ IEEE Sponsored 2nd International conference on Communication and Electronic systems ” held in PPG Institute of Technology India 19 th October 2017.
20	Session Chair in the “ IEEE Sponsored International conference on Signal processing and communication ” held in Karunya University India 28 th July 2017.
19	Session Chair in the “ IEEE Sponsored International conference on Innovations in Electrical Electronics Instrumentation and Media Technology ” held in Karunya University India 4 th February 2017.
18	Session Chair in the IEEE Sponsored International conference on Inventive systems and Control Organized by JCT College of Engineering and technology India on 19 th January 2017.
17	Session Chair in the 2017 International Conference on Computer Communication and Informatics (ICCCI 2017) Organized by Sree Shakthi Institute of Engineering and Technology - Coimbatore India on 7 th January 2017.
16	Session Chair in the IEEE International Conference on Communication and Electronics Systems (ICCES 2016) Organized by PPG Institute of Technology – Coimbatore India on 21 st October 2016.
15	Session Chair in the IEEE International Conference on Inventive Computation Technologies (ICICT 2016) Organized by RVS Technical Campus- Coimbatore India on 26 th to 27 th August 2016.
14	Session Chair in the IEEE EDS and IETE sponsored 3 rd International Conference on Devices Circuits and Systems Organized by Karunya University India on 3 rd to 5 th March 2016.
13	Session Chair in the IEEE sponsored International conference on Electronics and Communication Systems Organized by Karpagam College of Engineering India on 25 th February 2016.
12	Session Chair in the International conference on Systems Science Control Communication Engineering and Technology Organized by Karpagam Institute of Technology India on 10 th August 2015.
11	Session Chair in the International conference on Networks Electronics Communication and Control Organized by Cape Institute of Technology India on 5 th and 6 th March 2015.
10	Session Chair in the IEEE 9th International conference on Intelligent Systems and Control ” Organized by Karpagam college of Engineering India on 9 th and 10 th January 2015.
9	Session Chair in the IEEE SSCS Sponsored Conference On “Solid State Circuits” Organized by Saint Gits college of Engineering India on 29 th August 2014.
8	I have served as a organizing secretary of 2 nd International conference on Devices Circuits and Systems sponsored by IEEE EDS INDIA and IETE INDIA March 6 th to 8 th 2014.
7	Session Chair in the International Conference Innovations in Information Embedded and Communication systems Organized by Karpagam college of Engineering India on 13 th and 14 th of March 2014.
6	Session Chair in the International Conference on Devices circuits and systems Organized by Karunya University India on 6 th to 8 th March 2014.

5	Session Chair in the International Conference on Electronics and Communication Systems Organized by Karpagam college of Engineering India on 13 th and 14 th of February 2014.
4	I have served as a organizing secretary of International conference on Devices Circuits and Systems sponsored by IEEE EDS INDIA and IETEINDIA March 15 th and 16 th 2012.
3	Session Chair in the International Conference on Innovations in Intelligent Instrumentation Optimization and Signal Organized by Karunya University India on 1 st March 2013.
2	Session Chair in the IEEE International Multi Automation Computing Control Communication and Compressed Sensing Organized by St. Joseph's College of Engineering and Technology India on 22 th and 23 th March 2013.
1	Session Chair in the 2013 IEEE International Conference on Emerging Trends in Computing India on 25 th and 26 th March 2013 Communication and Nanotechnology Organized by Infant Jesus College of Engineering

NATIONAL CONFERENCE RESOURCE PERSON

2	Session Chair in the National Conference on Innovations in future communication Technologies, (NCICT'17) Organized by Karunya university, India on 30 th March 2017
1	Session Chair in the Fourth National Conference on Communication, Computation, Control and Automation, CCCA 2014 Organized by Sri Ramakrishna Institute of Technology, India on 28 th and 29 th March 2014.

WORKSHOP ORGANIZED

5	Organized One day workshop on “ 2nd National Workshop on Semiconductor device modeling and applications ” in Karunya University 11 th January 2017.
4	Organized One day IEEE ED Coimbatore Chapter sponsored workshop “ National Workshop on Semiconductor device modeling and applications ” in Karunya University 9 th October 2015.
3	Organized One day IEEE ED Coimbatore Chapter sponsored workshop “ National Workshop on semiconductor memories ” in 18 th September 2015. Karunya University
2	Organized two day workshop on “ National Workshop on circuit simulation on TANNER EDA Tool ” in Karunya 25 th and 26 th March 2011. University
1	Organized one day DIT sponsored workshop on “ Electronic System Design Manufacturing ” in Karunya University 14 th October 2012.

INTERNATIONAL / NATIONAL RESOURCE PERSON

52	Delivered a talk in Faculty Development program on “ Smart materials and sensors: Current updates and future perspective ” organized by Stella Mary's college of Engineering and Technology, India on 11 th July 2020
51	Keynote address on “ Electronic Devices to Circuits systems ” in International conference on Electronic and Sustainable communication systems held Hindustan Institute of Technology and sciences, India 2 nd , July 2020.
50	Delivered a talk in webinar on “ Nanoelectronics and its trending applications ” organized by Galgotias University, Delhi on 22 th June 2020
49	Delivered a talk in webinar on “ Current Trending Devices for 5G Communication ” organized by Department of ECE, Kumaraguru College of Technology, Coimbatore on 28 th June 2020
48	Resource person in the National level Three Day Online Faculty Development Program “ VLSI technology and hardware for AI and ML ” in GMR Institute of Technology, Rajam during 25 th to 27 th May 2020
47	Delivered a talk in Faculty orientation program on “ Future perspective in Research Mobilization ” on 13 th , January 2020 in KPR Institute of Engineering and Technology
46	Resource person in the Guest lecture on “ VLSI and its Applications ” on 09 th , January 2020 in Avinashilingam Institute for Home Science and Higher Education for Women.
45	Resource person in AICTE sponsored Short Term training program and delivered a lecture on “ Recent GaN power process for 5G applications ” on 20 st August 2019 in PSNA College of engineering and Technology.
44	Keynote address on “ Silicon based Energy harvesting devices ” in International conference on communication and electronic systems held PPG institute of Technology, India 17 th , July 2019.
43	Delivered a talk in Faculty development program on “ Bio medical solutions: Integrating new chips, Packages & Modules ” held in Jyothi Engineering college, Kerala, India on 10 th July 2019.

42	Resource person in AICTE Sponsored Two week faculty development program and delivered a talk on “Wearable and implantable Technologies” held in VimalJyothi Engineering college, Kerala, India on 25 th May 2019.
41	Keynote address on “Nanogenerators for energy harvesting applications” in International conference on Intelligent computing and control systems held Vaigai College of Engineering, India 15 th , May 2018.
40	Delivered a technical talk on “Semiconductor Devices and its recent trends” in IEEE Madras section symposium held in Sri Ramakrishna Engineering College, Coimbatore on 25 th January 2018
39	Keynote address on “Semiconductor devices and its applications” in 3 rd IEEE International conference on Inventive systems and control held in JCT College of Engineering and Technology, India 11 th , January 2018.
38	Guest Speaker on “ Current Trends in Semiconductor Devices ” in Francis Xavier Engineering College, Tirunelveli on 10 th January 2018
37	Keynote address on “Electronic system design and its reliability issues” in “ 3 rd IEEE International conference on Communication and Electronic systems held in PPG Institute of Technology, India 15 th and 16 th , October 2018.
36	Delivered Guest Lecture on “ Technology node beyond 14nm in VLSI Systems” in Vellor Institute of Technology, Vellore on 19 th September 2018.
35	Keynote address on “InP based HEMT and its frequency applications” in IEEE Sponsored International conference on Inventive research in computing applications Organized by RVS technical campus coimbatore, India on 2 nd July 2018.
34	Guest Lecture on “ Emerging semiconductor devices and its applications” in ShaheedRajguru college of applied sciences for women, University of Delhi, Delhi on 4 th April 2018.
33	Speech Delivered on “ Flexible electronics and future systems” in Institution of Engineers(India), Coimbatore chapter, Coimbatore on 12 th February 2018.
32	Keynote address on “Flexible semiconductor devices and its applications in future” in IEEE Sponsored 2 nd International conference on Inventive systems and control held in JCT College of Engineering and Technology, India 19 th , January 2018.
31	Guest Lecture on “ Recent Advancements in Electronics and Research opportunities in semiconductor devices” in Kumarguru college of Technology, Coimbatore on 22 nd November 2017.
30	Keynote address on “Impact of High Electron Mobility Transistor in MMIC Systems” in IEEE Sponsored 2 nd International conference on Communication and Electronic systems held in PPG Institute of Technology, India 19 th , October 2017
29	Resource person in the CSIR sponsored National seminar and delivered a lecture on “Analysis of Carbon Nanotube and GrapheneNanoribbon Field Effect Transistors” on 21 st August 2017 in Karpagam college of engineering.
28	Keynote address on “Current Trends in Junctionless Transistors for Ultra Low power Applications” in IEEE Sponsored International conference on Inventive systems and Control Organized by JCT College of Engineering and technology, India on 19 th January 2017.
27	Delivered a Talk on “Semiconductor Devices and Applications ” in One day IEEE ED Coimbatore Chapter sponsored workshop “ 2ndNational Workshop on Semiconductor device modeling and applications” in Karunya University, 11 th January 2017.
26	Guest Lecture on “ NPN and PNP Junctions” in FDTP ON Electron Devices organized by Karpagam Institute of Technology, Coimbatore on 23 th December 2016.
25	Invited talk on “ Semiconductor Nano Electronic Devices and its applications for Future Electronics” in National seminar on MEMS and Nano Electronic Devices held on 19 th and 20 th December 2016 in Bharathiar University, India
24	Delivered a Keynote address on “NanoscaleJunctionless Transistor Design and Its Applications ” in the IEEE International Conference on Communication and Electronics Systems(ICCES 2016) Organized by PPG Institute of Technology - Coimbatore, India on 21 st October 2016.
23	Resource person in the TEQIP-II sponsored faculty development program on “Multiscale modelling and simulation of Nanoelectronic Devices- A research Perspective” on 28 th July 2016 in PSG college of Technology.
22	Delivered a Keynote address on “GaN based High Electron Mobility Transistor (HEMT): Trends and applications” in the IEEE sponsored International conference on Electronics and Communication Systems Organized by Karpagam College of Engineering, India on 25 th

	February 2016.
21	Resource person for a seminar on “ Research paper writing: Methodology & Aspect ” held on 17 th February, organized by Department of Electronics and communication Engg ECHO association, Karunya University.
20	Delivered a Keynote address on “ Emerging trends in Nanotechnology and Integrated circuits ” in National Level Workshop on Recent trends of Nanotechnology in Electronics held at Arasu Engineering College, India on 8 th February 2016.
19	Delivered a Technical Talk on “ Introduction to High Power & Low Power Devices in Nanotechnology ” in Alva’s Institute of Engineering and Technology, India on 06 th November 2015.
18	Resource person for IEEE EDS Coimbatore chapter sponsored workshop on “ National Semiconductor device modeling and applications ” in Karunya University, 9 th October 2015.
17	Delivered a lecture on “ VLSI Technologies and research Trends ” in Royal college of Engineering and technology, India on 14 th August 2015.
16	Keynote address in the “ International conference on Systems, Science, Control, Communication, Engineering and Technology ” on Next Generation Electronics and its Applications Organized by Karpagam Institute of Technology, India on 10 th August 2015.
15	Keynote address in the “ International conference on Networks, Electronics, Communication and Control ” on Heterostructure based MOSFETs in Nano electronic Regime Organized by Cape Institute of Technology, India on 5 th and 6 th March 2015.
14	Keynote address in the “ IEEE 9th International conference on Intelligent Systems and Control ” On Advanced high power semiconductor devices and its application Organized by Karpagam college of Engineering, India on 9 th and 10 th January 2015.
13	Delivered a plenary talk in the IEEESSCS Sponsored Conference On “ Solid State Circuits ” Organized by SaintGits college of Engineering, India on 29 th August 2014.
12	Resource person for IEEE India EDS chapter sponsored workshop on “ Nano Electronics Device Modelling ” in Mahendra Engineering College on 14 th July 2014.
11	Delivered a Talk on “ Nano electronics in recent trends ” in RevaITM, Bangalore India on 24 th March 2014.
10	Delivered a Special Guest lecture on “ VLSI Design ” in Pavai College of Technology, India on 28 th January 2014.
9	Delivered a Guest lecture in IETE Student forum inauguration on “ Nano electronics in Communication systems ” in Karpagam college of Engineering, India on 30 th September 2013.
8	Delivered a special lecture in One day seminar on “ Advanced Research Methodologies in VLSI System ” in Cape Institute of Technology, India on 12 th August 2013.
7	Delivered a Keynote Talk in 2013 IEEE International Conference on Emerging Trends in Computing, Communication and Nanotechnology Organized by Infant Jesus College of Engineering, India on 25 th and 26 th March 2013.
6	Resource person in “ Workshop for Writing Journal Papers ” at Karunya University, Coimbatore on 6 th March 2013.
5	Guest Lecture on “ Emerging Trends in Nano Electronics for VLSI Applications ” in Paavai College Of Engineering, Namakkal on 12 th February 2013.
4	Technical Talk On “ Emerging Engineering Technologies Using FinFET with High-K Dielectrics ” in the IEEE INDIA EDS chapter workshop on FinFET Circuit Design-An Alternate for CMOS Design conducted in Muthayammal Engineering College, Rasipuram, India 2012.
3	Guest lecture in National workshop on “ Low power VLSI methodologies ” organized by Hindustan Institute of Technology, Coimbatore conducted on 24 th April 2010.
2	Resource person for National Workshop “ Find your way with Wonder Chip: VLSI ” organized by Karunya University conducted from 19 th of September 2009 to 21 st of September 2009.
1	Delivered an Invited lecture on the topic “ INTRODUCTION TO SYNOPSYS ” in Karunya University.

INTERNATIONAL COURSES / SEMINARS ATTENDED

10	Attended Five day International Faculty development program on “Exploring the Nuances of Deep learning for research applications- A Deeper Experience” held on 13 th to 17 th July 2020 Karunya Institute of Technology and sciences, India.
9	Participated in IEEE EDS Distinguished Lecture on “Transparent and flexible large area electronics

	" Organized by IEEE EDS Delhi chapter in association with University of Delhi South Campus on 16 th May 2020 .
8	Participated in IEEE EDS Distinguished Lecture on "Theory of Nano- Electron- Fluidic Logic(NFL): A New Digital Electronics concept " Organized by IEEE EDS Coimbatore chapter in association with ECE, Karunya institute of technology and sciences on 5 th December 2017 .
7	Participated in the workshop on "Nanolithography and Nanofabrication" Organized by Raith India and Raith GmbH Germany, held in Indian Institute of Science , Bangalore on 16 th Many 2017 .
6	Participated in " IEEE Sponsored International conference on Innovations in Electrical ,Electronics, Instrumentation and Media Technology" held in Karunya University, India on 3 rd and 4 th , February 2017.
5	Completed the course on "Crosscheck for conferences- Plagiarism Detection Tool" from IEEE on September 1 st 2016.
4	Participated in the IEEE Volunteer Leadership Training programme (VOLT) Program Graduate 2015.
3	Participated in pre-conference workshop in "International Conference on signal processing, Image processing and pattern Recognition" on Lab View held on 6 th February 2013 at Karunya University.
2	Attended the International workshop and IEEE EDS Mini-colloquia on "The past & future of Integrated Circuits Technologies" held during November 4-5, 2009 at S.K.P Engineering college, Tiruvannamalai.
1	Advanced International Workshop on "NANOSCIENCE AND TECHNOLOGY" Organized by Anna University Chennai on 2009.

NATIONAL COURSES / SEMINARS ATTENDED

28	Attended a Webinar on "Quantum dot cellular automata: Emerging Technology for Nano architecture" held on 20 th July 2020 in Sri Eshwar college of Engineering India.
27	Attended a Webinar on "Intellectual Property Rights and Patent Filing" held on 22 nd May 2020 Aurora's Degree & PG College, Chikkadpally, Hyderabad, India.
26	Attended a Leadership talk by MHRD Innovaion Cell held on 16 th May 2020 in Government Of India.
25	Attended a Webinar on "Selection of appropriate Statistical Tools: A critical way for successful Research " held on 12 th May 2020 in St Mary's College, Thoothukudi, India.
24	Attended a Webinar on "Research Methodology and its tools" held on 12 th May 2020 in Crescent Institute of Technology and sciences India.
23	Attended a Webinar on "Machine learning algorithms, applications and trends" held on 10 th May 2020 in Shri ShankarlalSundarbaiShasun Jain college for women association with Computer society of India.
22	Participated an Training programme on " Question paper setting" held on 30 th May to 1 st June 2018 in Karunya Institute of technology and sciences conducted by National Institute of Technical Teachers Training and research (MHRD, Gov Of India).
21	Attended "One day Joint workshop on Patent Filing Procedure" on 7 th June 2018 Organized by Rajiv Gandhi National Institute of Intellectual Property Management, Nagpur and Karunya Institute of technology and sciences.
20	Participated a One day workshop on " Intellectual property Rights Regime in India-IPRP17" held on 18 th September 2017 in Karunya University, India.
19	Participated in National workshop on " Applications of Graph theory in Engineering" held on 28 th April 2016 in Karunya University, India.
18	Participated in " National seminar on MEMS and Nano Electronic Devices 2016" held on 19 th and 20 th December 2016 in Bharathiar University, India.
17	Attended faculty development workshop on "Effective teaching Learning Methodology for Engineers" Organized by Department of Electrical Technology,Karunya University on 23 th Jun 2016
16	Attended one day National level conclave on IOT for smart city on 16 th April 2016 in karunya university.

15	Participated in “Virtual Labs Workshops” conducted at Karunya University on 20 th February 2016.
14	Attended one day workshop on “Intellectual Property Rights” on 15 th September, 2016 in Karunya University.
13	Attended a faculty development program on “Team building and Leadership skill” Organized by Dept. of ECE Karunya University on 6 th September 2014.
12	Attended a workshop on “COMSOL Multiphysics Modeling” organized by Electronics and Instrumentation Engineering Department, Karunya University on February 14, 2014.
11	Attended the one day “Workshop on Project Proposal Writing” in Karunya University, Coimbatore, February 13 th 2013.
10	Attended the one day National Workshop on “Recent trends in RF Design” in Anna University of Technology Coimbatore, 09 th October 2010.
9	Underwent the one day training program in center for VLSI & Nanotechnology in S.K.P Engineering College, Tiruvannamalai, 24 September 2010.
8	Attended the 3 rd INUP (Indian Nano electronics User Program) training program on Nanofabrication Technologies, IISC, Bangalore, 15-16 April 2010.
7	Attended the One day Hands-on-training program on “VLSI Back-end tools” organized by Kumaraguru college of Technology on 20 th February 2010.
6	Attended the workshop on “ASIC Design using SYNOPSYS” Organized by Karunya University collaboration with D’gipro Design Automation and Marketing Pvt.Ltd., on 2009.
5	Attended a “Research Workshop on Nanochemistry” Organized by Karunya University under KRWG scheme on January 28 & 29, 2009.
4	Work shop cum seminar on “RESEND TRENDS IN VLSI DESIGN & VERIFICATION-AN INDUSTRY PERSPECTIVE” at Manipal Centre for Information Science, Manipal University in 2008
3	Attended the one week “PEDAGOGIC TRAINING” in Karunya University in 2007.
2	AICTE sponsored National seminar on “EMERGING TRENDS IN VLSI DEVICES USING EMBEDDED SYSTEMS” conducted by NICE Kumaracoil in 2007
1	Underwent one week in-plant training in “Madurai Power Corporation Pvt.Ltd” in 2003.