

SET DE INSTRUCCIONES - INTEL 8085

Mnemónico	Código	M.Direcc.	Acción	#	Banderas
ACI data	11001110	INMEDIATO	(A)<-- (A) +(byte 2) + (Cy)	2	Z, S, P, Cy, Ac
ADC M	10001110	REG. IND.	(A)<-- (A) + ((H)(L)) + (Cy)	1	Z, S, P, Cy, Ac
ADC r	10001SSS	REGISTRO	(A)<-- (A) + (r) + (Cy)	1	Z, S, P, Cy, Ac
ADD M	10000110	REG. IND.	(A)<-- (A) + ((H)(L))	1	Z, S, P, Cy, Ac
ADD r	10000SSS	REGISTRO	(A)<-- (A) + (r)	1	Z, S, P, Cy, Ac
ADI data	11000110	INMEDIATO	(A)<-- (A) + (byte 2)	2	Z, S, P, Cy, Ac
ANA M	10100110	REG. IND.	(A)<-- (A) . ((H)(L))	1	Z, S, P, Cy=0, Ac
ANA r	10100SSS	REGISTRO	(A)<-- (A) . (r)	1	Z, S, P, Cy=0, Ac
ANI data	11100110	INMEDIATO	(A)<-- (A) . (byte 2)	2	Z, S, P, Cy=0, Ac=0
CALL addr	11001101	INMEDIATO	ver (1)	3	
CC addr	11011100	INMEDIATO	if Cy=1, CALL	3	
CM addr	11111100	INMEDIATO	if S=1, CALL	3	
CMA	00101111		(A)<-- (A)'	1	
CMC	00111111		(Cy)<-- (Cy)'	1	Cy
CMP M	10111110	REG. IND.	(A) - ((H)(L))	1	S, P, Ac, ver (10)
CMP r	10111SSS	REGISTRO	(A) - (r)	1	S, P, Ac, ver (9)
CNC addr	11010100	INMEDIATO	if Cy=0, CALL	3	
CNZ addr	11000100	INMEDIATO	if Z=0, CALL	3	
CP addr	11110100	INMEDIATO	if S=0, CALL	3	
CPE addr	11101100	INMEDIATO	if P=1, CALL	3	
CPI data	11111110	INMEDIATO	(A) - (byte 2)	2	S, P, Ac, ver (11)
CPO addr	11100100	INMEDIATO	if P=0, CALL	3	
CZ addr	11001100	INMEDIATO	if Z=1, CALL	3	
DAA	00100111		ajuste decimal de A	1	Z, S, P,Cy, Ac
DAD B	00001001	REGISTRO	(H)(L)<-- (H)(L) + (rh)(rl)	1	Cy
DAD D	00011001	REGISTRO	(H)(L)<-- (H)(L) + (rh)(rl)	1	Cy
DAD H	00101001	REGISTRO	(H)(L)<-- (H)(L) + (rh)(rl)	1	Cy
DAD SP	00111001	REGISTRO	(H)(L)<-- (H)(L) + (rh)(rl)	1	Cy
DCR M	00110101	REG. IND.	((H)(L))<-- ((H)(L)) - 1	1	Z, S, P, Ac
DCR r	00DDD101	REGISTRO	(r)<-- (r) - 1	1	Z, S, P, Ac
DCX B	00001011	REGISTRO	(rh)(rl)<-- (rh)(rl) - 1	1	
DCX D	00011011	REGISTRO	(rh)(rl)<-- (rh)(rl) - 1	1	
DCX H	00101011	REGISTRO	(rh)(rl)<-- (rh)(rl) - 1	1	
DCX SP	00111011	REGISTRO	(rh)(rl)<-- (rh)(rl) - 1	1	
DI	11110011		deshabilita interrupción	1	
EI	11111011		habilita interrupción	1	
HLT	01110110		stop	1	
IN port	11011011	DIRECTO	(A)<-- (data)	2	
INR M	00110100	REG. IND.	((H)(L))<-- ((H)(L)) + 1	1	Z, S, P, Ac
INR r	00DDD100	REGISTRO	(r)<-- (r) + 1	1	Z, S, P, Ac
INX B	00000011	REGISTRO	(rh)(rl)<-- (rh)(rl) + 1	1	
INX D	00010011	REGISTRO	(rh)(rl)<-- (rh)(rl) + 1	1	
INX H	00100011	REGISTRO	(rh)(rl)<-- (rh)(rl) + 1	1	
INX SP	00110011	REGISTRO	(rh)(rl)<-- (rh)(rl) + 1	1	

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JC addr	11011010	INMEDIATO	if Cy=1, (PC)<--(b 3)(b 2)	3	
JM addr	11111010	INMEDIATO	if S=1, (PC)<--(b 3)(b 2)	3	
JMP addr	11000011	INMEDIATO	(PC)<-- (byte3)(byte2)	3	
JNC addr	11010010	INMEDIATO	if Cy=0, (PC)<--(b 3)(b 2)	3	
JNZ addr	11000010	INMEDIATO	if Z=0, (PC)<--(byte3)(byte2)	3	
JP addr	11110010	INMEDIATO	if S=0, (PC)<--(byte3)(byte2)	3	
JPE addr	11101010	INMEDIATO	if P=1, (PC)<--(byte3)(byte2)	3	
JPO addr	11100010	INMEDIATO	if P=0, (PC)<--(byte3)(byte2)	3	
JZ addr	11001010	INMEDIATO	if Z=1, (PC)<--(byte3)(byte2)	3	
LDA addr	00111010	DIRECTO	(A)<-- ((byte3)(byte2))	3	
LDAX B	00001010	REG. IND.	(A)<--((rp))	1	
LDAX D	00011010	REG. IND.	(A)<--((rp))	1	
LHLD addr	00101010	DIRECTO	ver (8)	3	
LXI B,d16	00000001	INMEDIATO	(rh)<--(byte3), (rl)<--(byte2)	3	
LXI D,d16	00010001	INMEDIATO	(rh)<--(byte3), (rl)<--(byte2)	3	
LXI H,d16	00100001	INMEDIATO	(rh)<--(byte3); (rl)<--(byte2)	3	
LXI SP,d16	00110001	INMEDIATO	(rh)<--(byte3); (rl)<--(byte2)	3	
MOV M, r	01110SSS	REG. IND.	((H)(L)) <-- (r)	1	
MOV r, M	01DDD110	REG. IND.	(r) <-- ((H)(L))	1	
MOV r1,r2	01DDDSSS	REGISTRO	(r1)<--(r2)	1	
MVI M,data	00110110	INMEDIATO	((H)(L)) <-- (byte2)	2	
MVI r,data	00DDD110	INMEDIATO	(r) <-- (byte2)	2	
NOP	00000000		Pausa	1	
ORA M	10110110	REG. IND.	(A) <-- (A) + ((H)(L))	1	Z, S, P, Cy = Ac = 0
ORA r	10110SSS	REGISTRO	(A) <-- (A) + (r)	1	Z, S, P, Cy = Ac = 0
ORI data	11110110	INMEDIATO	(A) <-- (A) + (byte2)	2	Z, S, P, Cy = Ac = 0
OUT port	11010011	DIRECTO	(data) <-- (A)	2	
PCHL	11101001	REGISTRO	(PCH) <-- (H) , (PCL) <-- (L)	1	
POP B	11000001	REG. IND.	ver (6)	1	
POP D	11010001	REG. IND.	ver (6)	1	
POP H	11100001	REG. IND.	ver (6)	1	
POP PSW	11110001	REG. IND.	ver (6)	1	
PUSH B	11000101	REG. IND.	ver (4)	1	
PUSH D	11010101	REG. IND.	ver (4)	1	
PUSH H	11100101	REG. IND.	ver (4)	1	
PUSH PSW	11110101	REG. IND.	ver (5)	1	
RAL	00010111		(An+1)<--(An) , (A0)<--(Cy)	1	(Cy)<--(A7)
RAR	00011111		(An)<--(An+1) , (A7)<--(Cy)	1	(Cy)<--(A0)
RC	11011000	REG. IND.	if CY=1, RET	1	
RET	11001001	REG. IND.	ver (2)	1	
RLC	00000111		(An+1)<--(An) , (A0)<--(A7)	1	(Cy)<--(A7)
RM	11111000	REG. IND.	if S=1, RET	1	
RNC	11010000	REG. IND.	if Cy=0,RET	1	
RNZ	11000000	REG. IND.	if Z=0, RET	1	
RP	11110000	REG. IND.	if S=0, RET	1	
RPE	11101000	REG. IND.	if P=1, RET	1	
RPO	11100000	REG. IND.	if P=0, RET	1	

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RRC	00001111		(An)<--(An+1) , (A7)<--(A0)	1	(Cy)<--(A0)
RST n	11AAA111	REG. IND.	ver (3)	1	
RZ	11001000	REG. IND.	if Z=1, RET	1	
SBB M	10011110	REG. IND.	(A) <-- (A) - ((H)(L)) - (Cy)	1	Z, S, P, Cy, Ac
SBB r	10011SSS	REGISTRO	(A) <-- (A) - (r) - (Cy)	1	Z, S, P, Cy, Ac
SBI data	11011110	INMEDIATO	(A) <-- (A) - (byte2) - (Cy)	2	Z, S, P, Cy, Ac
SHLD addr	00100010	DIRECTO	ver (7)	3	
SPHL	11111001	REGISTRO	(SP) <-- (H)(L)	1	
STA addr	00110010	DIRECTO	((byte3)(byte2)) <-- (A)	3	
STAX B	00000010	REG. IND.	((rp)) <-- (A)	1	
STAX D	00010010	REG. IND.	((rp)) <-- (A)	1	
STC	00110111		(Cy) <-- 1	1	Cy
SUB M	10010110	REG. IND.	(A) <-- (A) - ((H)(L))	1	Z, S, P, Cy, Ac
SUB r	10010SSS	REGISTRO	(A) <-- (A) - (r)	1	Z, S, P, Cy, Ac
SUI data	11010110	INMEDIATO	(A) <-- (A) - (byte2)	2	Z, S, P, Cy, Ac
XCHG	11101011	REGISTRO	(H) <--> (D) , (L) <--> (E)	1	
XRA M	10101110	REG. IND.	(A) <-- (A) (+) ((H)(L))	1	Z, S, P, Cy = Ac = 0
XRA r	10101SSS	REGISTRO	(A) <-- (A) (+) (r)	1	Z, S, P, Cy = Ac = 0
XRI data	11101110	INMEDIATO	(A) <-- (A) (+) (byte2)	2	Z, S, P, Cy = Ac = 0
XTHL	11100011	REG. IND.	(L)<-->((SP)) , (H)<-->((SP)+1)	1	