

Vadim Borisov

Reutlingen, Germany
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Age: 29 years
Nationality: Russian
Married

EXPERIENCE

Reutlingen University, Germany
Software Researcher

10/2016 - present

Developing a new CAD tool. Investigation of novel blackbox verification techniques using Machine Learning (scikit-learn, xgboost) and Artificial Neural Networks (TensorFlow, Keras) packages.

Fraunhofer IIS, Design Automation Division (EAS), Germany
Researcher / CAD Engineer / Python developer

07/2015 - 08/2016

Automation of the analog design flow using **Python** language; new sorting algorithms for transistors arrays.

RIKEN, Kobe, **Japan**
Research Intern

08/2014 - 10/2014

R&D a Carbon Nanotubes Transistor (CNT) behavior model using VerilogA programming language.

SKTB ES, Russia
Physical IC designer

08/2011 - 03/2014

Analog IC design.

EDUCATION

M.Sc., Microsystems, **2.0/1.0**

04/2014 - 07/2016

Bremen City University of Applied Sciences, **Germany**

Thesis title "Development of a Fully Automated CMOS Bandgap Voltage Reference IP for Multiple Technology Nodes Down to 28 nm for Automotive Applications using the Python Language"

M.Eng., Electrical engineering, **GPA 3.9/4.0**
Voronezh State Technical University, Russia

09/2011 - 06/2013

TECHNICAL SKILLS

Unix/MacOS/Windows, Git, SVN, SQL.

Programming languages: **Python**, C++, Cadence SKILL (LISP), Matlab/Octave, Verilog(A), Latex.

Other: A high-level presentation skill.

HOBBIES

History; **Chess**; AI, Programming (huge machine learning fan); Cosmos; Films; Etymology; Music; Eco-Volunteering; Japan; Web-Design.