

A

A

B

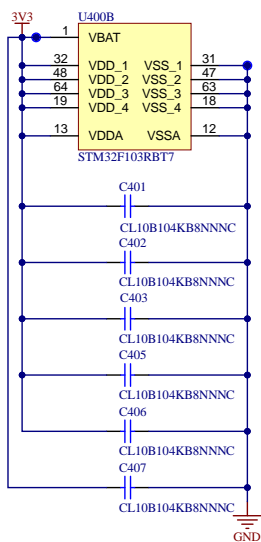
B

C

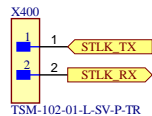
C

D

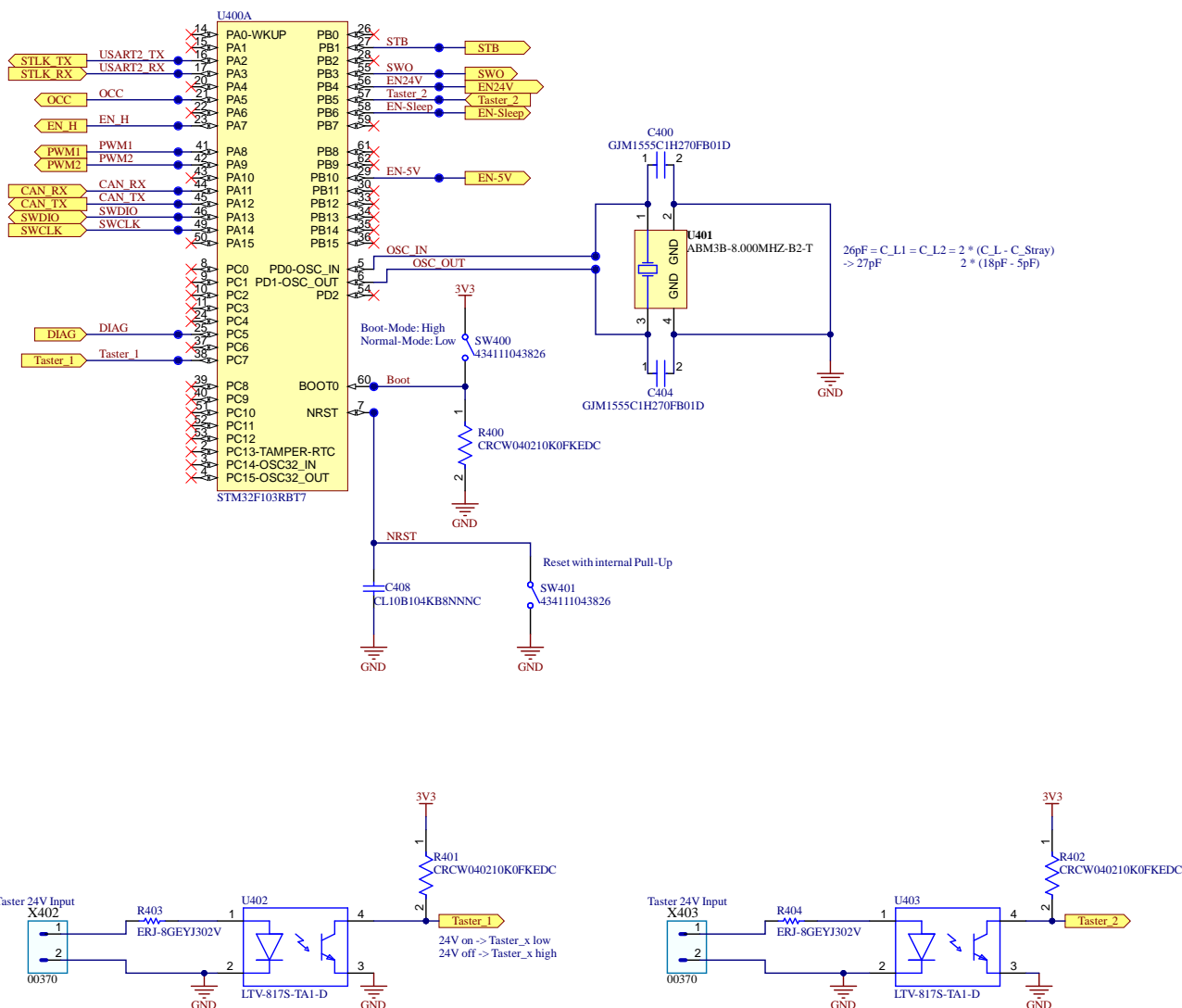
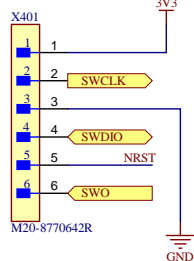
D



USART2 Pins for ST-LINK/V2-1



Debugger Pins for ST-LINK/V2-1

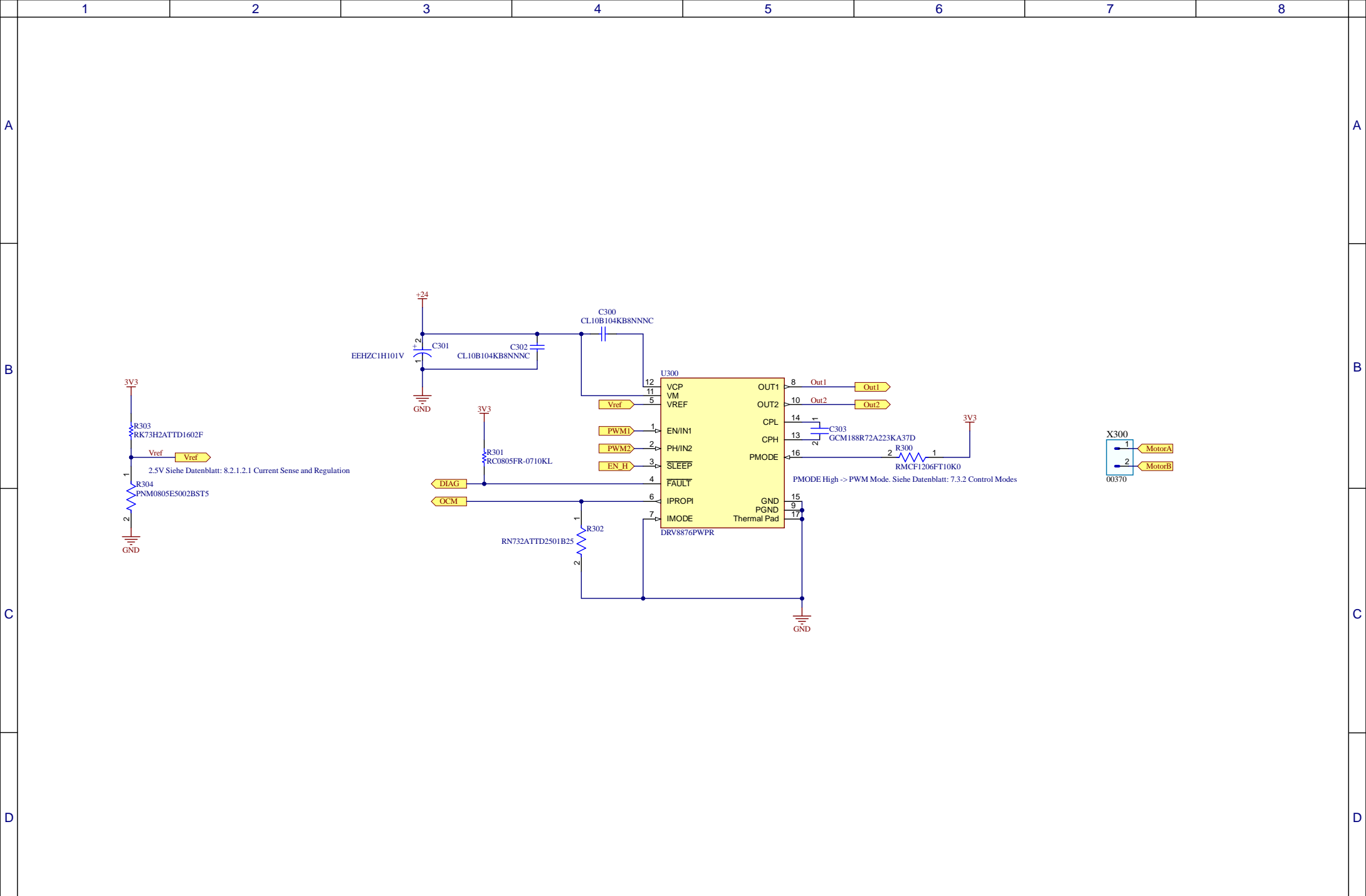


Revision	Date	Name	Revision	Date	Name	Date	JJJJ-mm-dd

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Engineered by A. Scherr
Drawn by A. Scherr
Approved by SCH_Approved

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Part Name: PCB A ACI [No Variations]
Sheet Name: 02-ACI-CPU-Unit-HV00.00.SchDoc

essible to third.		Format: A3	
Version: HV00.00		INDEX PCB: 2022-09-xx	
TKAW-No.(PCBA):		Sheet:	



Revision	Date	Name	Revision	Date	Name	Date	JJJJ-mm-dd	Cannot open file G:\Entwicklung\Leistung selektronik\20-Altium\05 -Library\Grafiken für TK Elevator Innovation and Operations GmbH	This Drawing is explicitly our property. Without our permission it may neither be copied nor accessible to third.		Format: A3	
						Engineered by	A. Scherr		Part Name: PCB A ACI		Version: HV00.00	INDEX PCB:
						Drawn by	A. Scherr		[No Variations]		2022-09-xx	
						Approved by	SCH_Approved		Sheet Name:		TKAW-No.(PCBA):	
TKAW-Entwicklung\Projekte\FuSa\10_LMRise\SC-806010180\TKE-Aktuator\ACI\Altium\ACI\03-ACI-Drive-Unit-HV00.00.SchDoc								TK Elevator Innovation and Operations GmbH	03-ACI-Drive-Unit-HV00.00.SchDoc		Sheet:	
											3 / 5	

1

2

3

4

5

6

7

8

A

B

C

D

FID500

FID501

FID502

FID503

FID504

FID505

TK500

Label 500

6620 000 9327

USR1-15-22 (unpopulated PCB)

SW500

.hex

Software CPI-WE Bundle for USR, UMI, UMB

Checkbox500

CT Programming

Functionaltest

B501

B500

B502

E502

E503

E501

upon consultation

EN61 800-5-1:2008-04

Between circuits (OVC III: 4000V surge, 2120Vpeak overvoltage):
Clearance on PCB: 3.0mm for basic isolation, 5.5mm for reinforced isolation
For 3000m use factor 1.14, means 3.5mm resp. 6.3mm
Creepage on PCB: 500Vrms -> 2.5mm, 800Vdc -> 4.0mm, 1000Vdc -> 5mm
900Vdc -> 4.5mm (interpolated)
Inside ciruits (OVCI: 2500V surge, 2120Vpeak overvoltage):
Clearance on PCB: 1.5mm for operational isolation due to surge, 2.3mm due to overvoltage (interpolated)
For 3000m use factor 1.14, means 2.7mm
Creepage on PCB: 500Vrms -> 2.5mm, 800Vdc -> 4.0mm, 1000Vdc -> 5mm
Creepage on PCB: 500Vrms -> 1.3mm, 800Vdc -> 2.4mm, 1000Vdc -> 3.2mm, but CTI>=175
*?

Revision	Date	Name	Revision	Date	Name	Date	JJJJ-mm-dd	Cannot open file G:\Entwicklung\Leistung selekttronik\20>Altium\05 -Library\Grafiken für	This Drawing is explicitly our property. Whithout our permission it may neither be copied nor accessible to third.		Format: A3	
						Engineered by	A. Scherr		Part Name: PCB A ACI	Version: HV00.00	INDEX PCB: 2022-09-xx	
						Drawn by	A. Scherr		[No Variations]			
						Approved by	SCH_Approved		Sheet Name: 15-ACI-HV00.00 - misc.SchDoc	TKAW-No.(PCBA):		Sheet: 5 / 5

