NG CAPWAP IRAM Package Alpha Release 108_x_9

1 General

This IRAM package of Next Generation CAPWAP includes the following main features: CAPWAP new features on top of the existing Legacy IP offloading features which includes: Coarse Classification (CC), Independent-Mode (IM), Host-Commands (HC), IPv4/6 Fragmentation (IPF), IPv4/6 Reassembly (IPR), IPsec and Header Manipulation (HM).

2 Availability

The package is currently available for the following devices.

Table 1. Package Availability by Device

Device	Version Number	Compiler Version	Loader file name (.h .bin)
T1024 rev 1.0	108_4_9	_	t1024_r1.0.h
			fsl_fman_ucode_t1024_r1.0 _108_4_9.bin
B4860 rev 2.0	108_4_9	_	b4860_r2.0.h
B4860 rev 2.2			b4860_r2.2.h
			fsl_fman_ucode_b4860_r2.0 _108_4_9.bin
			fsl_fman_ucode_b4860_r2.2 _108_4_9.bin
T4240 rev 1.0	108_4_9	_	t4240_r1.0.h
T4240 rev 2.0			fsl_fman_ucode_t4240_r1.0 _108_4_9.bin
			t4240_r2.0.h
			fsl_fman_ucode_t4240_r2.0 _108_4_9.bin
	Table continues of	en the next nego	

Table continues on the next page...



Table 1. Package Availability by Device (continued)

T2080 rev 1.0	108_4_9	_	t2080_r1.0.h
T2080 rev 1.1	1		t2080_r1.1.h
			- fsl_fman_ucode_t2080_r1.0 _108_4_9.bin
			fsl_fman_ucode_t2080_r1.1 _108_4_9.bin
LS1043 rev 1.1	108_4_9	_	ls1043_r1.1.h
			fsl_fman_ucode_ls1043_r1.1 _108_4_9.bin
LS1046 rev 1.0	108_4_9	_	ls1046_r1.0.h fsl_fman_ucode_ls1046_r1.0 _108_4_9.bin
T1040 rev 1.0	108_5_9* (* reduced version	_	t1040_r1.0.h
T1040 rev 1.1	w/o IM)		t1040_r1.1.h
			fsl_fman_ucode_t1040_r1.0 _108_5_9.bin
			fsl_fman_ucode_t1040_r1.1 _108_5_9.bin

3 Revision History

Table 2. Revision History for Alpha Release 108.x.9

Release Date: March 21 2016		
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.	
New Features(Not in spec)	Support Semaphore to protect updating Depletion Counter.	
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.	
Bug Fixes/CCB	 Fix a FE bug when checking end of internal buffer. Semaphore to protect updating Depletion Counter. Fix a bug that ucode will stuck when HM returns to FE_HM. Update checksum to subtract 0xff and add new DSCP factor for ALU. 	
Table continues on the next page		

Table 2. Revision History for Alpha Release 108.x.9 (continued)

Known Issues	None.
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_18 release notes for example).

Table 3. Revision History for Alpha Release 108.x.8

Release Date: March 20 2016		
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.	
New Features(Not in spec)	Support for dynamic change MTU.	
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.	
Bug Fixes/CCB	 Add support for dynamic change MTU. Save MTU of capwap/IPF to internal context before manip TD check. Added Trace buffer script to analyze output files. Script for creating HW sky blue verilog risc monitor file. 	
Known Issues	None.	
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_18 release notes for example).	

Table 4. Revision History for Alpha Release 108.x.7

Release Date: January 10 2016		
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.	
New Features(Not in spec)	Support FE (Functional Element), FE parameters, port page parameters, support for TOS/TC propagation.	
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.	
Table continues on the next page		

Table 4. Revision History for Alpha Release 108.x.7 (continued)

Bug Fixes/CCB	Added support for TOS/TC propagation when replacing IPv4 ->IPV6 header and viceversa.	
	2. Fixed a stack usage for NGCAPWAP project.	
	3. Added port page parameters	
	4. Added FE support and FE parameters.	
	5. Added FE_Enter	
	6. Added FE HM	
Known Issues	None.	
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_16 release notes for example).	

Table 5. Revision History for Alpha Release 108.x.6

Release Date: October 26 2015		
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.	
New Features(Not in spec)	None.	
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.	
Bug Fixes/CCB	 Updated NextGenCapwap_Parser. Split the IPv6 label into two labels. Soft parser: Updated udp lite soft parser: Soft parser jumps to UDP HXS instead of ending parse needed for setting UDP bit in LCV. 	
Known Issues	None.	
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_15 release notes for example).	

Table 6. Revision History for Alpha Release 108.x.5

Release Date: April 21 2015	
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.
New Features(Not in spec)	Support IPsec manipulation and IP fragmentation on egress OP when frame statrs with IP header (no L2) and no parser.
Table continues on the next page	

5

Table 6. Revision History for Alpha Release 108.x.5 (continued)

Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.
Bug Fixes/CCB	 Support IP fragmentation when frame starts with IP header and no L2. IPsec Manip for frame w/o L2 was not functional (FD length was incorrect). OPCode 2 was not functional for this package due to previous changes. Soft parser: Handle situation that UDP lite header does not exist and GRE header exists.
Known Issues	None.
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_15 release notes for example).

Table 7. Revision History for Alpha Release 108.x.4

Release Date: March 22 2015		
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.	
New Features(Not in spec)	Support no parser in IPsec manipulation on encryption.	
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.	
Bug Fixes/CCB	None.	
Known Issues	None.	
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_15 release notes for example).	

Table 8. Revision History for Alpha Release 108.x.3

Release Date: Feb. 1 2015	
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapte
New Features(Not in spec)	HM new command: replace field header command.
Table continues on the next page	

Table 8. Revision History for Alpha Release 108.x.3 (continued)

Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.
Bug Fixes/CCB	 HM L3 insert: IPv6 routing headre in L4 checksum was not correct. Feature copy TOS was not functional together with checksum calculation. Fixed Errata HM2 (FManV3): HM on OP when input frame is SG and VSPE=1 may result with frame data corruption.
Known Issues	None.
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_15 release notes for example) Release Date:

Table 9. Revision History for Alpha Release 108.x.2

Release Date: Sep. 17, 2014				
New Features	Next Generation CAPWAP features. For more information refer to specification of FMan controller chapter.			
New Features(Not in spec)	None.			
Spec Un-Supported Features	The image for T1040 is not supporting independent mode (IM). The reason for this is the reduced IRAM size of this silicon which is 32K bytes. Assuming user requires in uboot to run IM then for uboot it is required to use other image which supports IM (as IPACC_106_x_14 for example) and only after uboot load this image.			
Bug Fixes/CCB	None.			
Known Issues	None.			
Restrictions	Same as IPACC package restrictions. (as IPACC_106_x_14 release notes for example)			

How To Reach Us

Home Page:

nxp.com

Web Support:

nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: nxp.com/SalesTermsandConditions.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, I2C BUS, ICODE, JCOP, LIFE VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFIRE, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, AltiVec, C-5, CodeTEST, CodeWarrior, ColdFire, ColdFire+, C-Ware, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorlQ, QorlQ Qonverge, Ready Play, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, SMARTMOS, Tower, TurboLink, and UMEMS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. ARM, AMBA, ARM Powered, Artisan, Cortex, Jazelle, Keil, SecurCore, Thumb, TrustZone, and µVision are registered trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. ARM7, ARM9, ARM11, big.LITTLE, CoreLink, CoreSight, DesignStart, Mali, mbed, NEON, POP, Sensinode, Socrates, ULINK and Versatile are trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

> NG_CAPWAP_108_x_9 Rev. 1 10 Feb 2017

