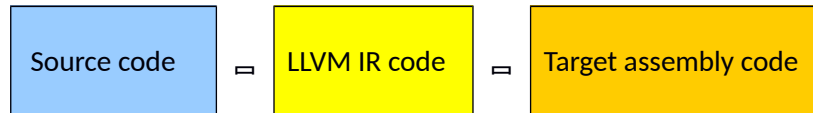


## CMPE 230 Systems Programming

### Homework 1 (due March 14 23:59)

( This project can be implemented only in C/C++ or Java )

LLVM (low level virtual machine - <http://llvm.org/> ) is a compiler infrastructure that provides IR (intermediate representation) that can be used to generate code for various target architectures.



In this project you will develop a translator called STM2IR that will input assignment statements and expressions (one on each line). STM2IR will generate low level LLVM IR code that will compute and output these statements.

For example, given the following code in file.stm:

```
x1 =3
y= 11/2
zvalue=23+x1*(1+y)
zvalue
k=x1-y-zvalue
k=x1+3*y*(1*(2+5))
k+1
```

STM2IR will output the following IR code:

#### LLVM IR (file.ll)

```
; ModuleID = 'stm2ir'
declare i32 @printf(i8*, ...)
@print.str = constant [4 x i8] c"%d\0A\00"

define i32 @main() {
    %k = alloca i32
    %x1 = alloca i32
    %y = alloca i32
    %zvalue = alloca i32
    store i32 3, i32* %x1
    %1 = udiv i32 11,2
    store i32 %1, i32* %y
    %2 = load i32* %x1
    %3 = load i32* %y
    %4 = add i32 1,%3
    %5 = mul i32 %2,%4
    %6 = add i32 23,%5
    store i32 %6, i32* %zvalue
    %7 = load i32* %zvalue
    call i32 @printf(i8*, ...) * @printf(i8* getelementptr ([4 x i8]* @print.str, i32 0, i32 0), i32 %7 )
    %9 = load i32* %x1
    %10 = load i32* %y
    %11 = sub i32 %9,%10
    %12 = load i32* %zvalue
    %13 = sub i32 %11,%12
    store i32 %13, i32* %k
    %14 = load i32* %x1
    %15 = load i32* %y
    %16 = mul i32 3,%15
    %17 = add i32 2,5
    %18 = mul i32 1,%17
    %19 = mul i32 %16,%18
    %20 = add i32 %14,%19
    store i32 %20, i32* %k
    %21 = load i32* %k
    %22 = add i32 %21,1
    call i32 @printf(i8*, ...) * @printf(i8* getelementptr ([4 x i8]* @print.str, i32 0, i32 0), i32 %22 )
    ret i32 0
}
```

## Syntax Error Handling

When there is a syntax error on the given statement file, the program should exit without generating LLVM IR code (when it encounters the first error) and write an error message on the standard output stream. The error message will have the following template:

Error: Line <number>: <message>

For example:

```
var1 = 12
result = var1 + var2
result
```

The expected output ( on the standard output stream) is as follows:

```
Error: Line 2: undefined variable var2
```

The exact wording of the error message is not important, but it should be informative about the error and it should point the correct line number of the given statement file.

### Please note the following about the IR code:

- LLVM IR uses static single assignment (SSA) based representation. In assignment statements, variables are assigned a value once.
- `alloca` is used to allocate space for variables and return address of allocation.
- In IR, variables start with % sign.
- The keyword `i8`, `i16` and `i32` means 8 bit, 16 bit and 32 bit type respectively.
- The keyword `i32 *` means 32 bit pointer.
- Variables `%i` (where `i` is an integer) are temporary variables.
- The yellow colored code defines the module name and the prototype for the `printf` output statement. Generate this part as it is shown in the above example.
- The green colored code is for printing the value of a variable using the `printf` function.
- You can assume only binary operations (`+`, `-`, `*`, `/`) will be used in the expressions. All variables and operations are integer operations. Division operation gives the quotient.

Commands	Explanation
<code>stm2ir file.stm</code>	Runs <code>stm2ir</code> on <code>file.stm</code> and produces IR code in <code>file.ll</code>
<code>lli file.ll</code>	Runs the <code>llvm</code> interpreter & dynamic compiler. For the above example, this command produces the output: 41 109
<code>llc file.ll -o file.s</code>	Invoke <code>llc</code> compiler to produce assembler code
<code>clang file.s -o file.exe</code>	Compiles assembler code to produce the executable
<code>./file.exe</code>	Runs the executable. For the above example, this command produces the output: 41 109

Note that you are implementing only the `stm2ir` program. The others are LLVM commands. You should prepare a makefile that compiles your source code. Your project will be graded according to the following criteria:

Documentation (written document describing how you implemented your project)	12%
Comments in your code	8%
Implementation and tests	80%

### Submission

You can enroll canvas web site following this url: <https://canvas.instructure.com/enroll/9Y7HEC> You will navigate to Assignments and choose Homework1. You will upload only your source codes and Makefile as a single compressed file.

### Late Submission

If the project is submitted late, the following penalties will be applied:

- 0 < hours late ≤ 24 : 25%
- 24 < hours late ≤ 48 : 50%
- hours late > 48 : 100%

### Proof of Existence

Before you submit your project, please notarize your project zip file at <http://virtual-notary.org/> by using the “Notarize a document” button and save the details of the notary log. Do **NOT** lose the notary log and the project zip file. It is a proof that your project zip file existed during the time of submission. If for some reason something went wrong with submission, notary log and the corresponding project zip file will prove that your project zip file existed. Only the project zip file that matches the notary log will be accepted. If you show a project zip file that does not match (correspond to) the notary log, it will **NOT** be accepted !.