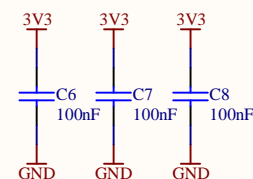
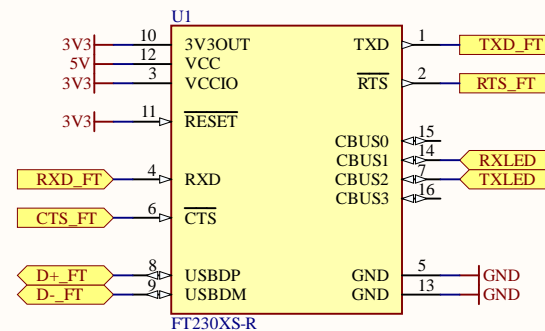
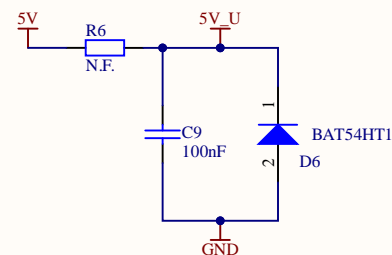


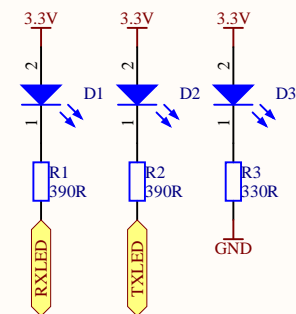
USB Data + and -



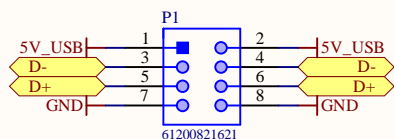
Decoupling on 3V3



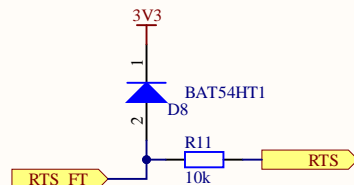
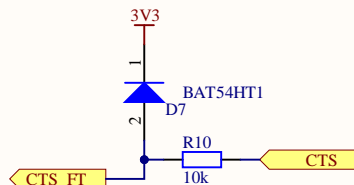
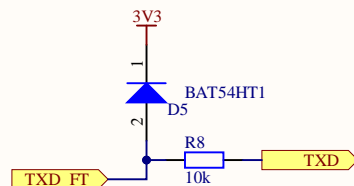
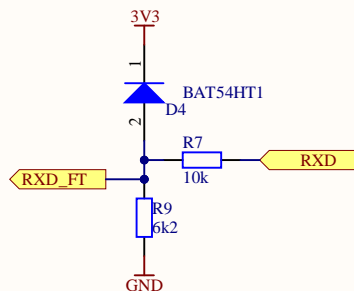
5V Pass Through



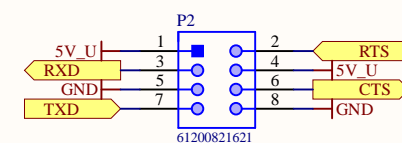
TX, RX and Power On LEDs



USB Connector and Header



Protection on UART input pins



UART Header

Title: USB-UART Interface Module		Dept. of ECSE The University of Auckland 20 Symonds Street Auckland	
Author: Duleepa JT		Revision: 1.0	
Date: 27/01/2021	Time: 5:11:00 PM	Sheet 1 of 1	New Zealand
File: E:\Duleepa\Git\flyback-classkit-2021\USB_Interface\USB_UART_Module\USB_Interface_SCH.SchDoc			

