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D	Revision History Date Revision Changes 24.1.2012 R1 First Release									D
	30.3.2012 14.11.2012	R2	Fixed several minor issued Added USB boot fallback Connected FPGA_PROG	ck, connected FT232 to VCC_5V, slow	ved down DCDC compensatio	on filter				
С	03.07.2013	R3	Updated pinouts R4 = R3 + SD-Card + B Patch => Sheet3							С
В										В
A						Company Sheet name Project Revision 007 Scale 1:1	Enclustra 17_REVISI Mars PM3 R4 Date Designed Format A3	IONHISTORY 1	04.03.2014 ets 17/ 17	A