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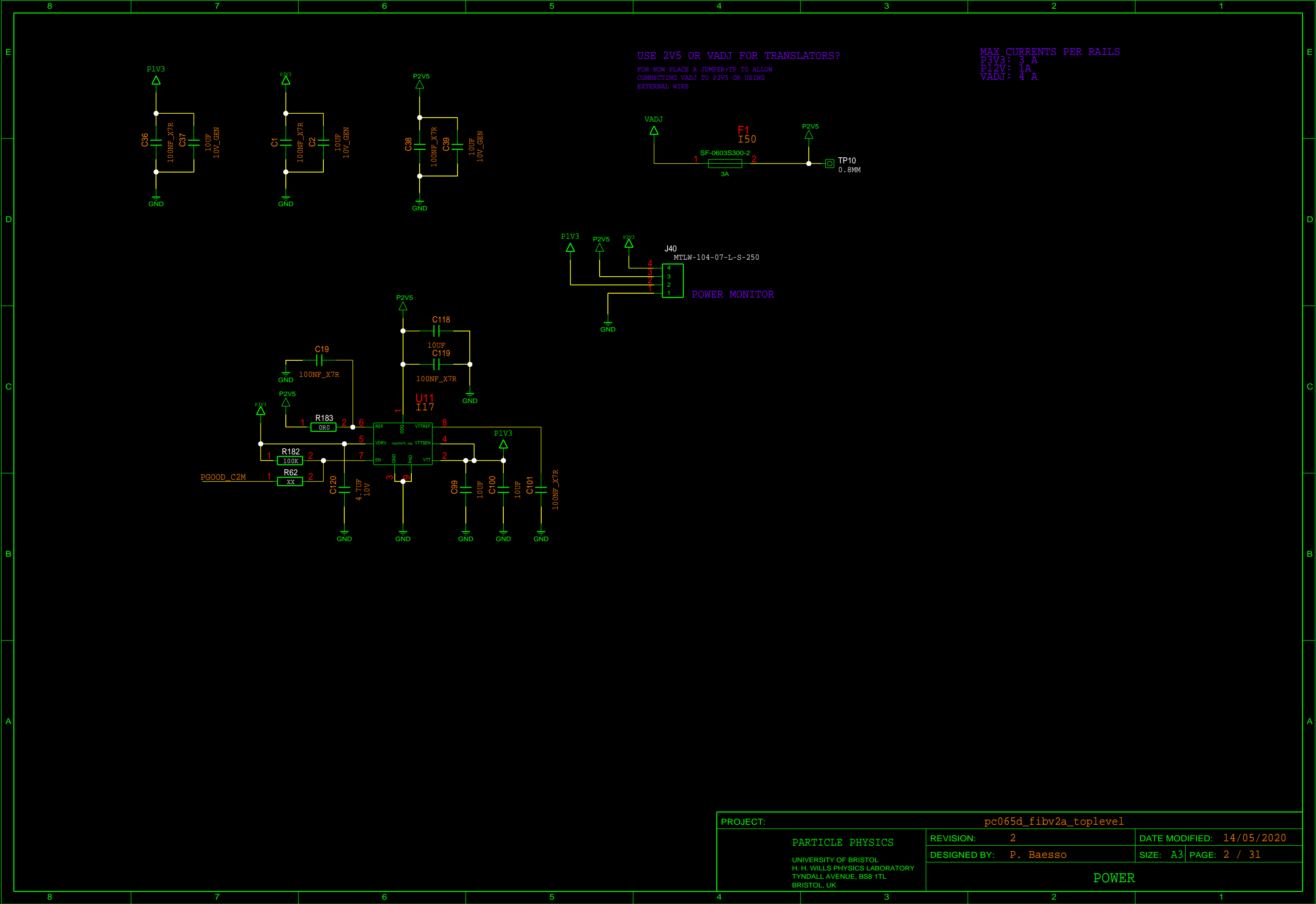
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17	page1	sfp_module
18	page1	sfp_module
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20	I2C LEVEL TRANSLATOR	i2c_level_translate
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28	I2C LEVEL TRANSLATOR	i2c_level_translate
29	AB	fmc_hpc_generic
30	CDGH	fmc_hpc_generic

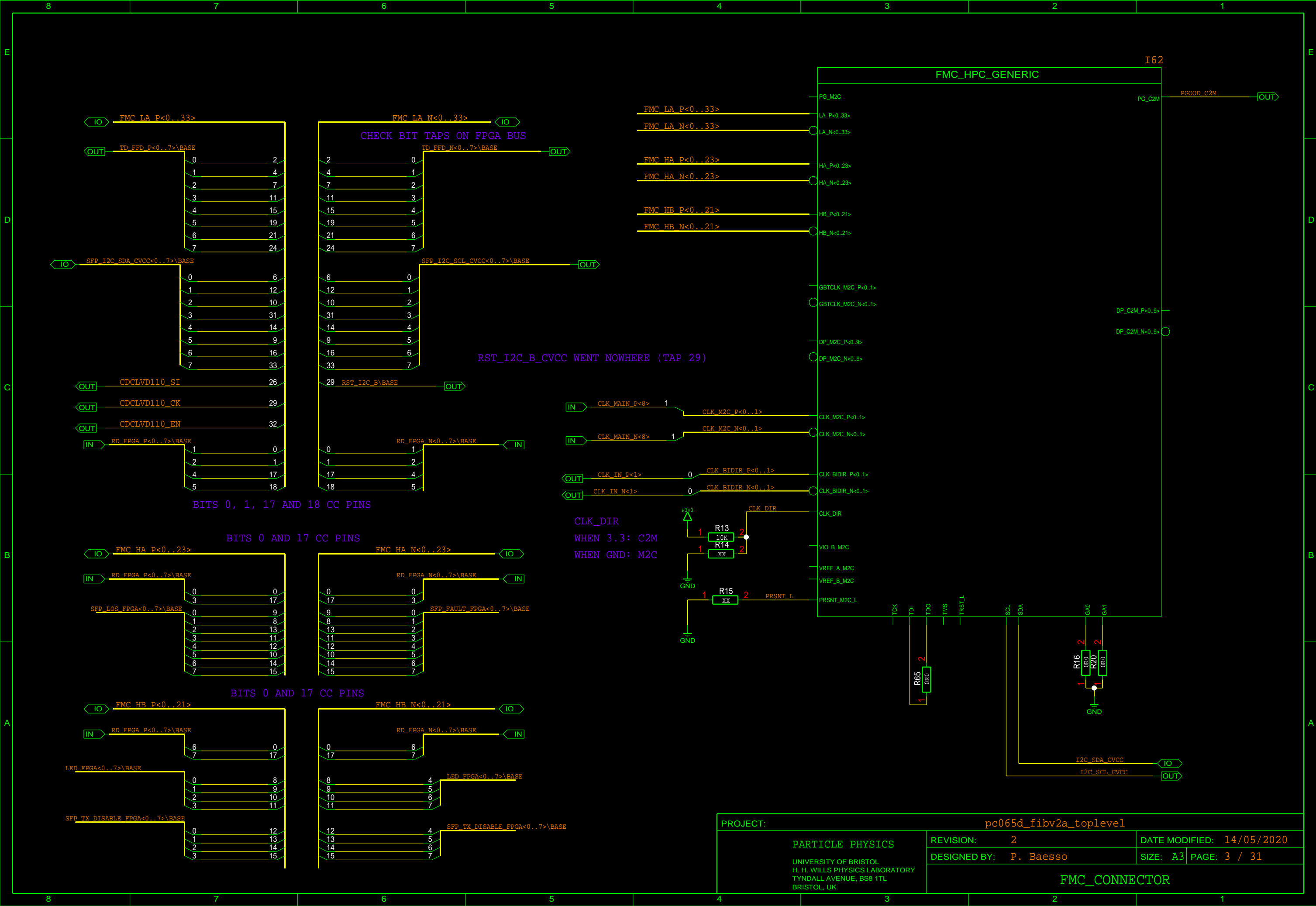
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CDR 0X60 (ALTERNATIVE: 0X40)  
CLOCK 0X68 (0X69, 0X6A, 0X6B)  
EXPANDER 0X74  
EXPANDER 0X75

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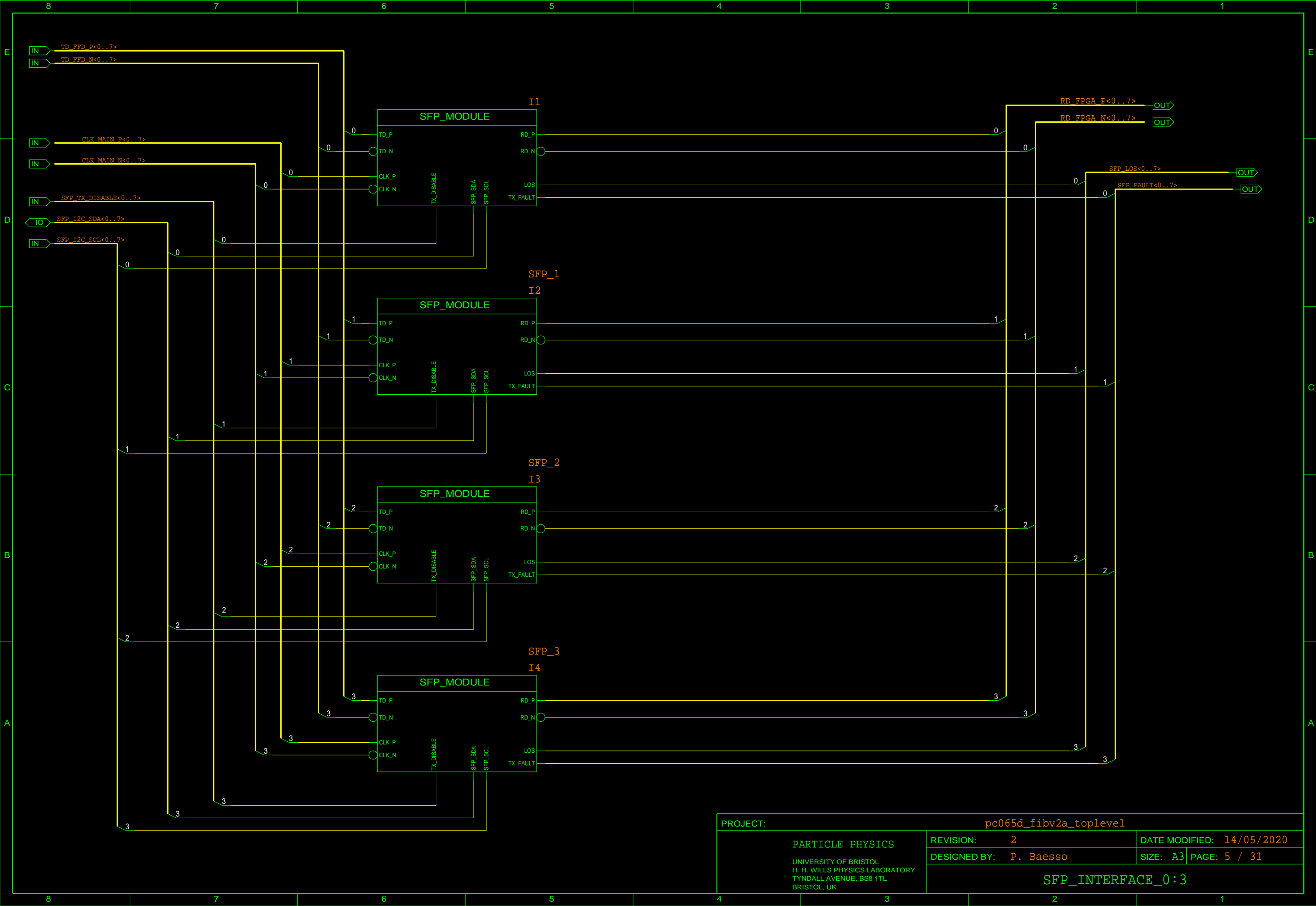
PROJECT: pc065d_fibv2a_toplevel			
PARTICLE PHYSICS  UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORATORY TYNDALL AVENUE, BS8 1TL BRISTOL, UK	REVISION: 2	DATE MODIFIED: 14/05/2020	
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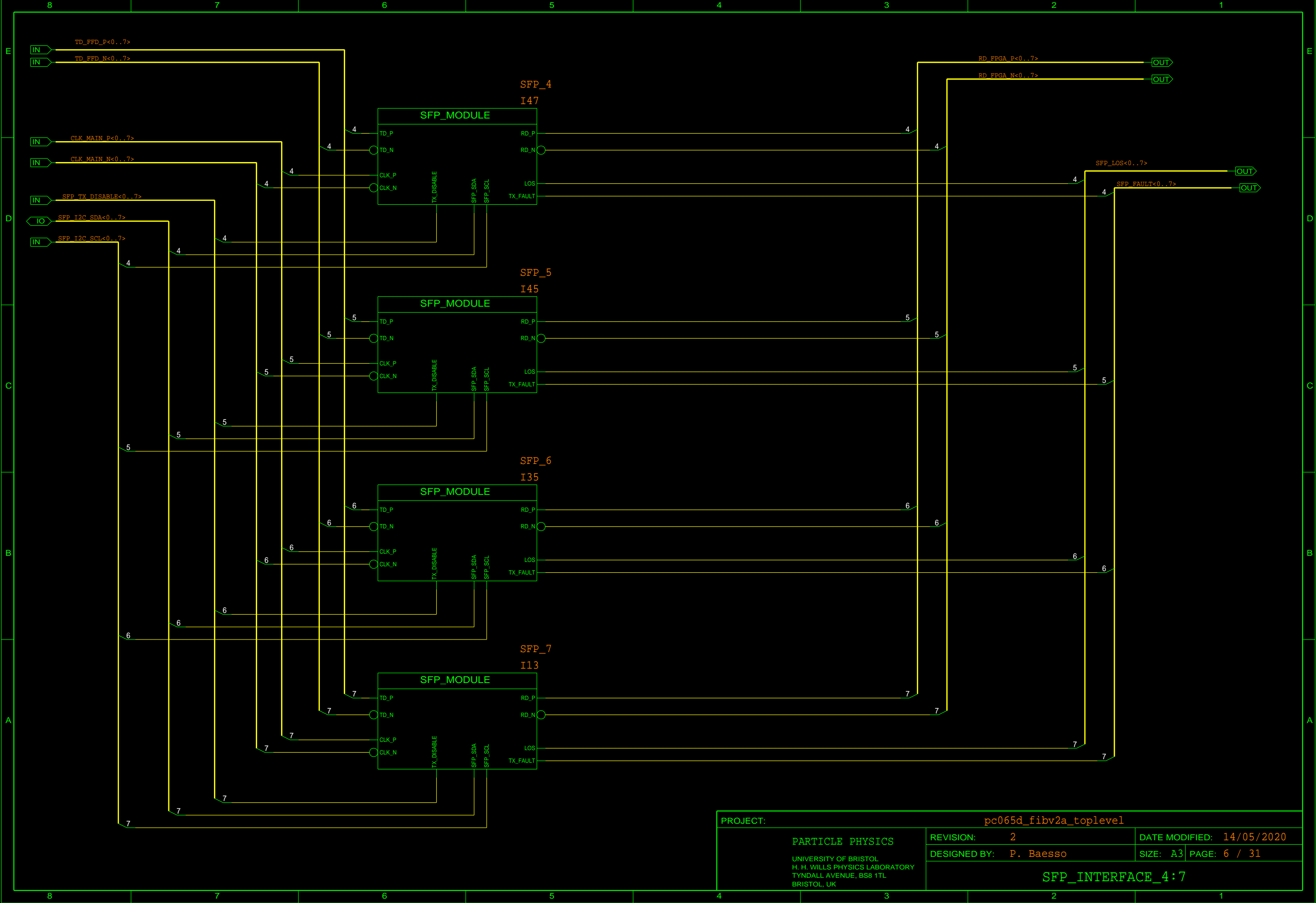


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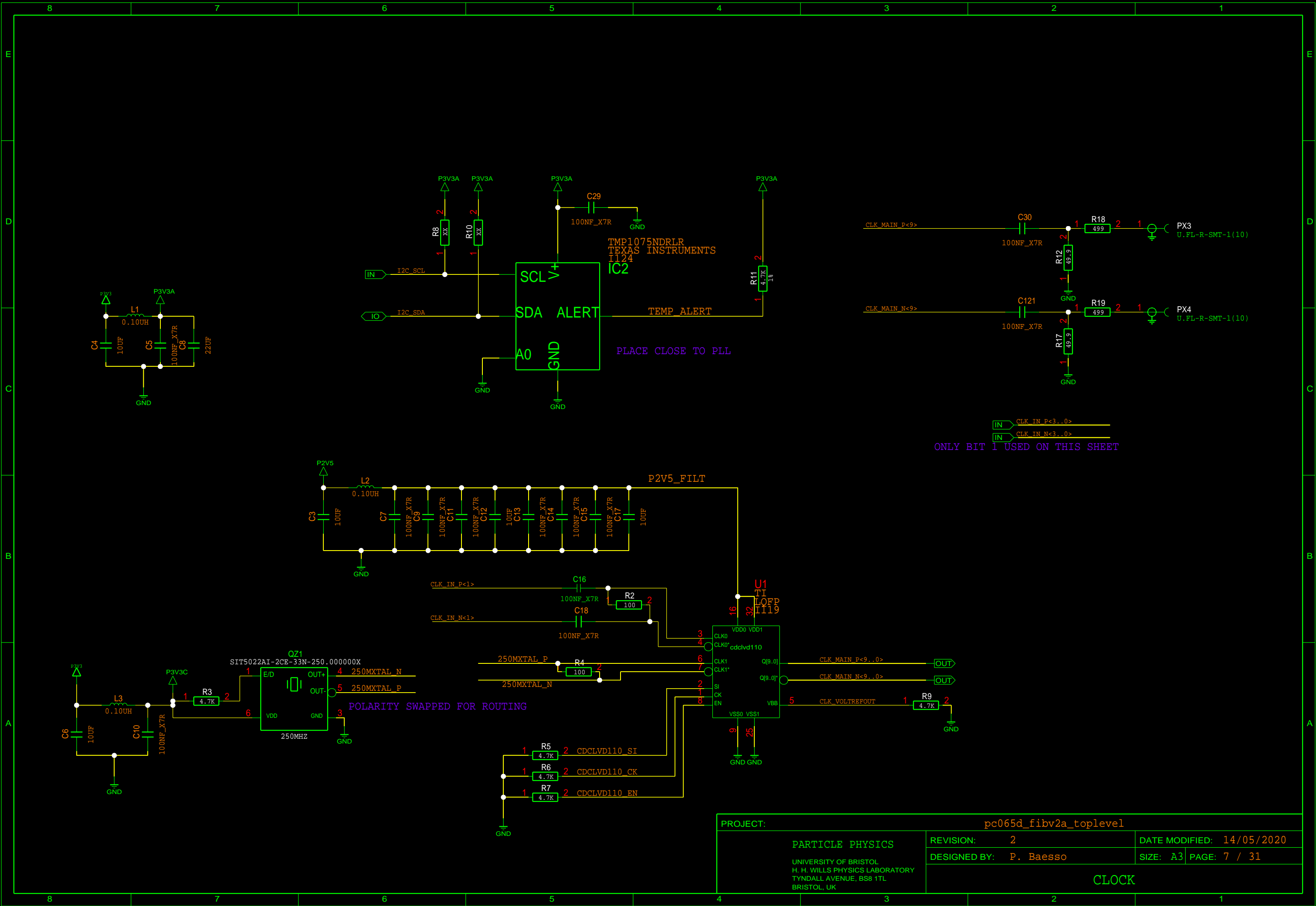




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	SFP_INTERFACE_0:3		

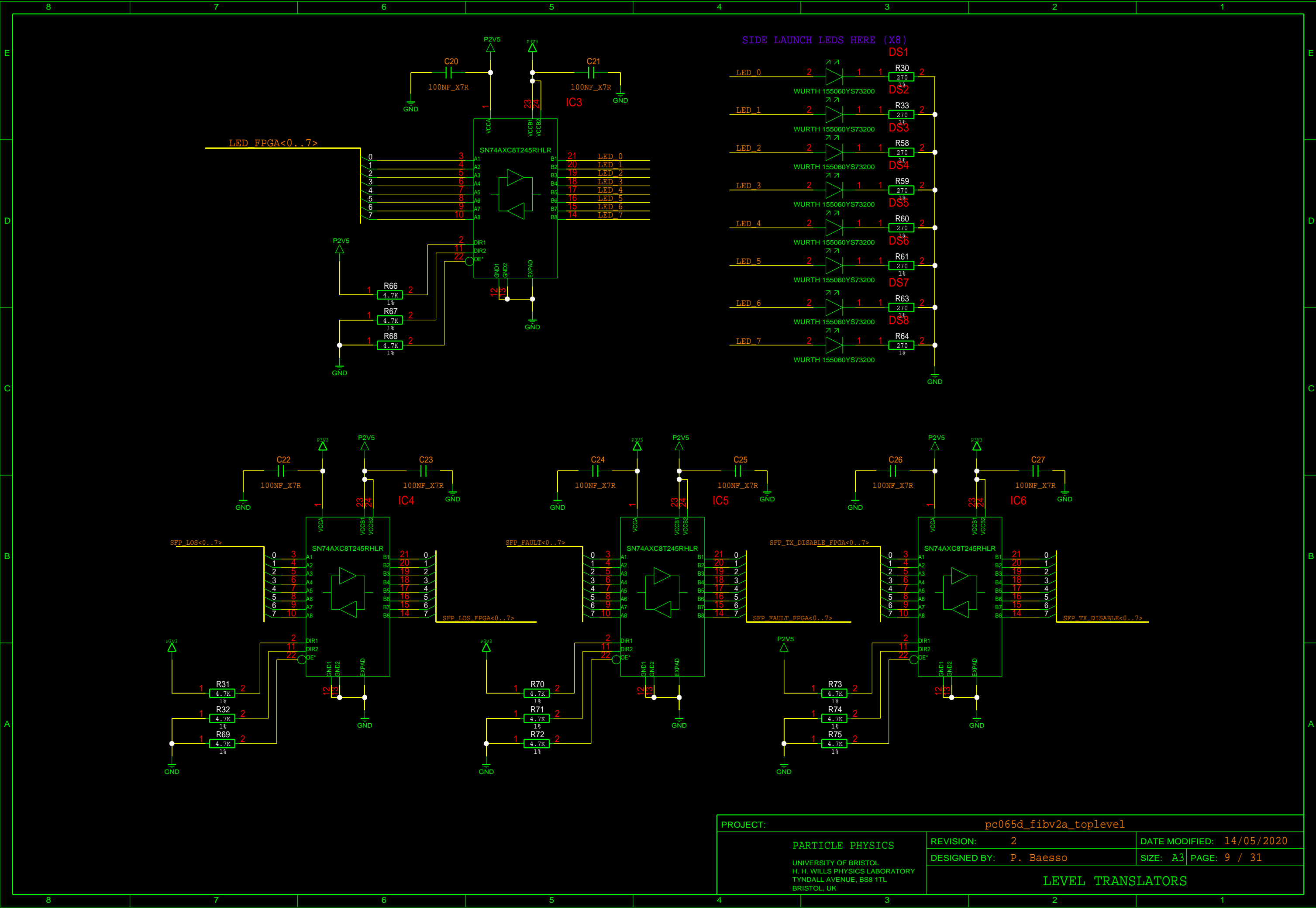


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E

D

D



C

B

B

A

A

8

7

6

5

4

3

2

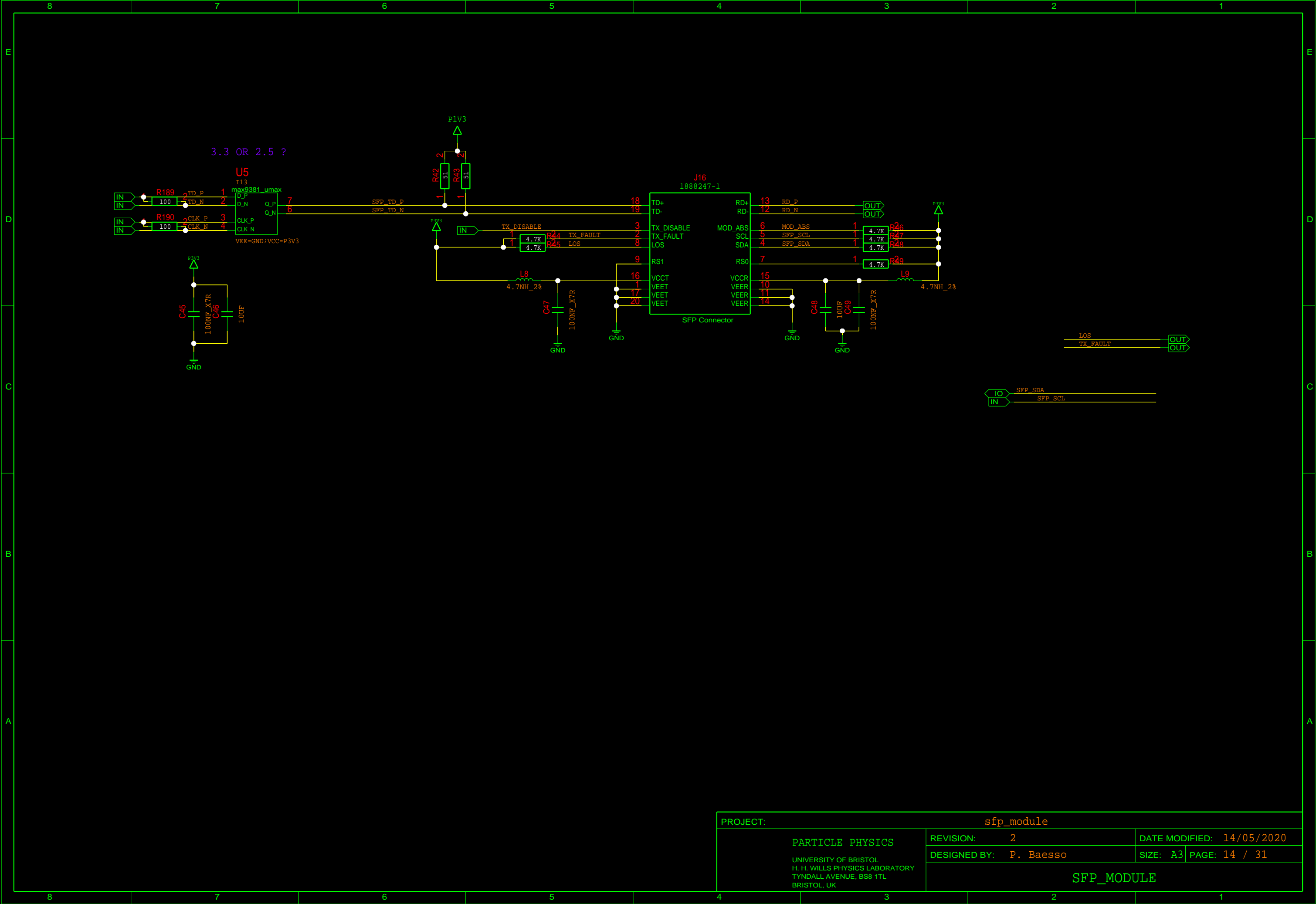
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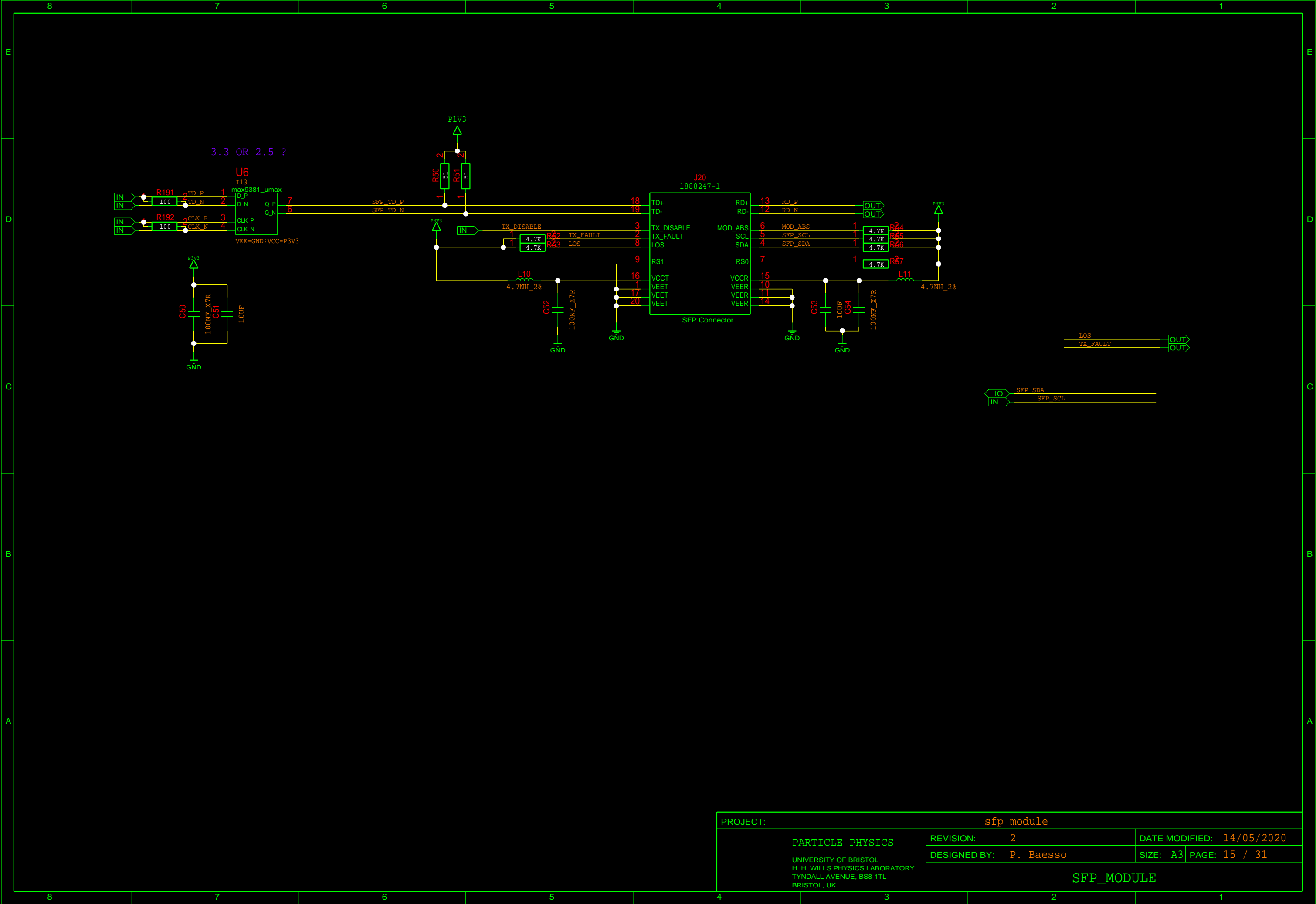
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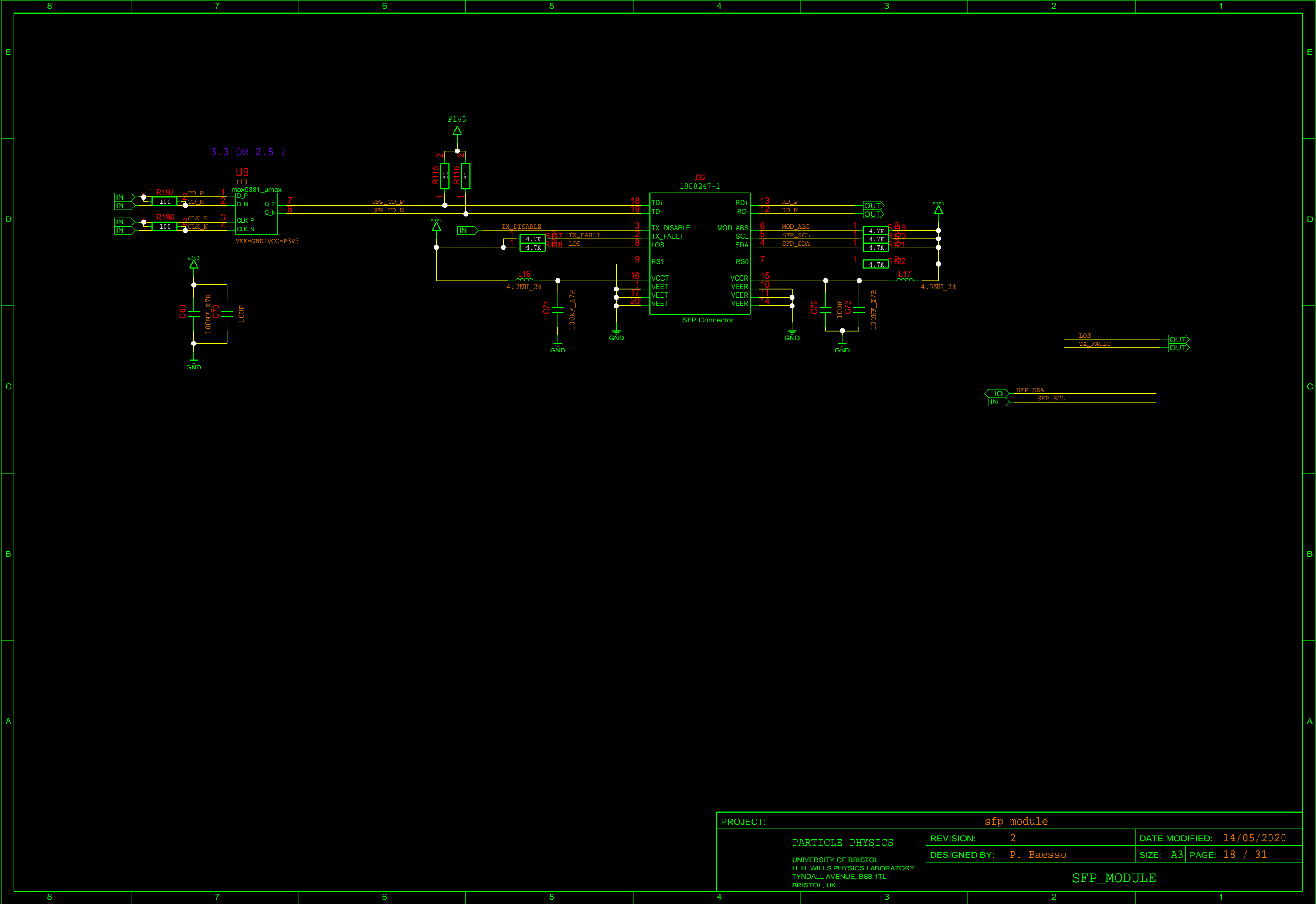


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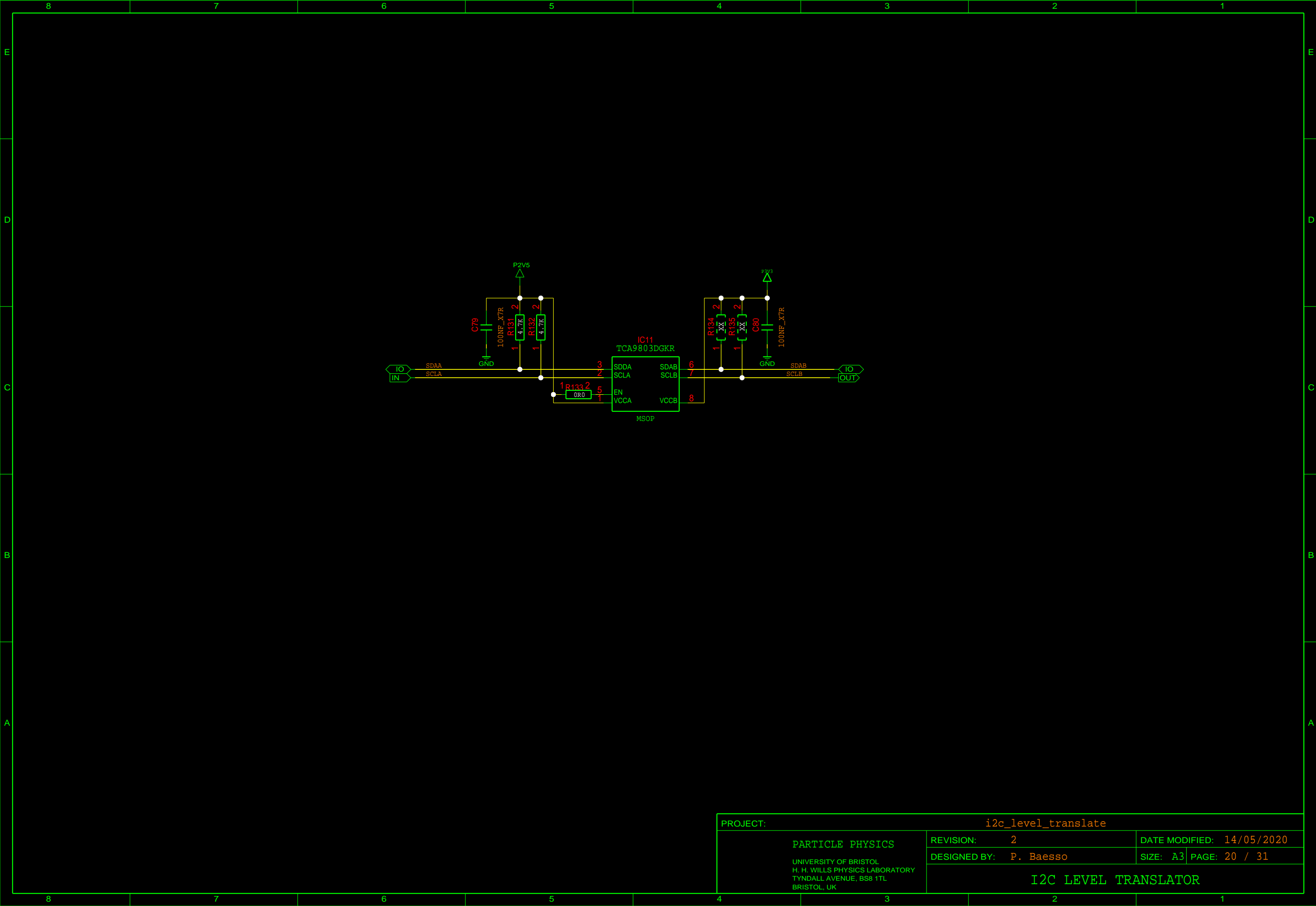
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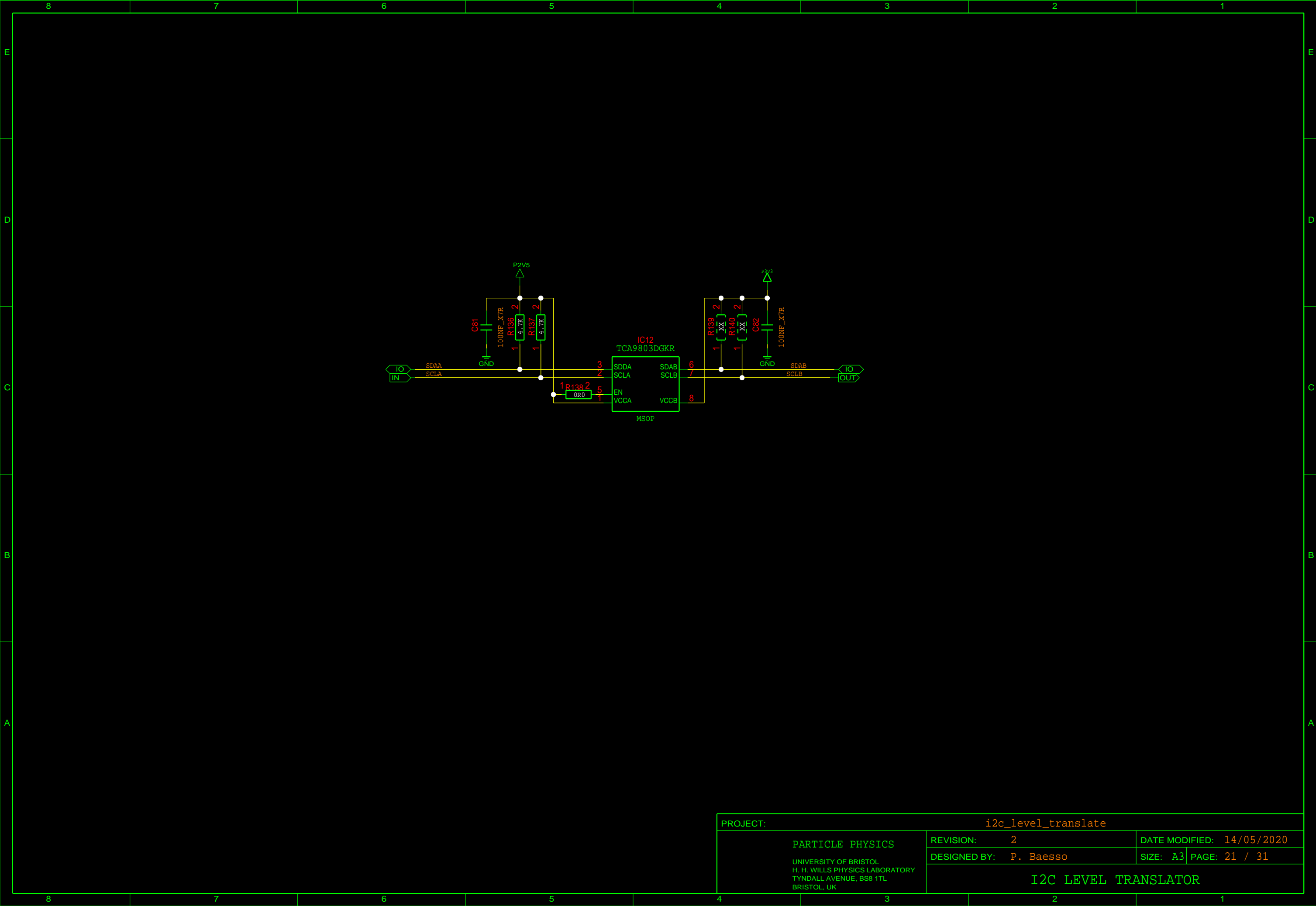
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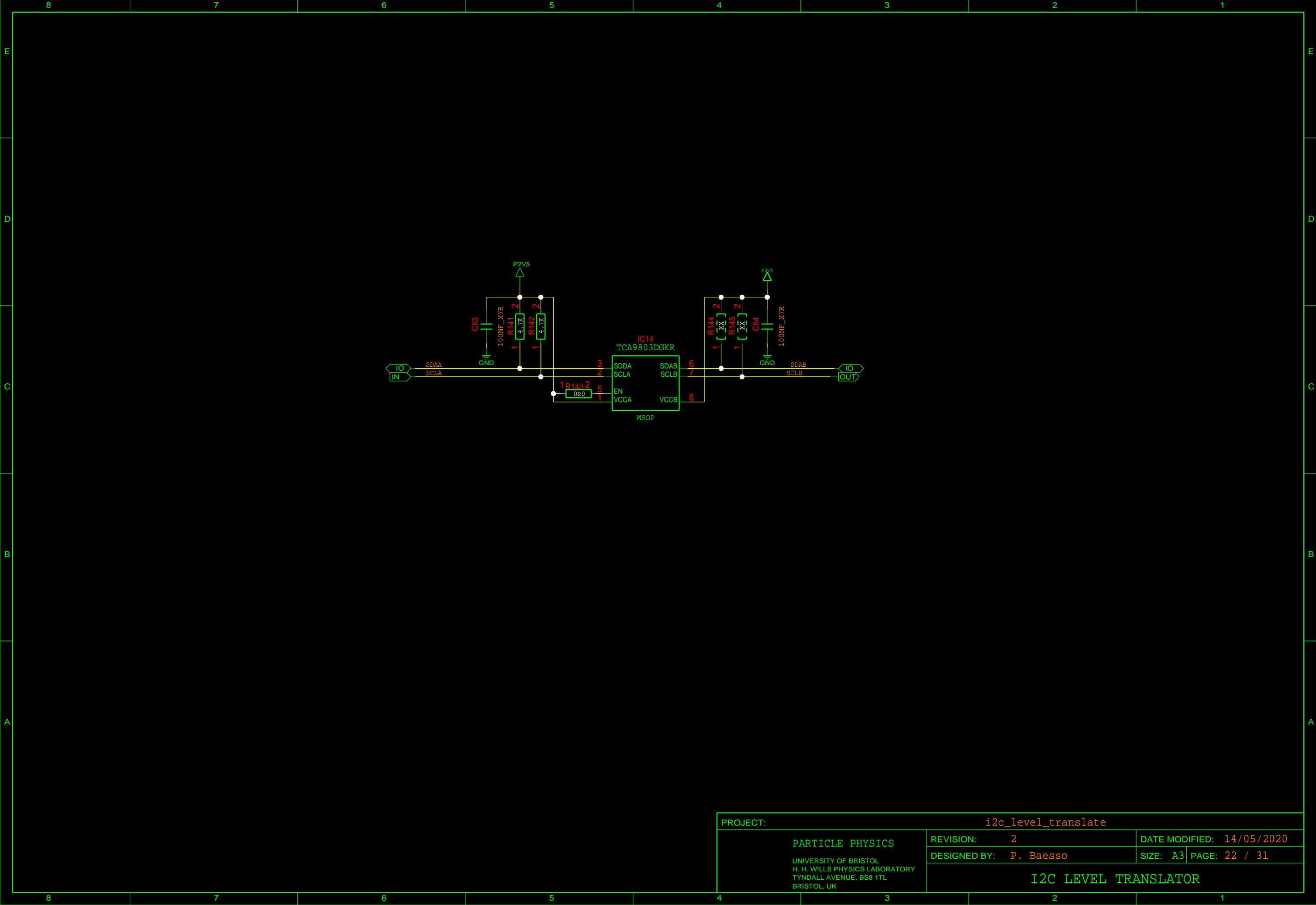
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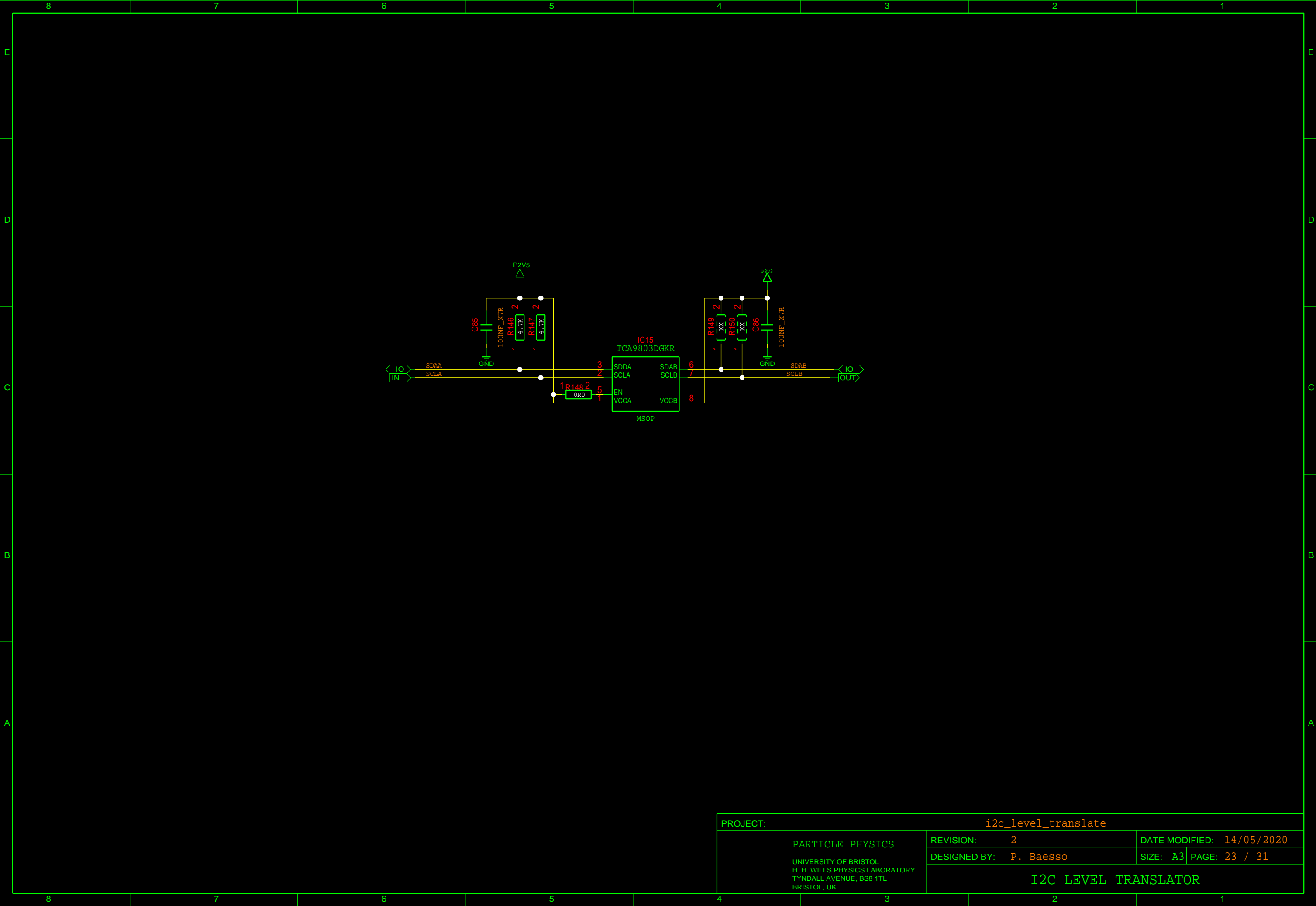


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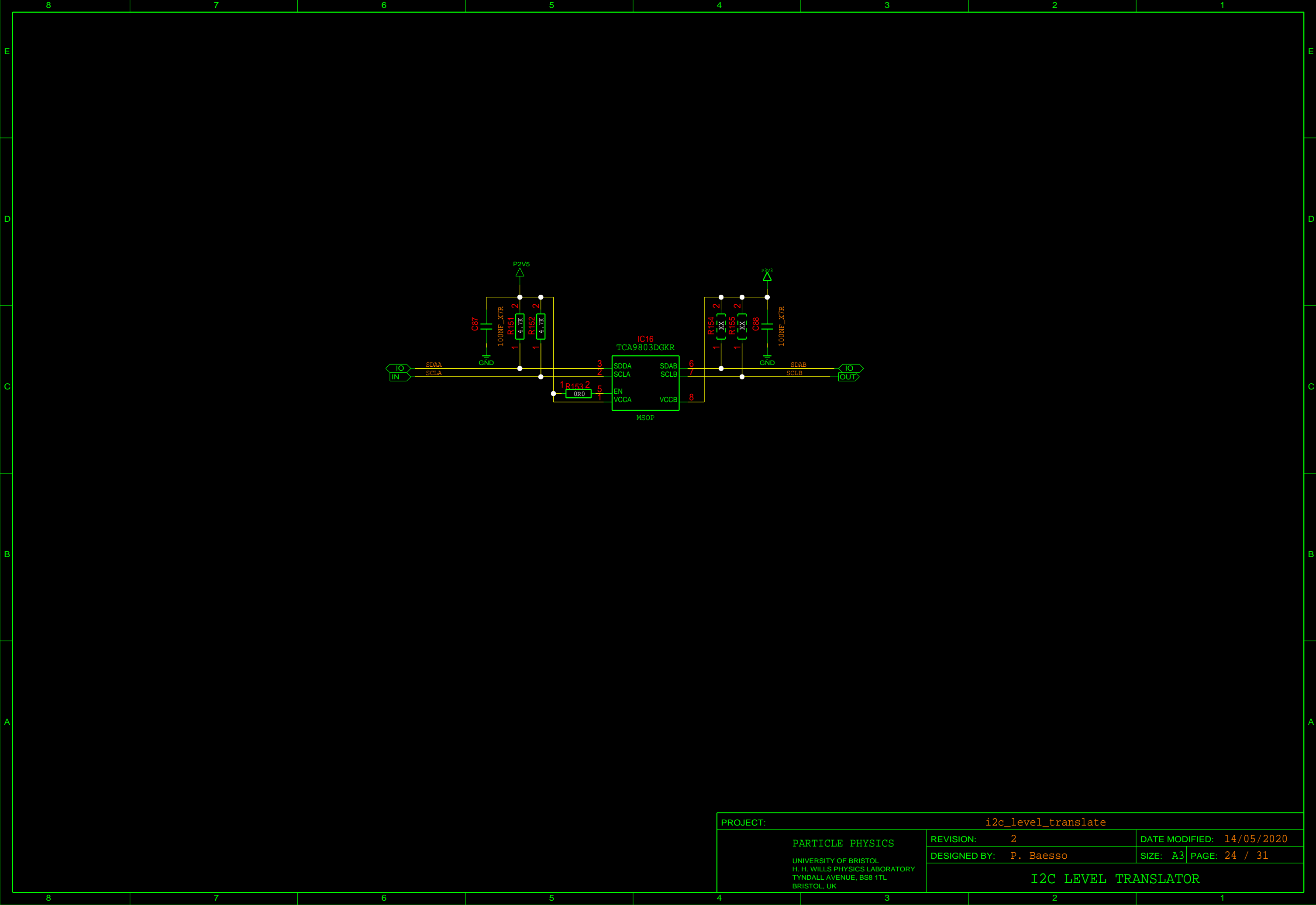


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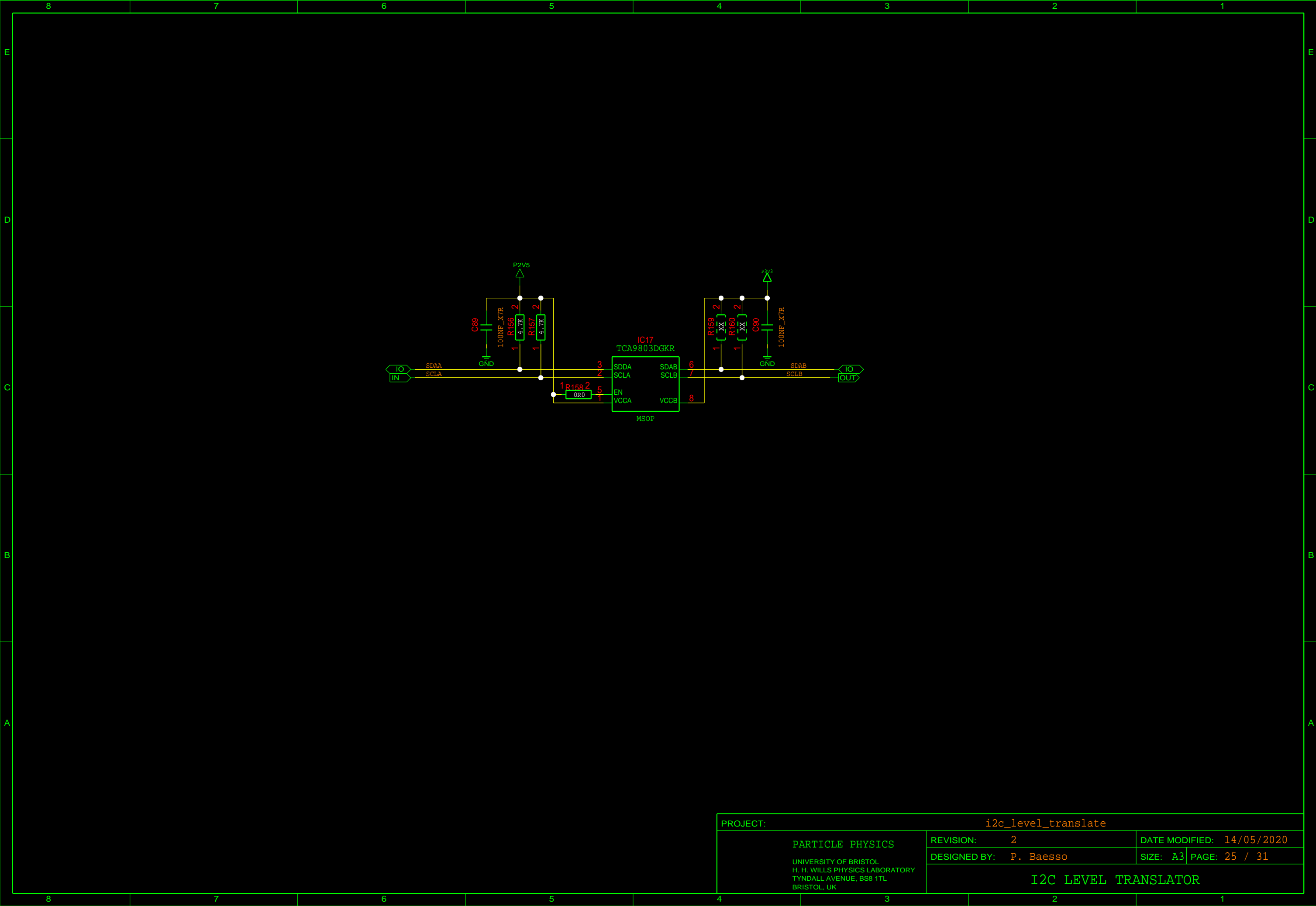


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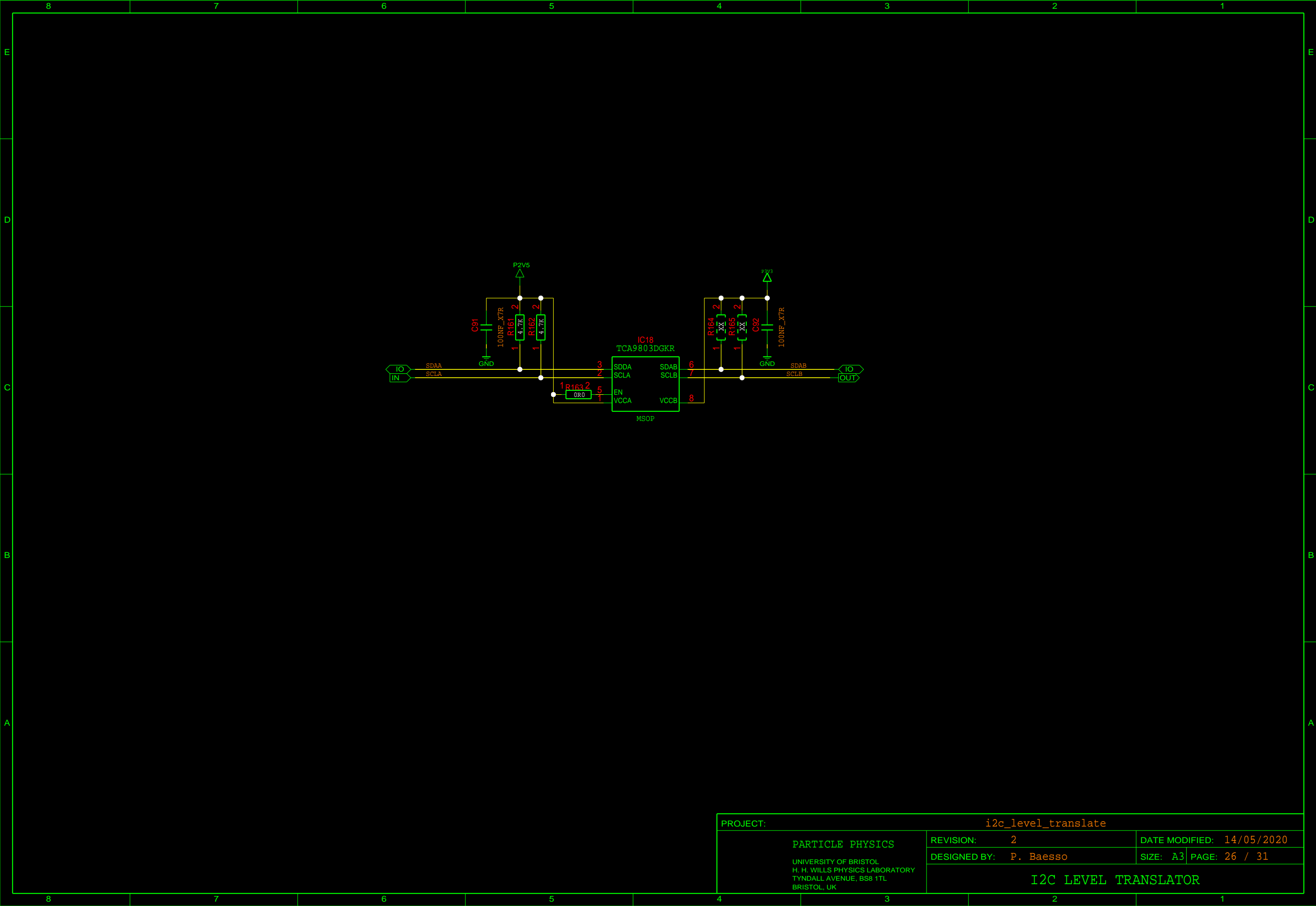


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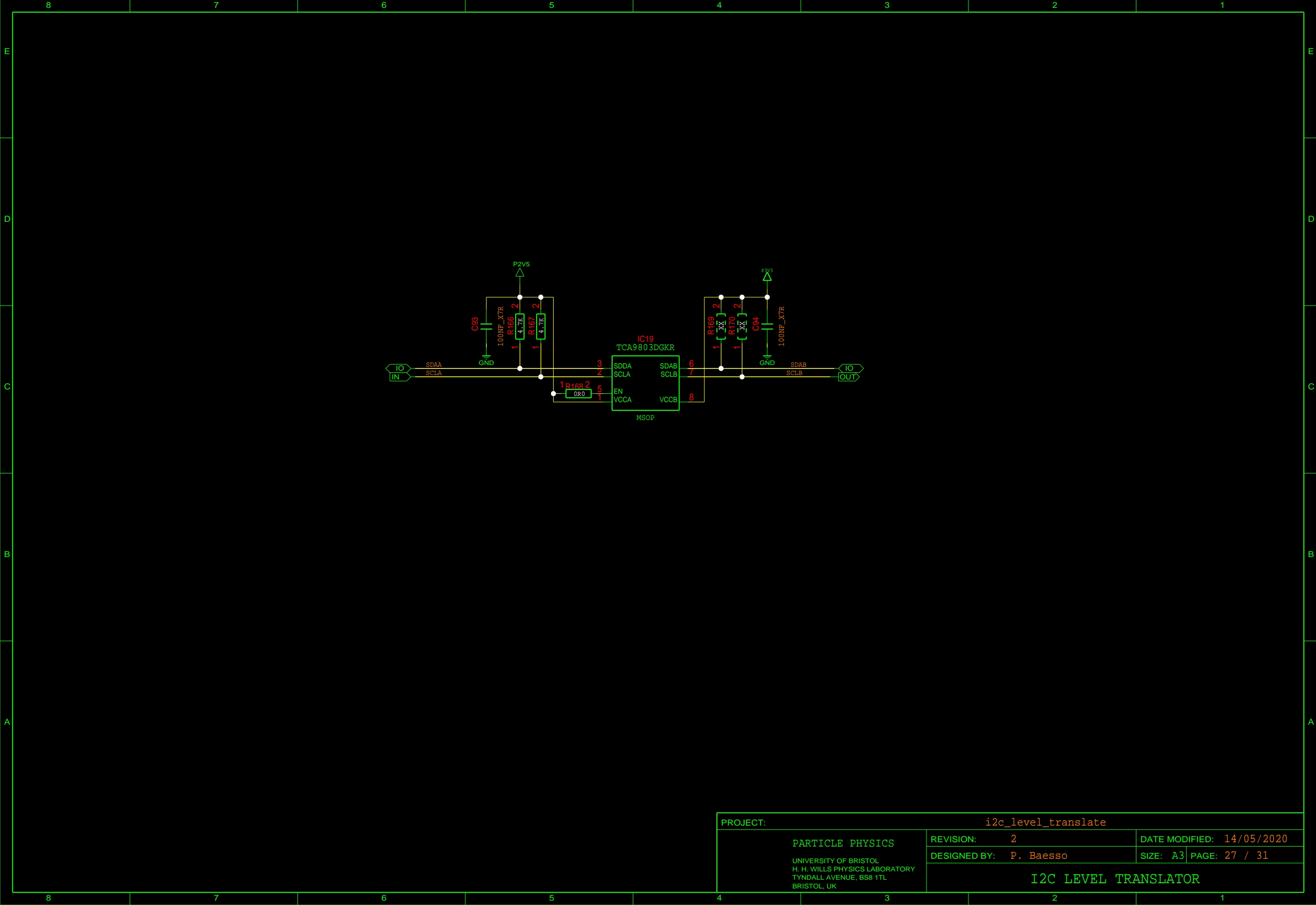




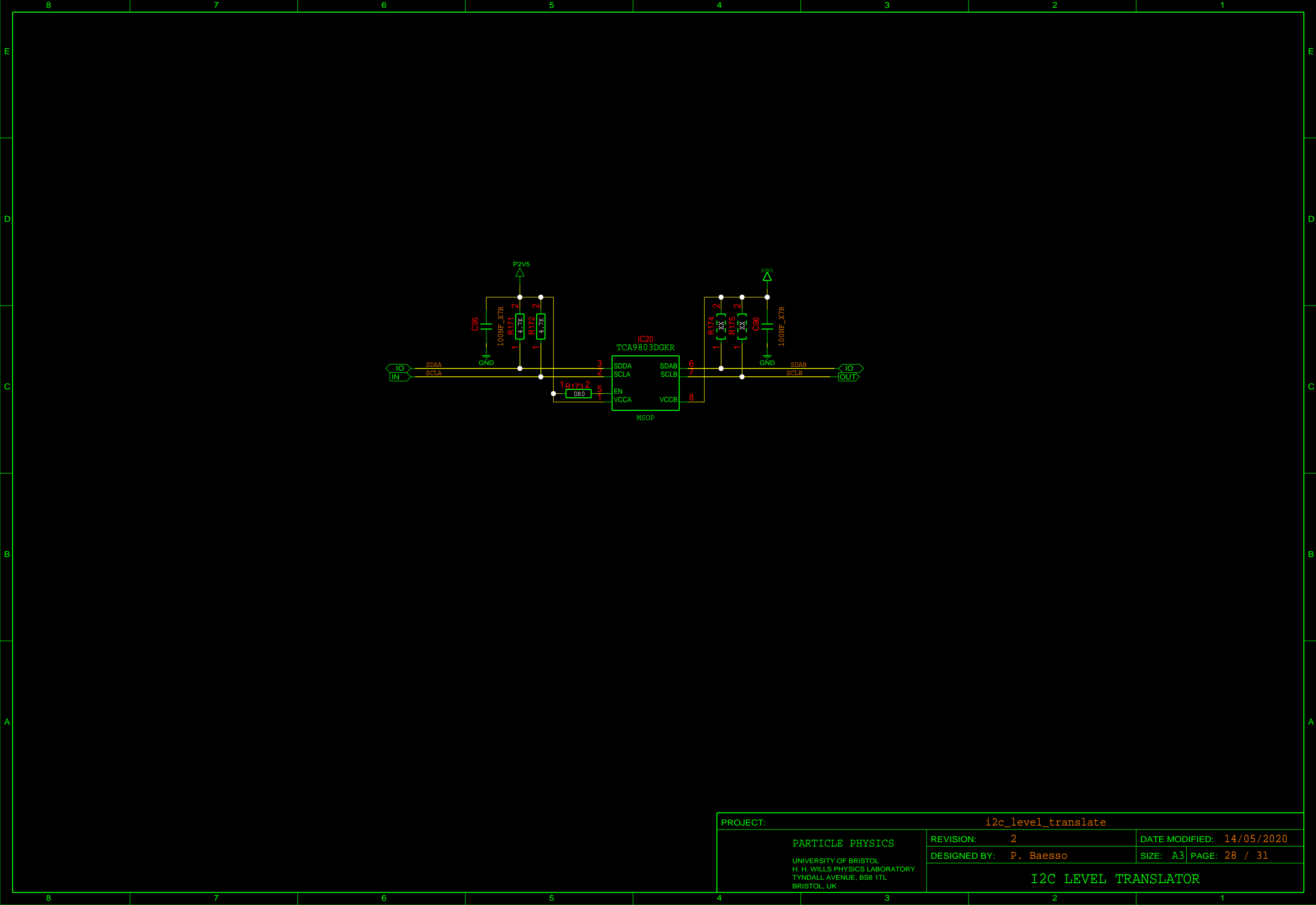
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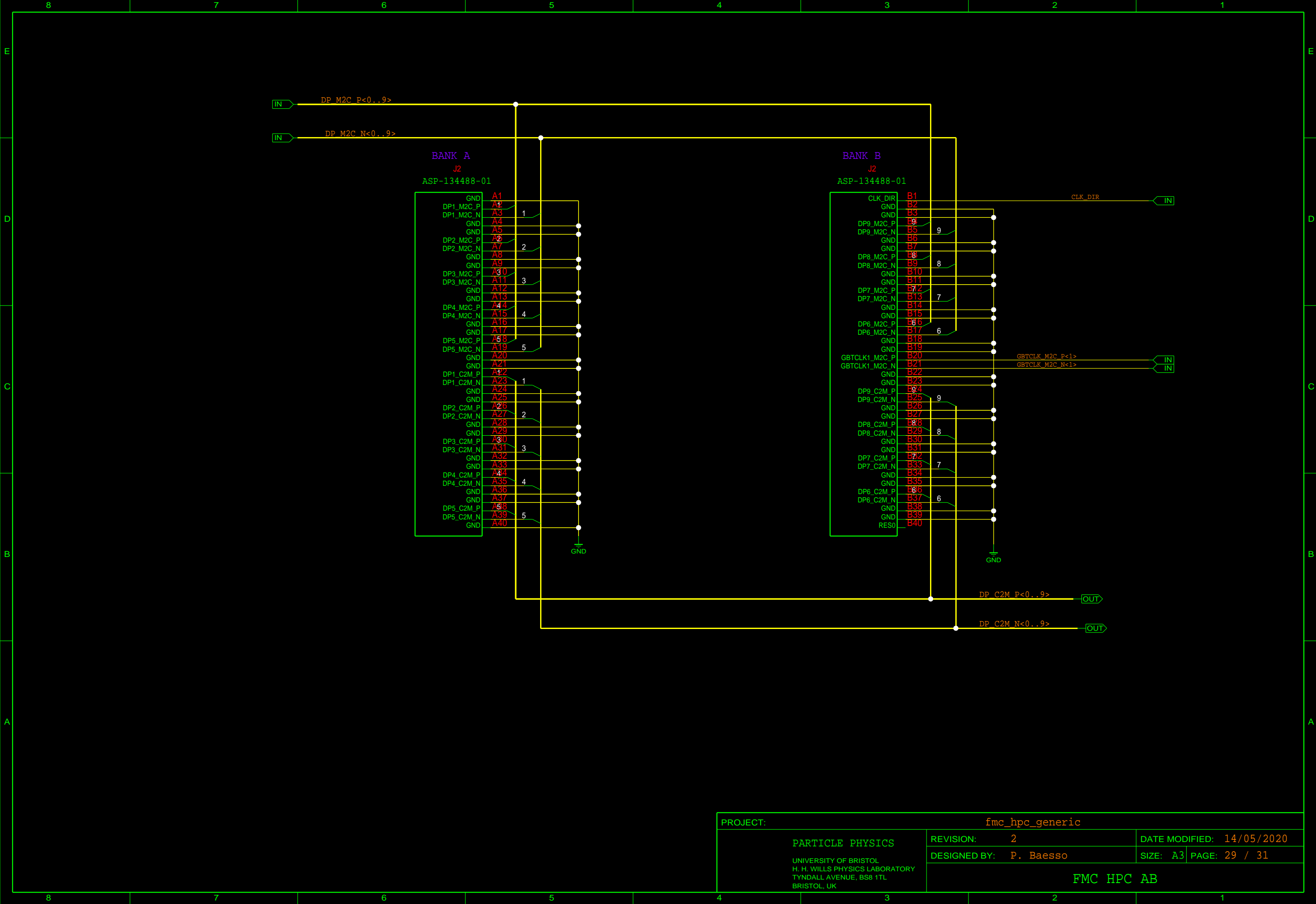
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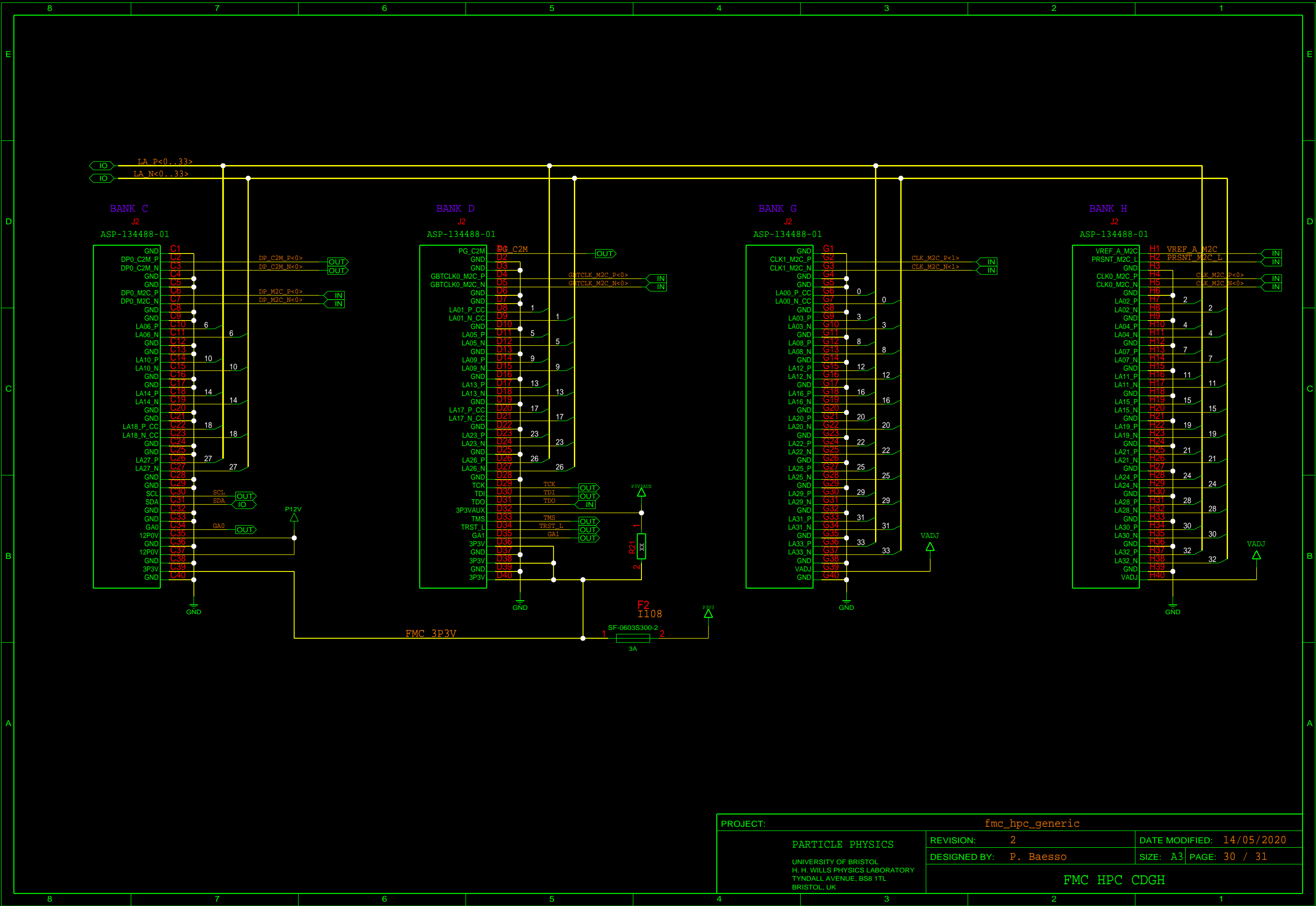


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PROJECT: fmc_hpc_generic			
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