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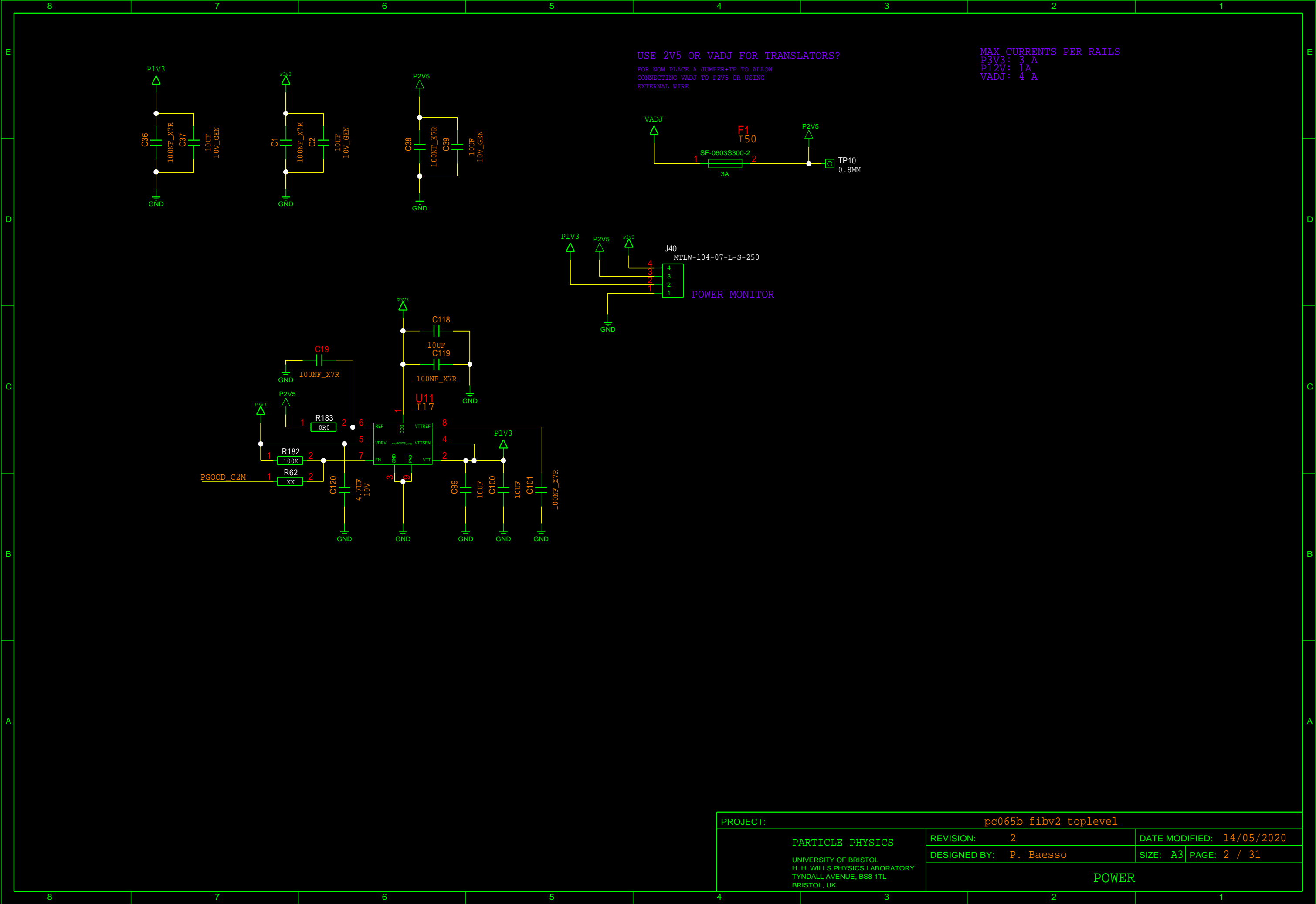
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28	I2C LEVEL TRANSLATOR	i2c_level_translate
29	AB	fmc_hpc_generic
30	CDGH	fmc_hpc_generic

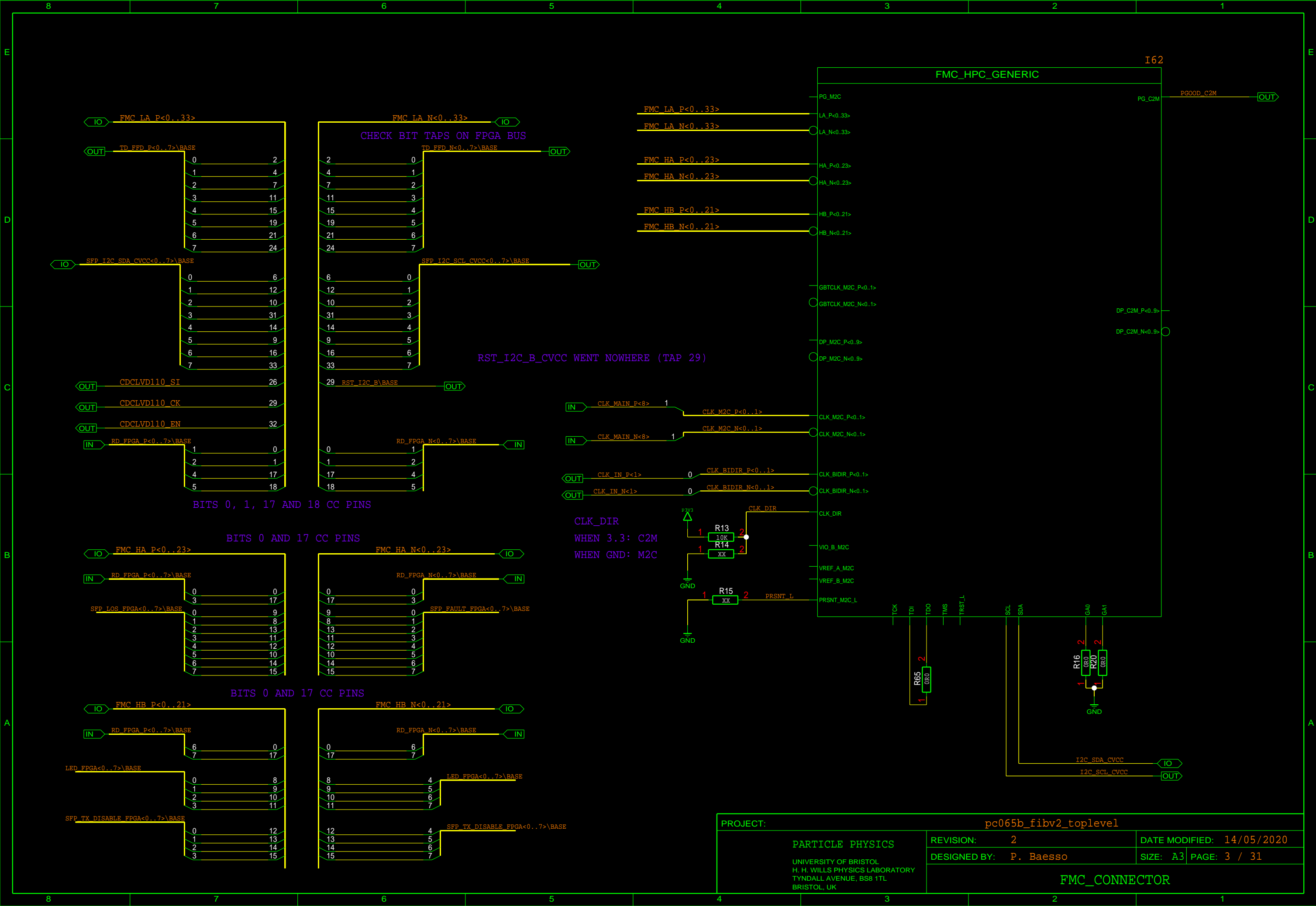
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CLOCK 0X68 (0X69, 0X6A, 0X6B)  
EXPANDER 0X74  
EXPANDER 0X75

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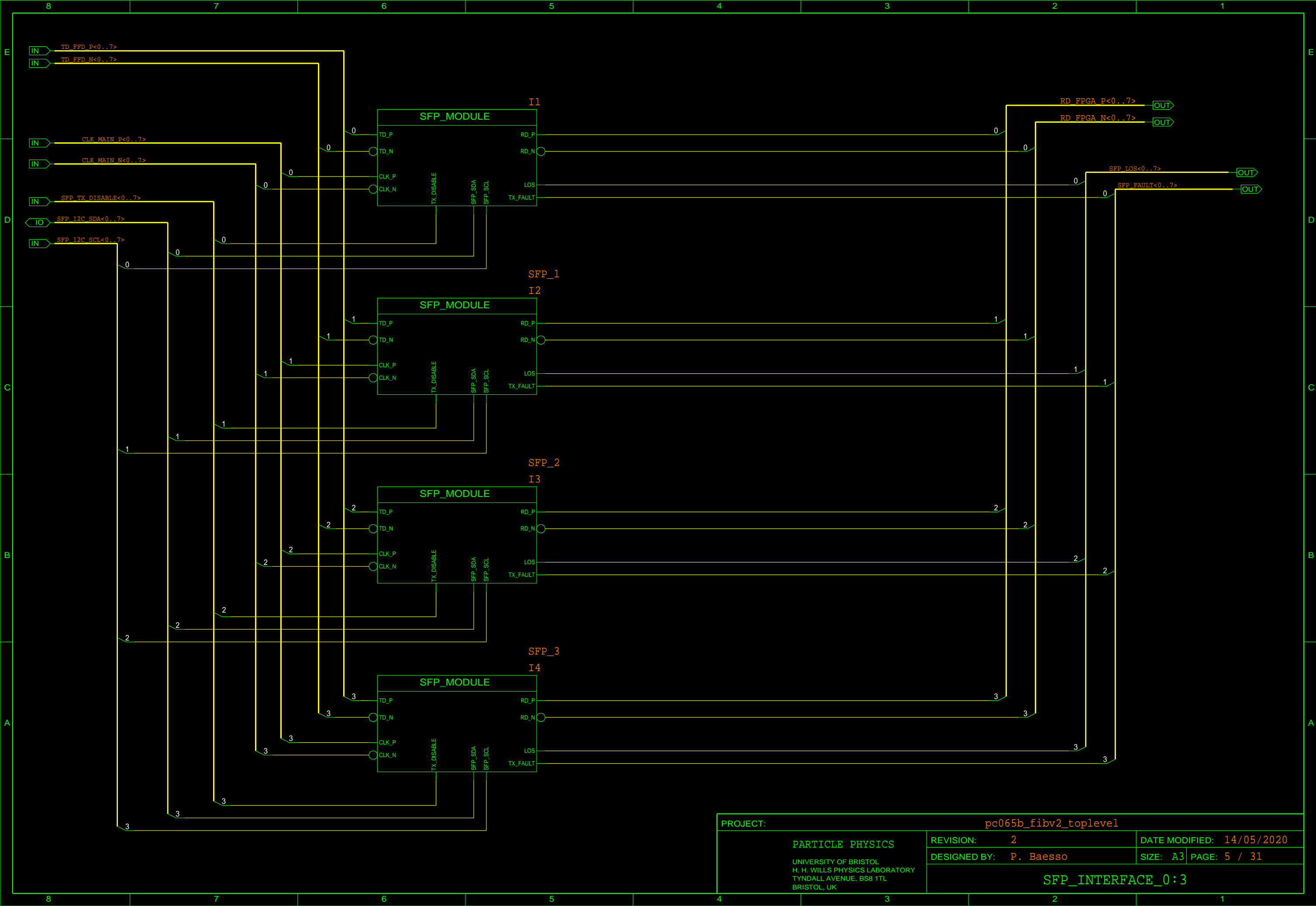
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PROJECT:		pc065b_fibv2_toplevel	
PARTICLE PHYSICS  UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORATORY TYNDALL AVENUE, BS8 1TL BRISTOL, UK	REVISION:	2	DATE MODIFIED: 14/05/2020
	DESIGNED BY:	P. Baesso	SIZE: A3 PAGE: 1 / 31
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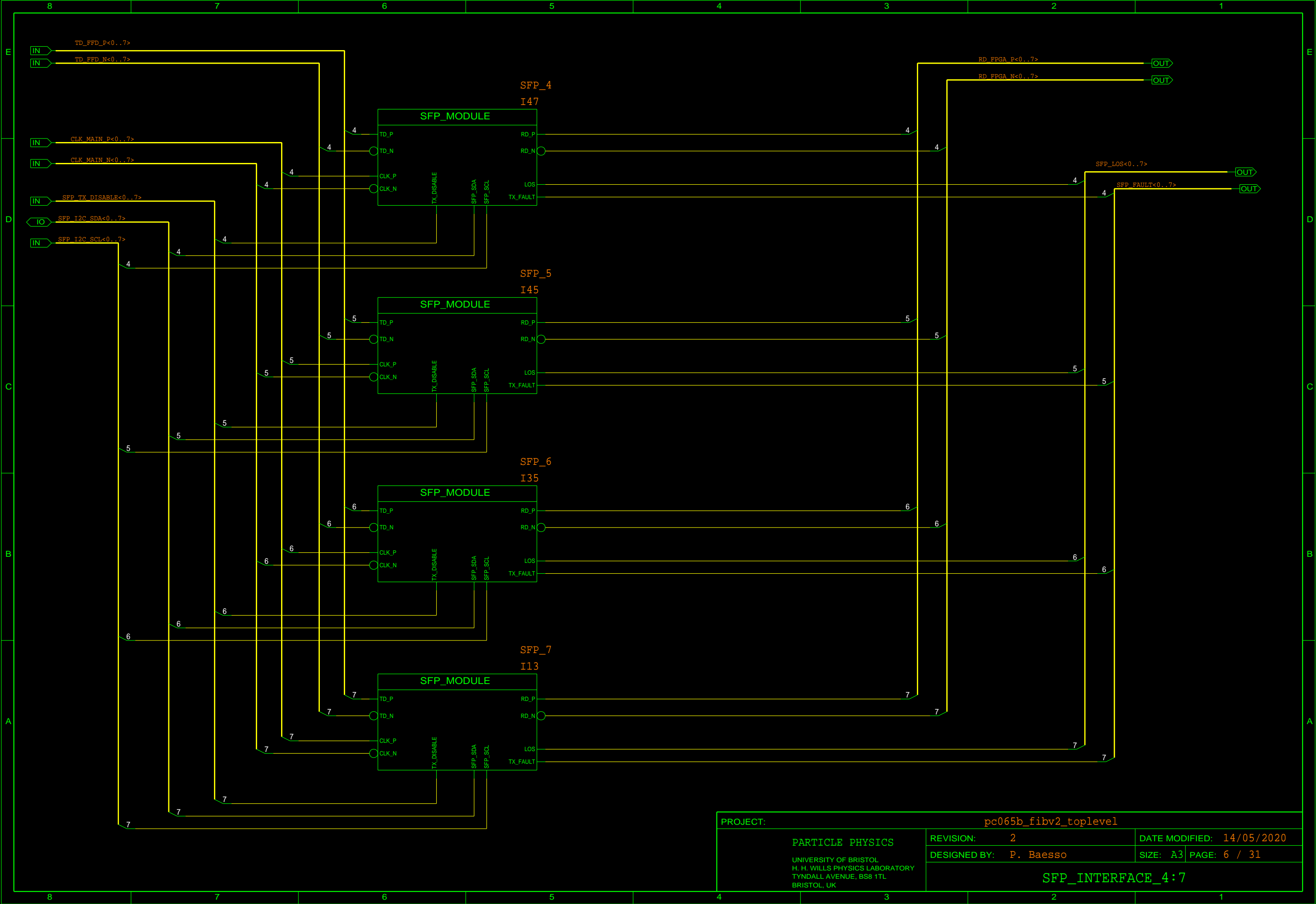




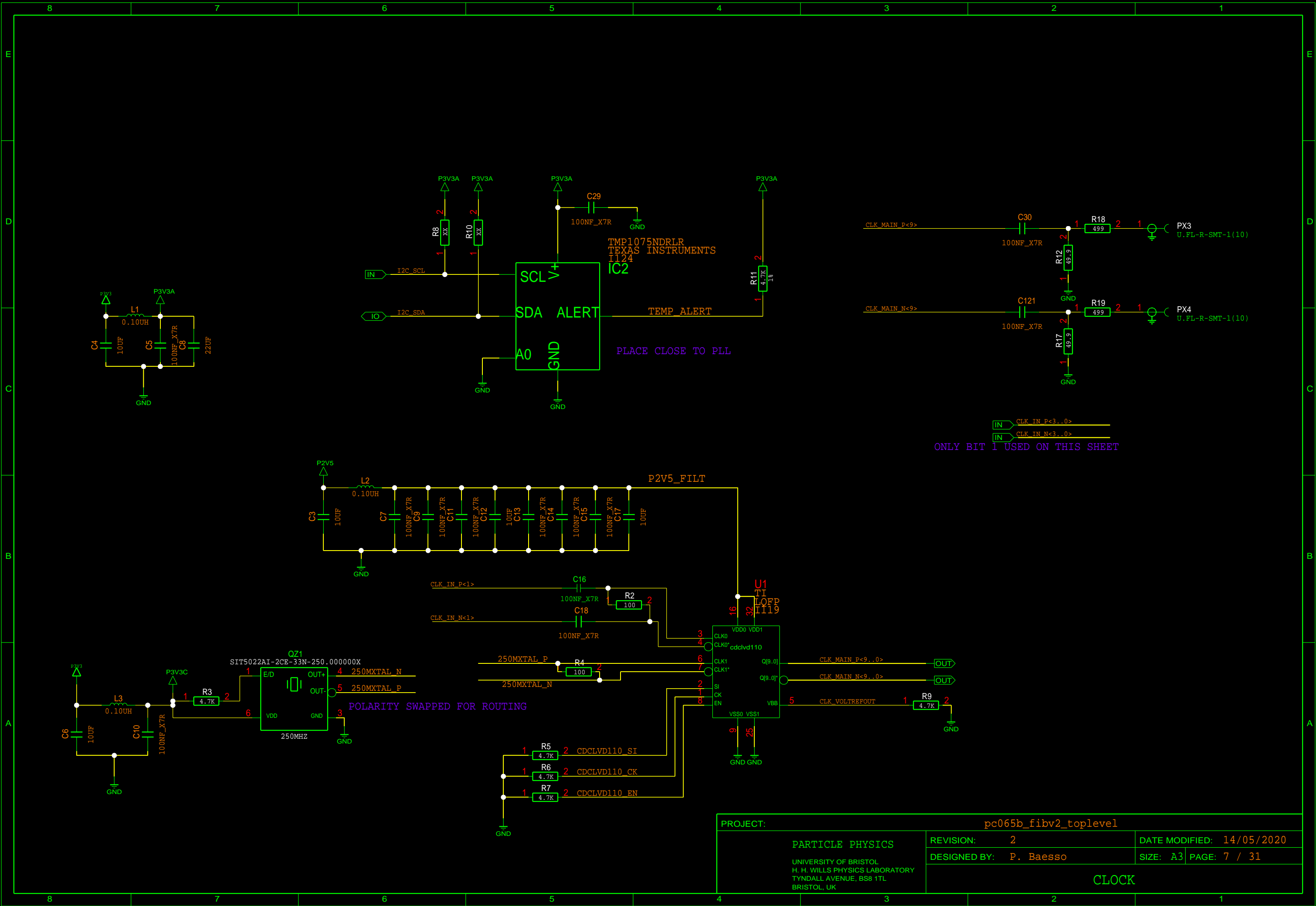


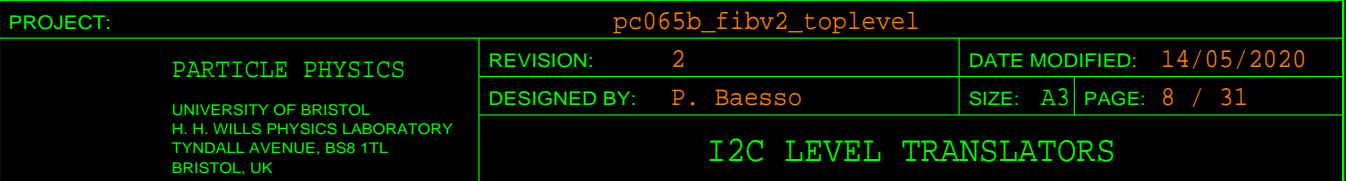


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	SFP_INTERFACE_0:3		

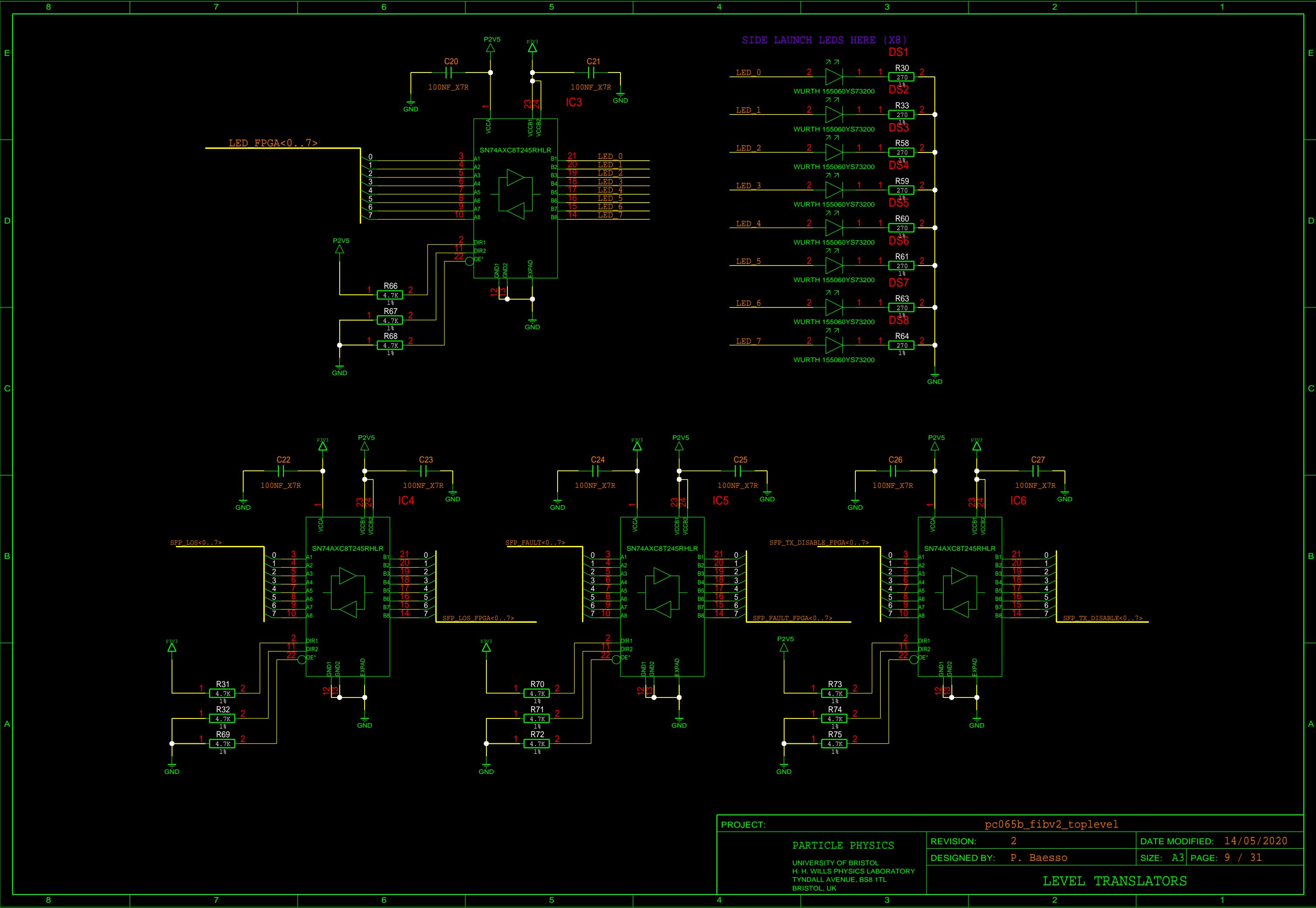


PROJECT:		pc065b_fibv2_toplevel	
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		LEVEL TRANSLATORS			



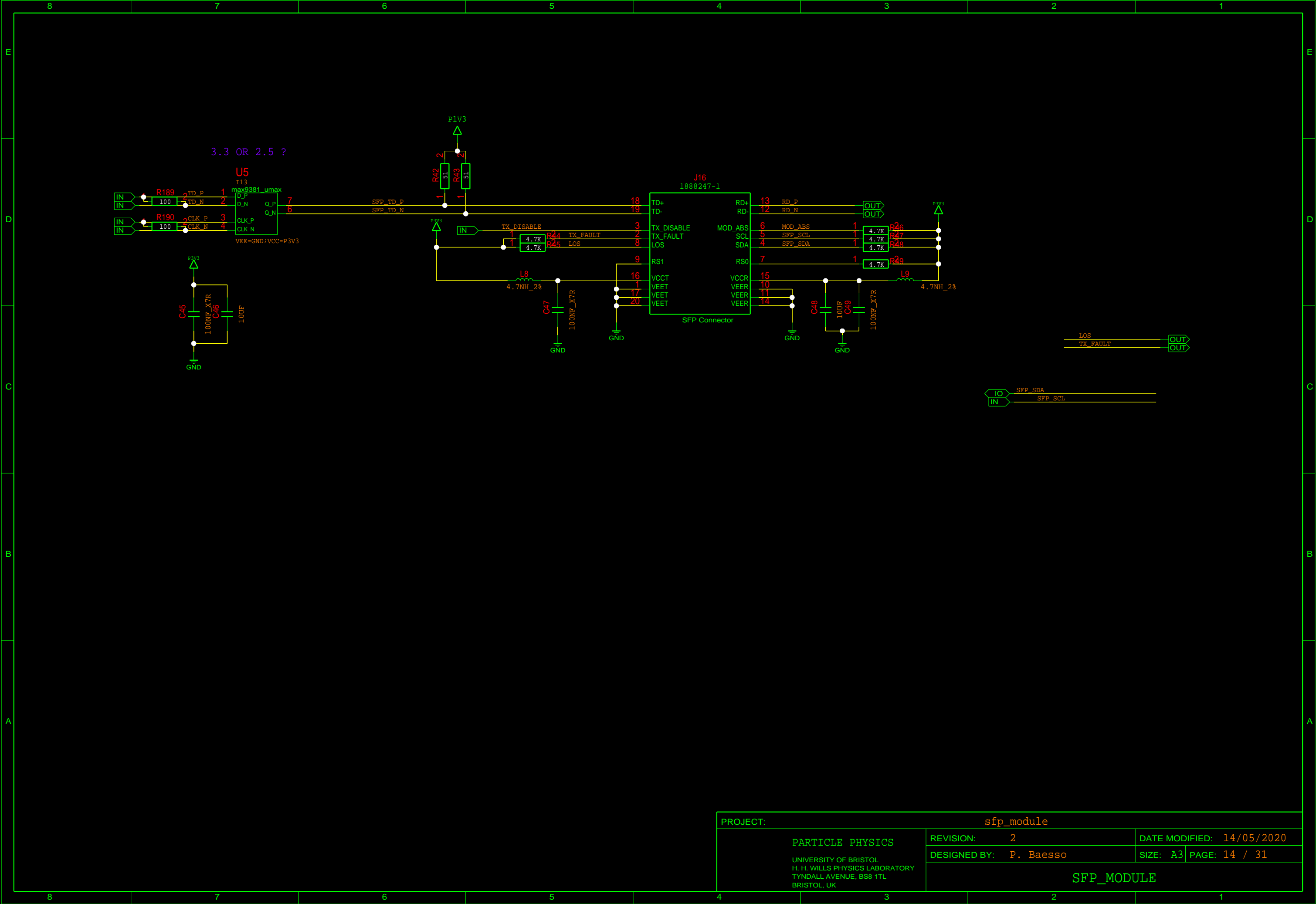




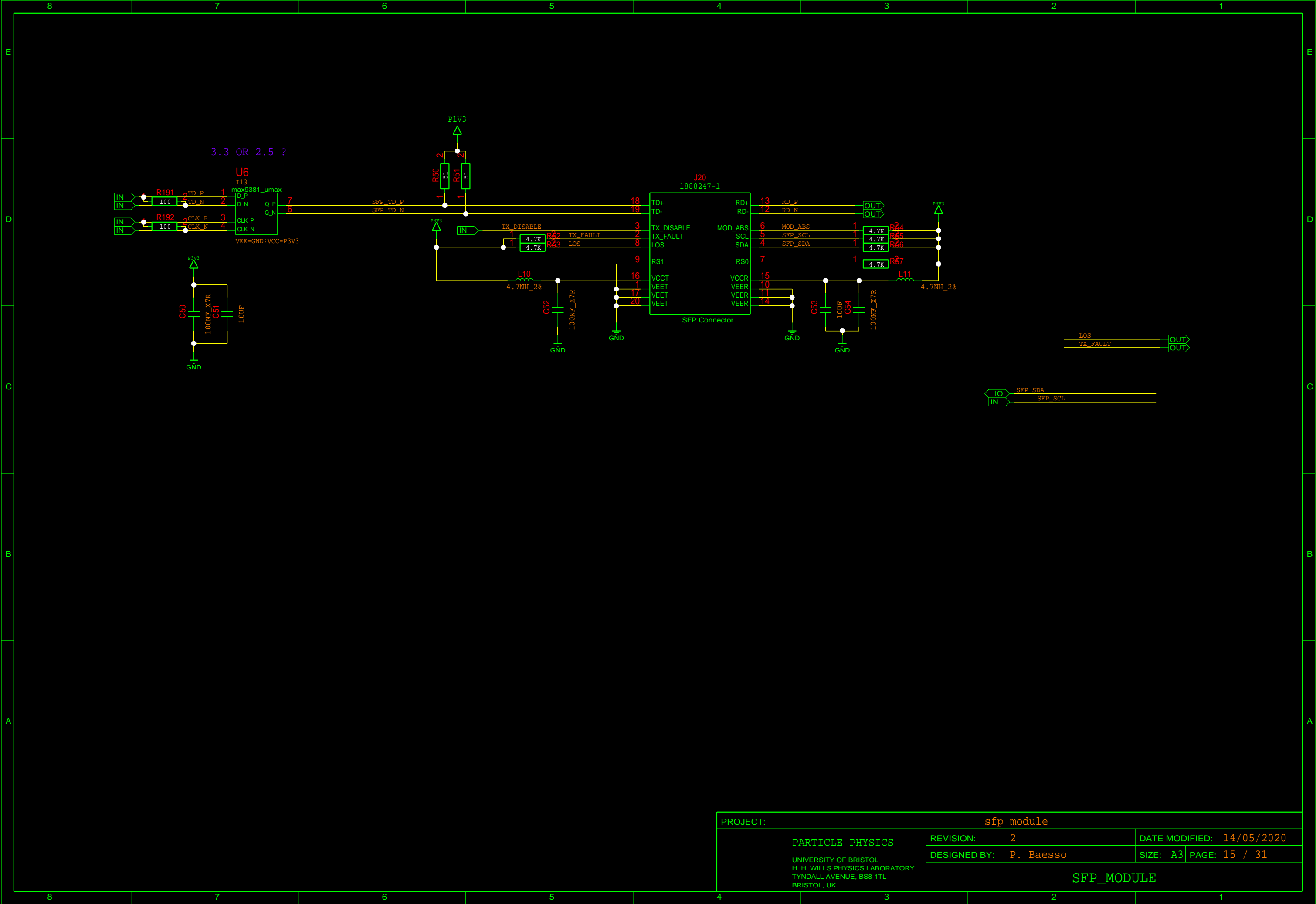
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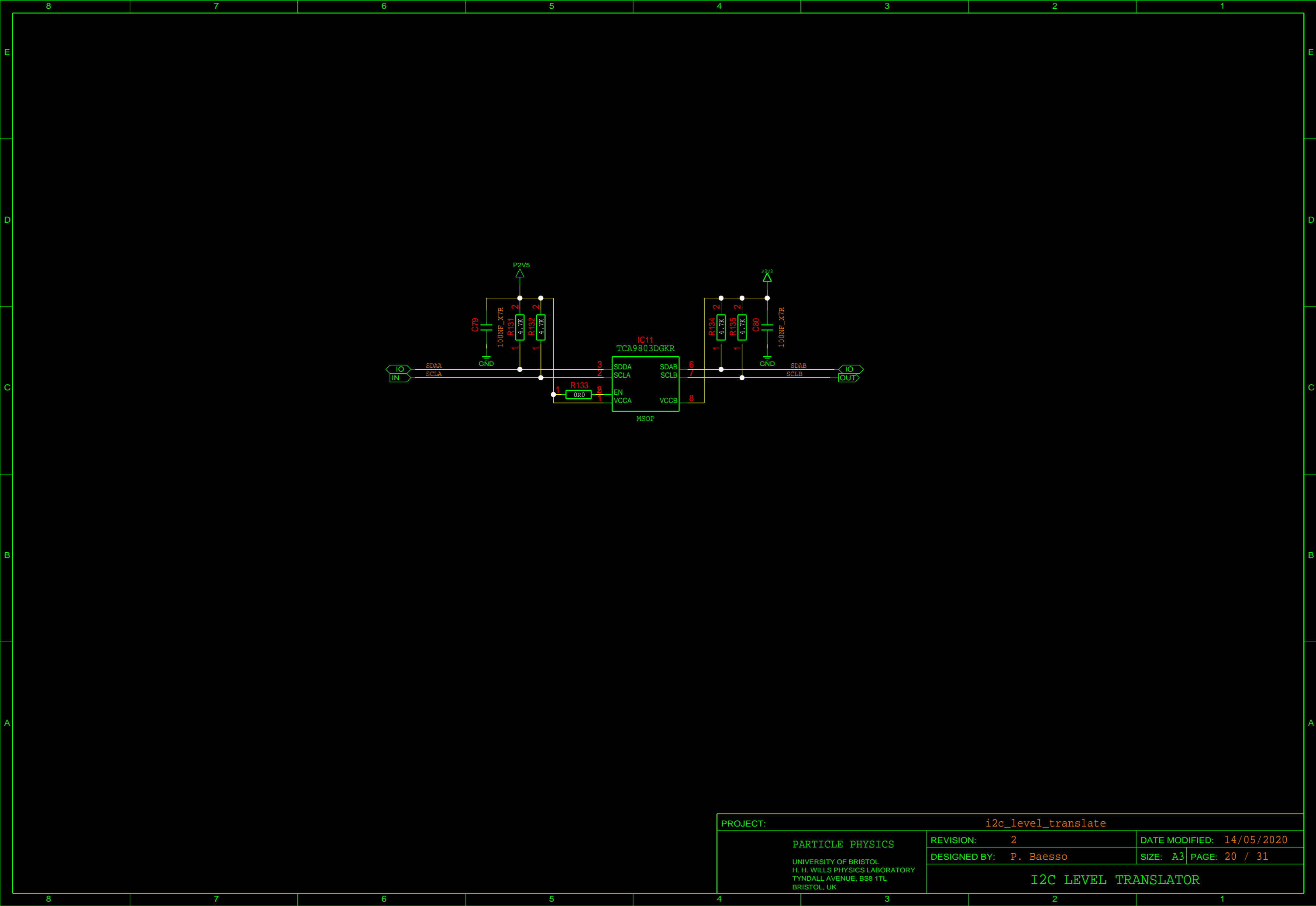


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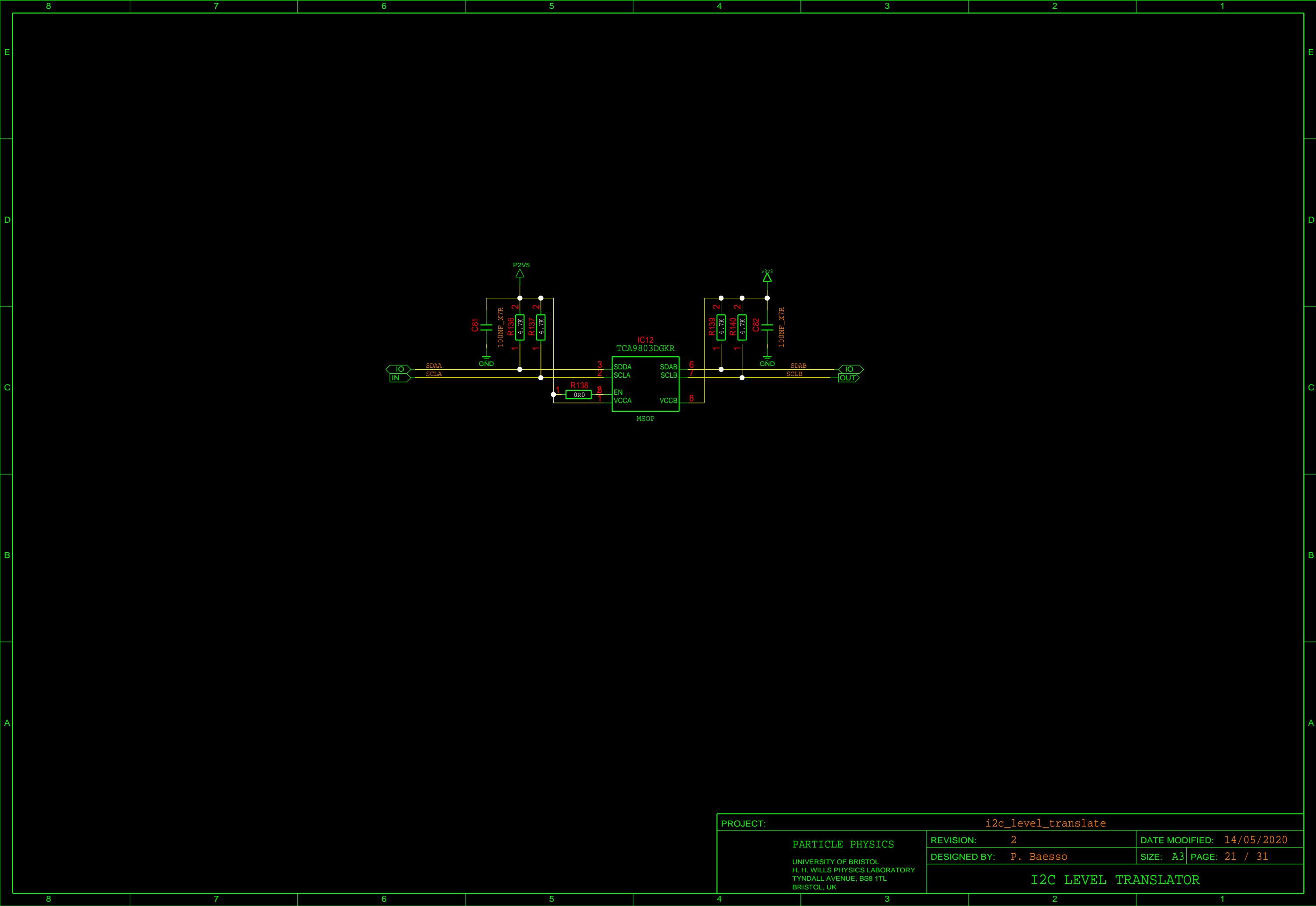




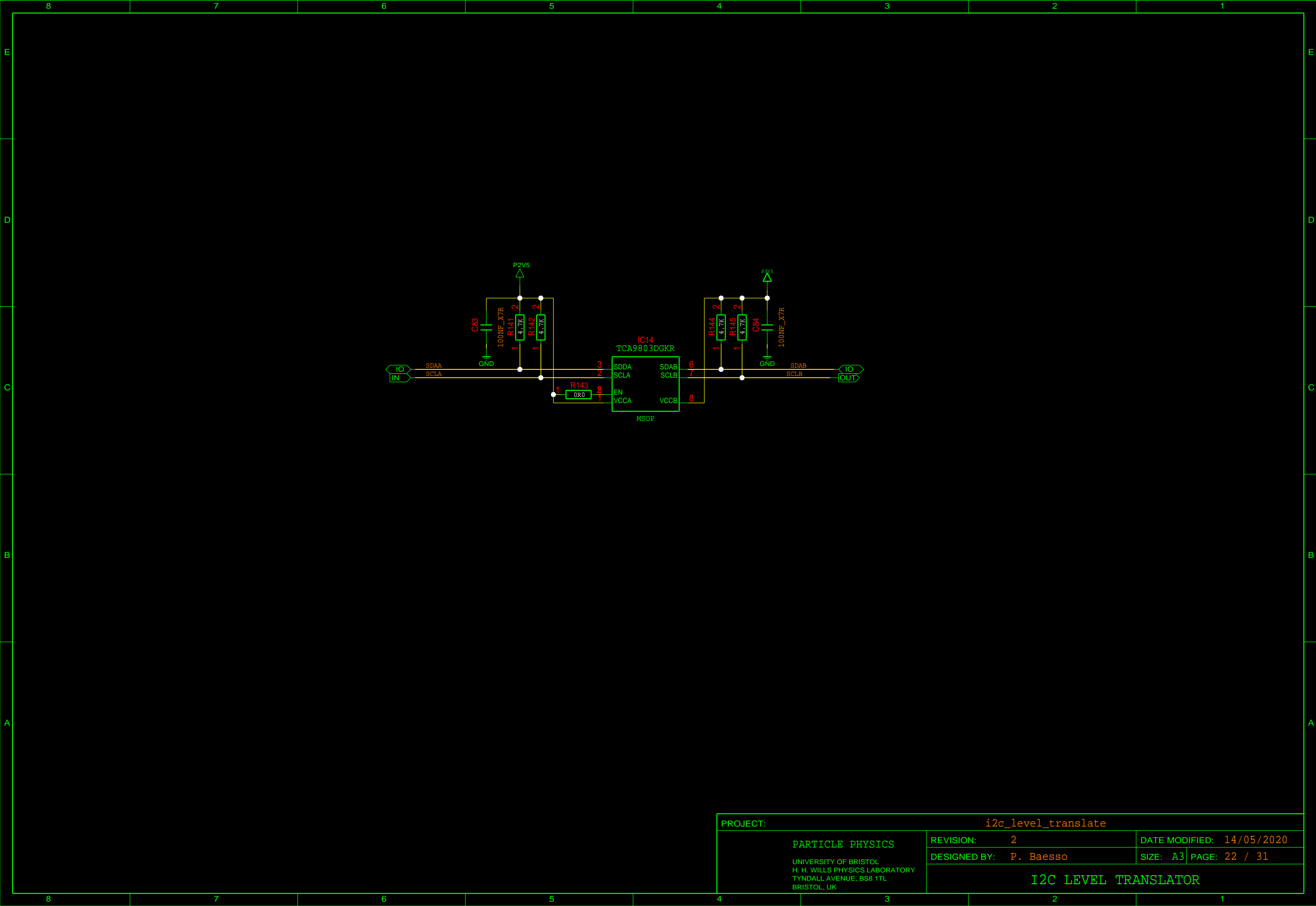
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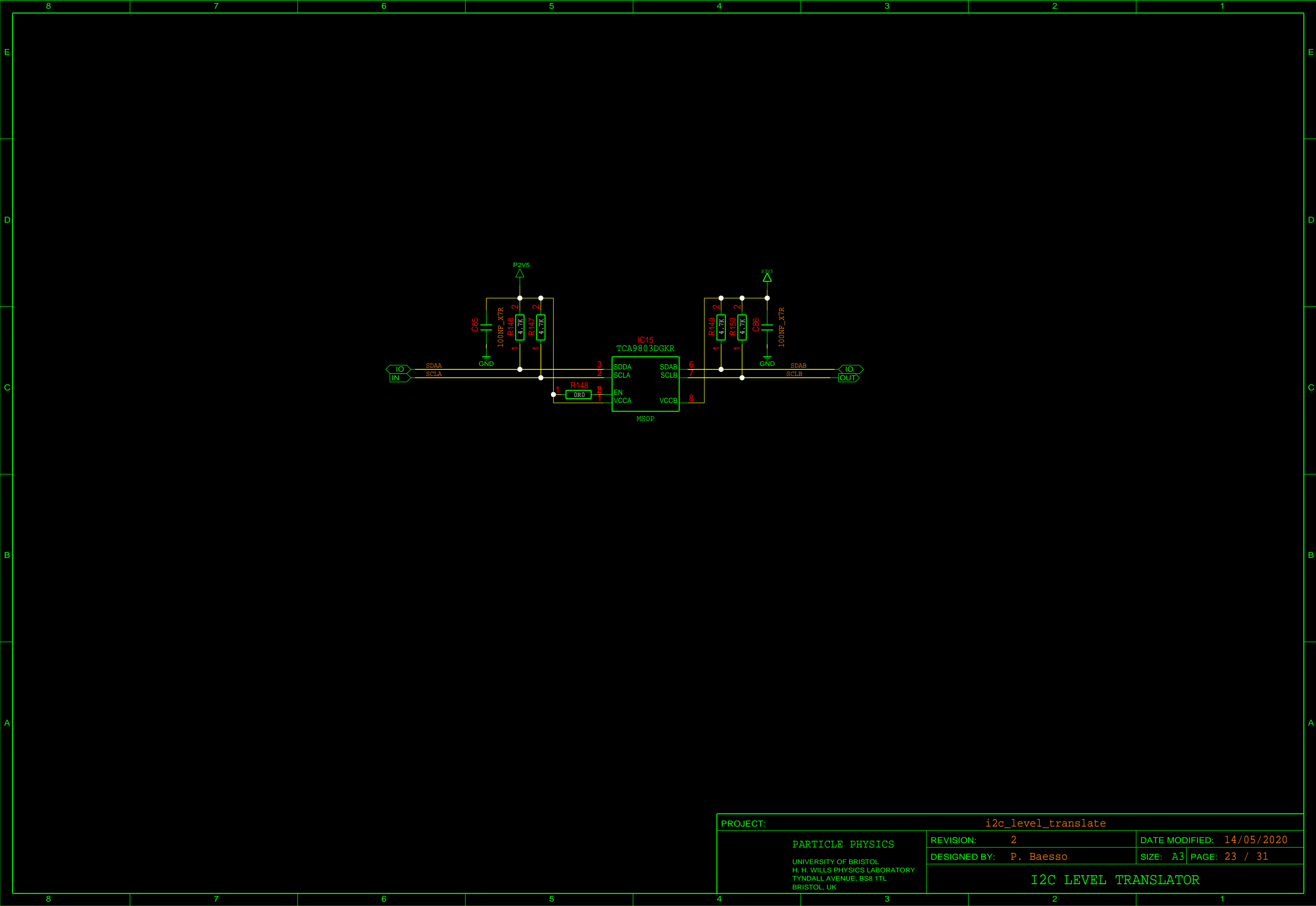
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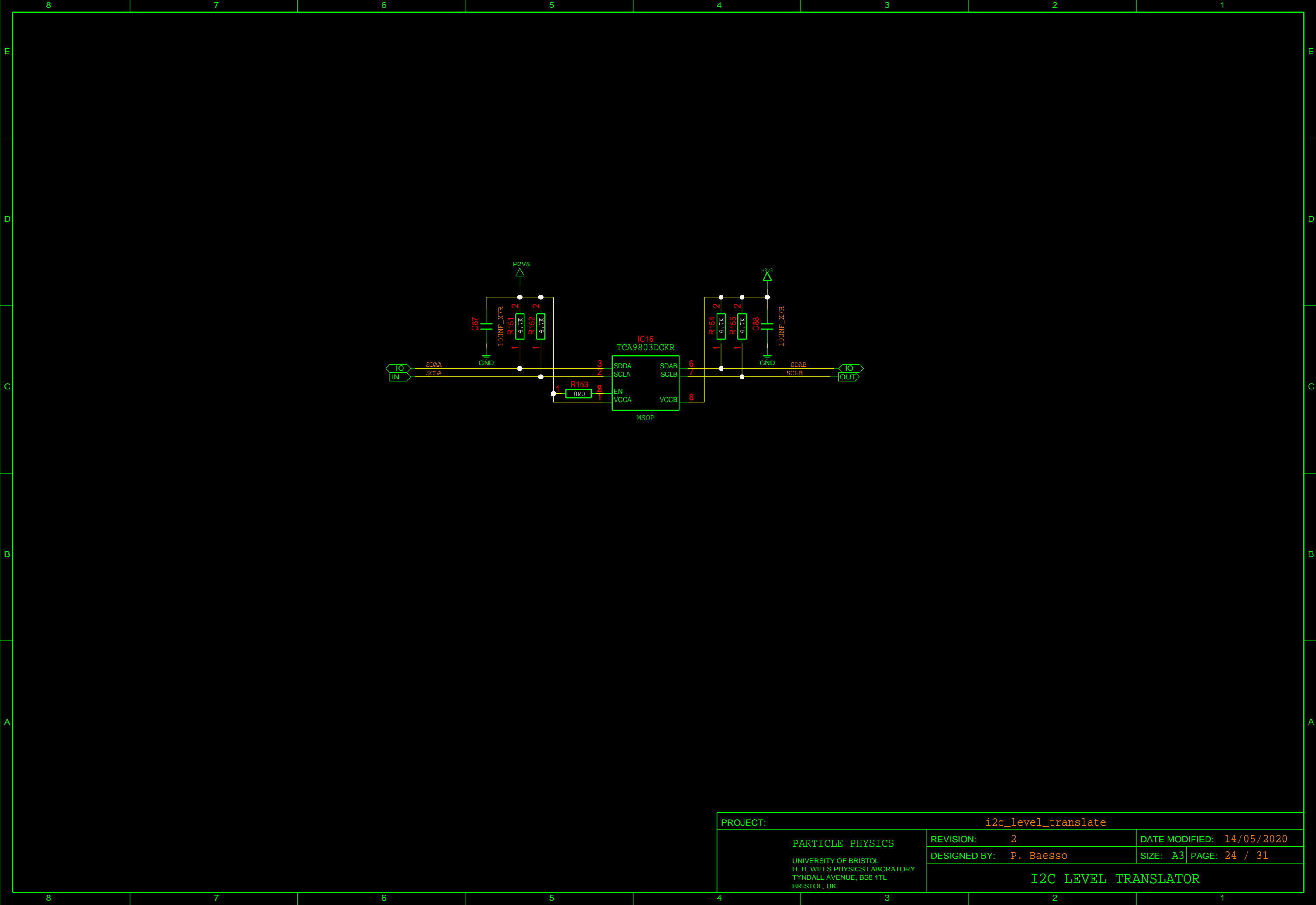
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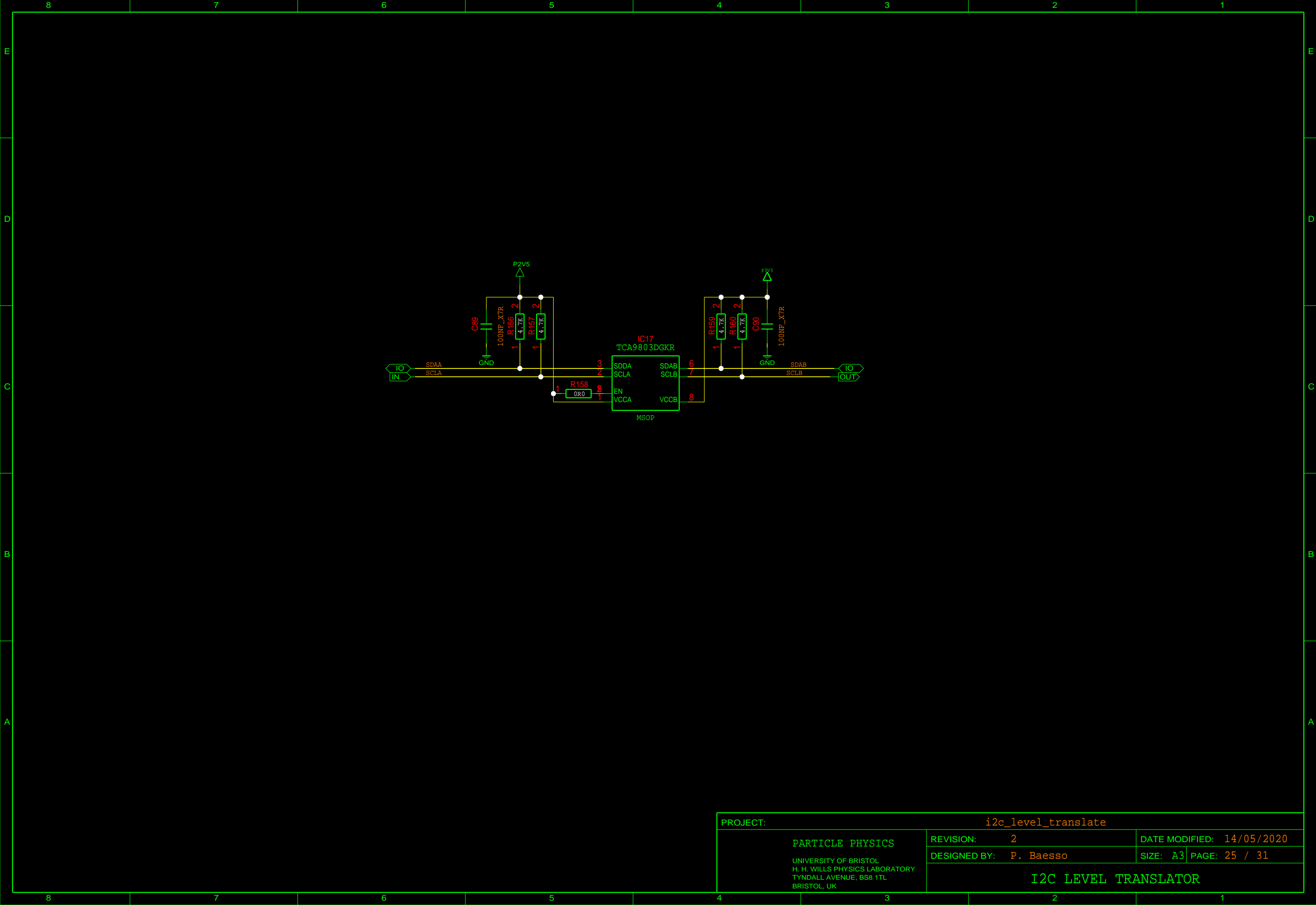


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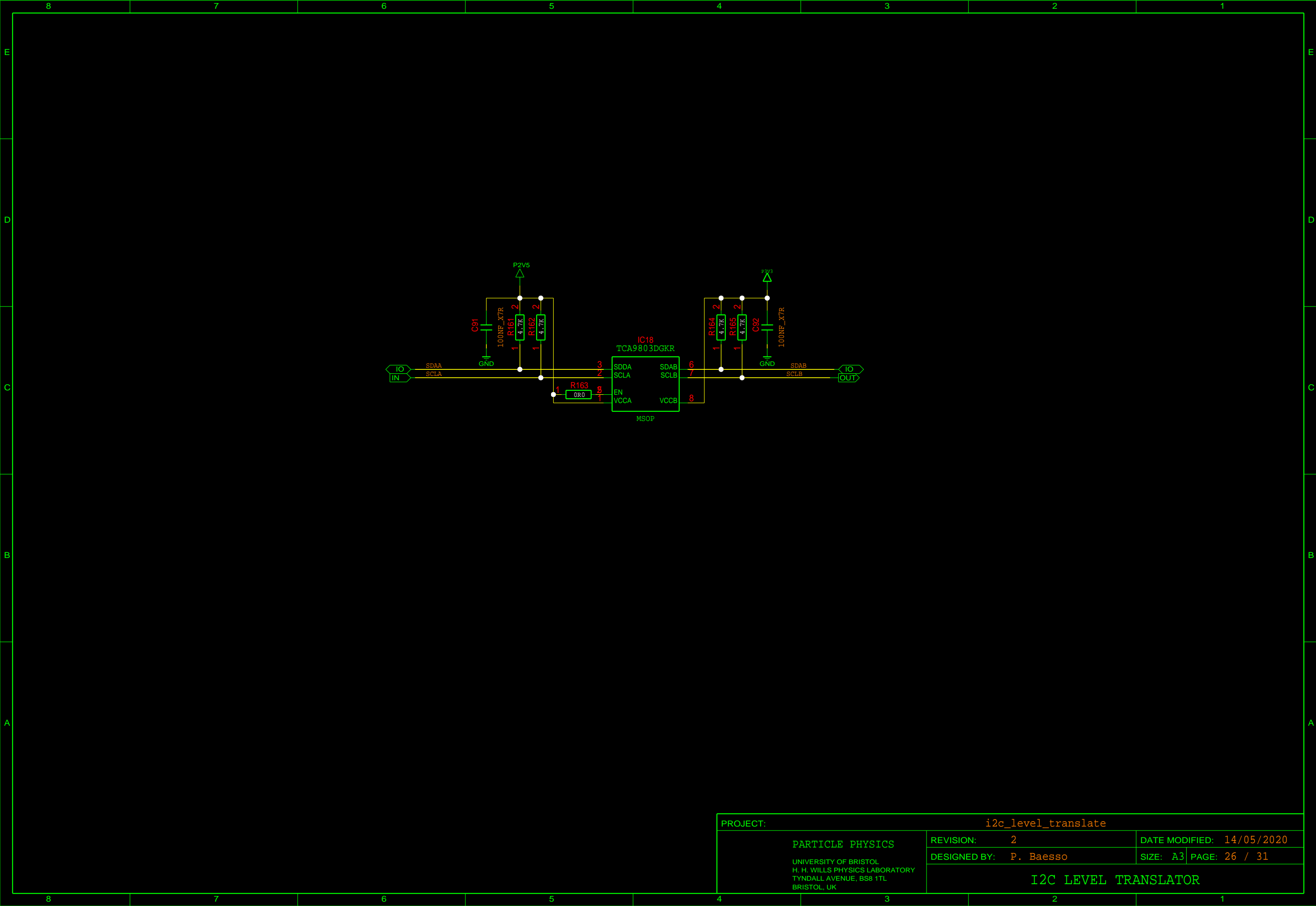


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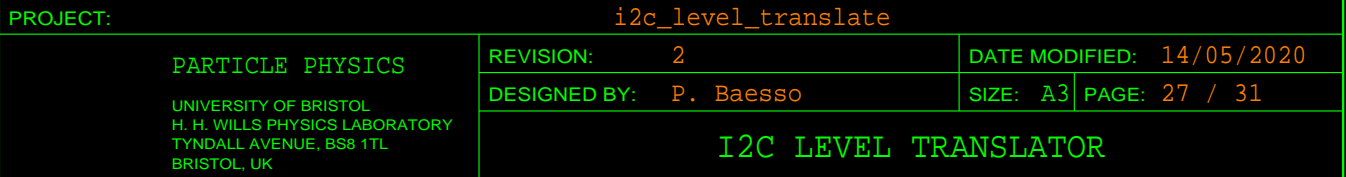


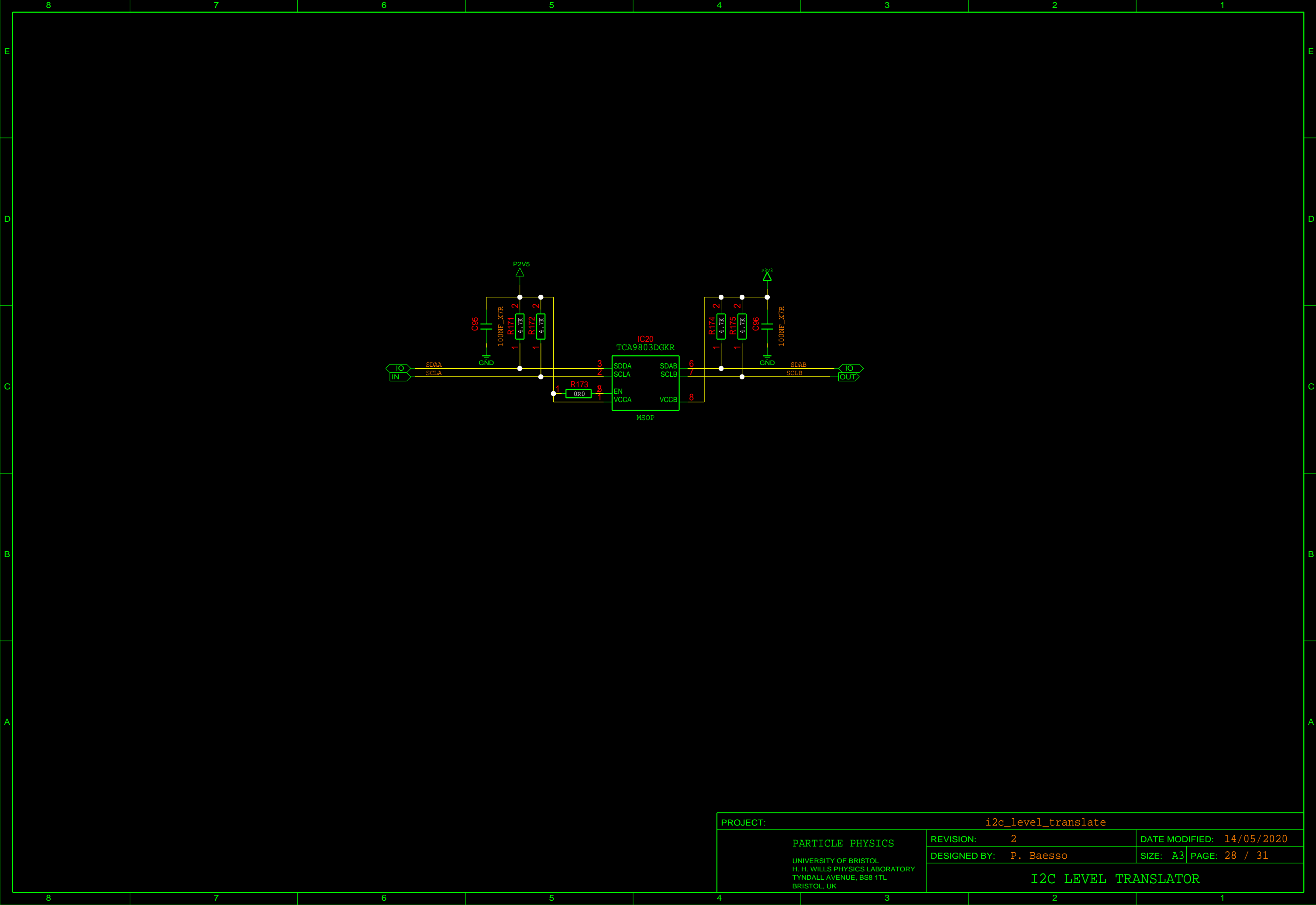


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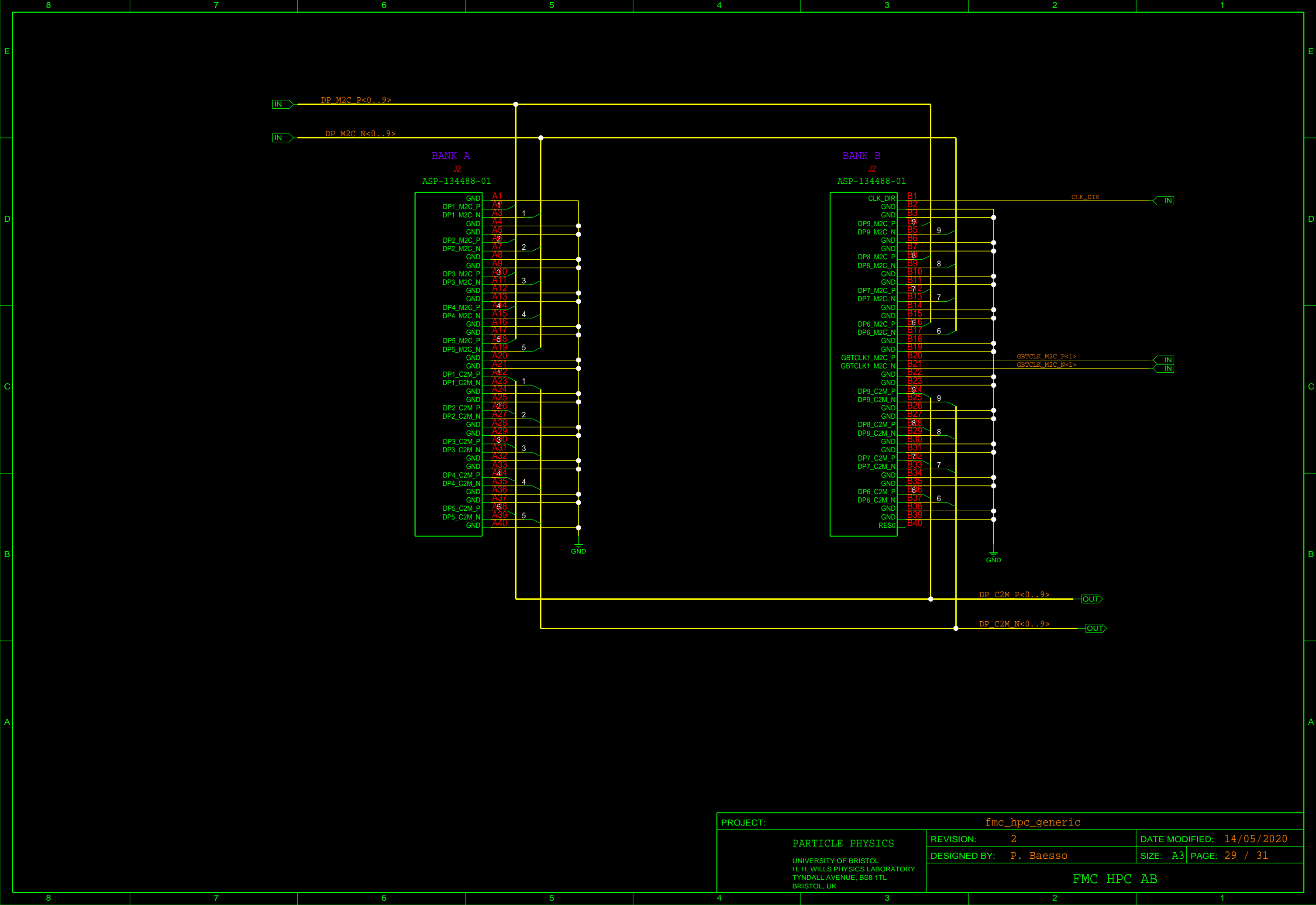


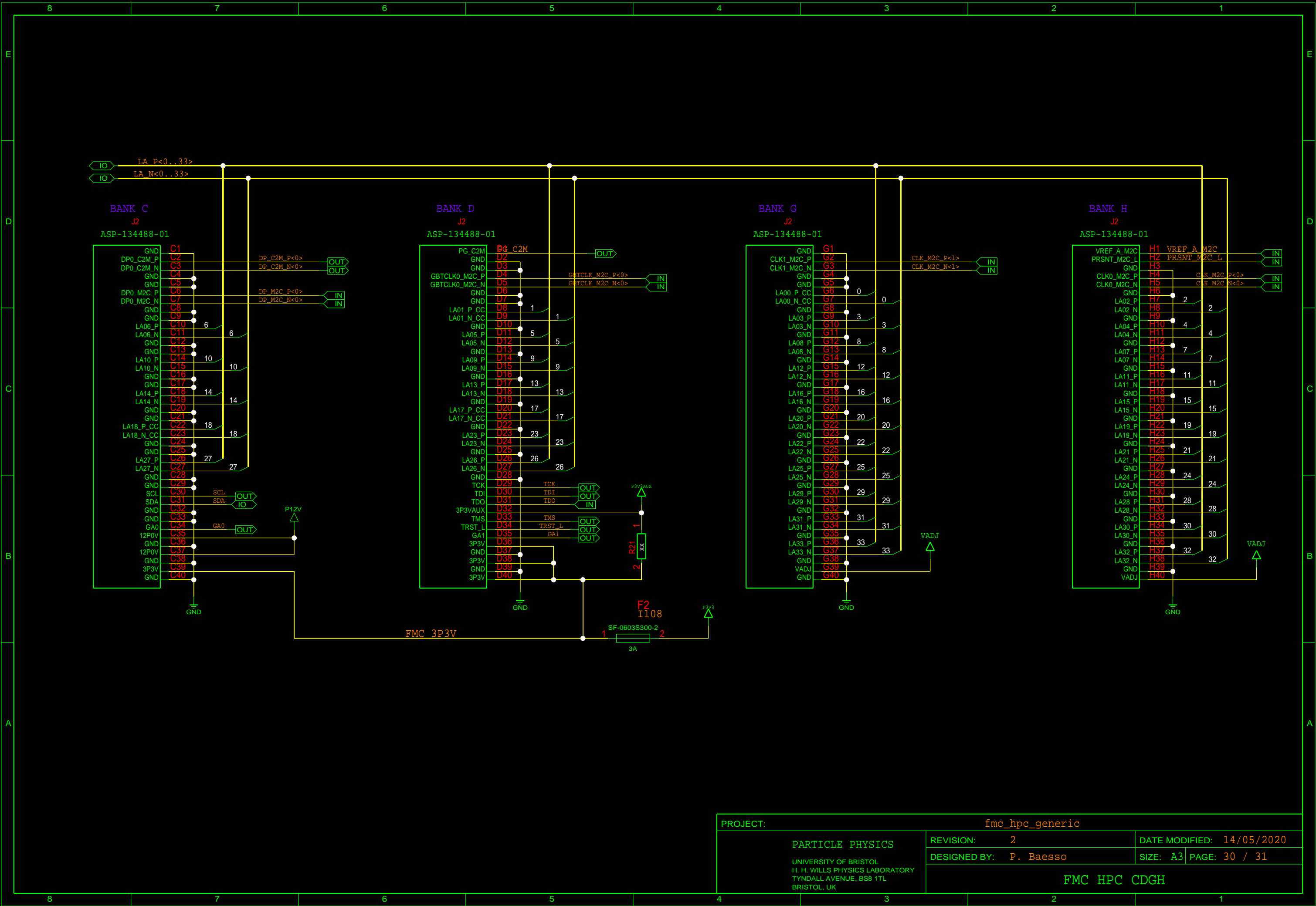
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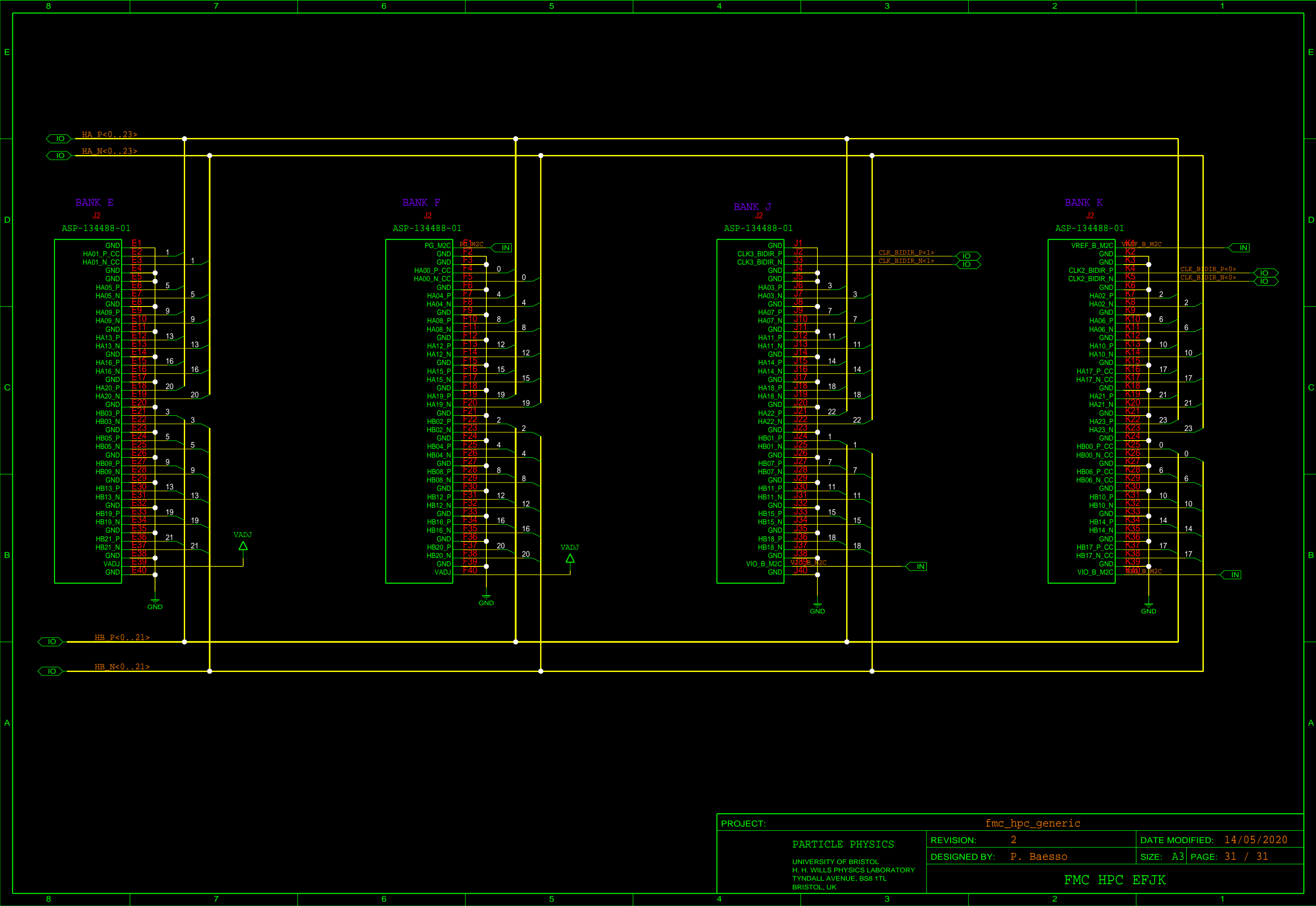


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	FMC HPC CDGH		



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		FMC HPC EFJK			