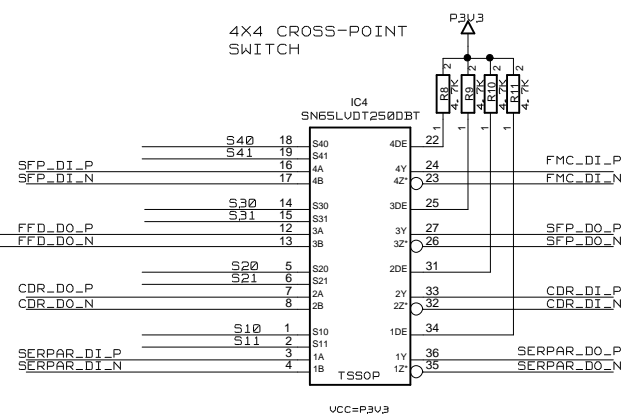
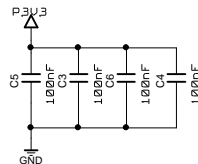
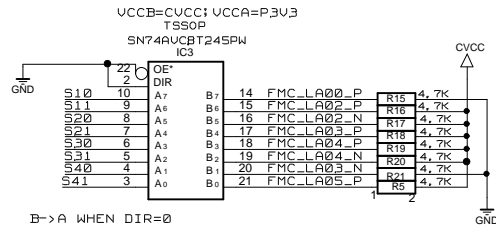
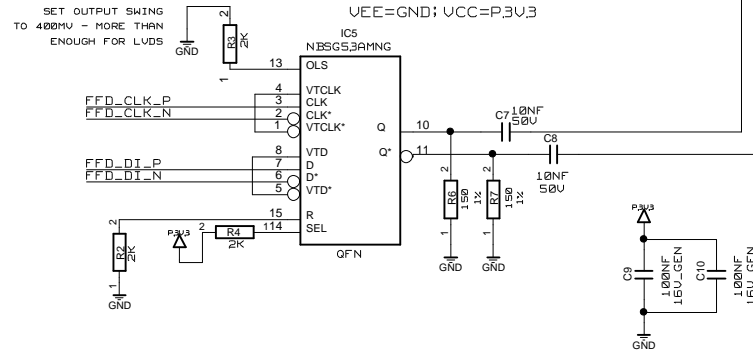


HIGH SPEED D-TYPE FLIP-FLOP
(TO REDUCE JITTER ON ENCODED DATA)



DEFAULTS (PULL UP/DOWN RESISTORS)

```

-----
S10:S11
-----
0 : 1 : 1Y/Z FMI_DI <= CDR_DO

-----
S20:S21
-----
1 : 1 : 2Y/Z SFP_DO <= SFP_DI

-----
S30:S31
-----
1 : 1 : 3Y/Z CDR_DI <= SFP_DI

-----
S40:S41
-----
0 : 1 : 4Y/Z TO_COAX <= CDR_DO
  
```

THIS PAGE ADAPTED FROM
CERN PROJECT EDA-02319-V3-1 (TTC-FMC)
DESIGN P. VISCHOUDES , LAYOUT D. LOTTE , 13/JULY/12

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			P. BAESSO	D. CUSSANS		DRAFT
ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS

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TITLE uob_hep_pc053a_11b
MODULE: pc053a_toplevel
PROTODUNE TIMING/SYNC TEST FMC
4X4 CROSS-POINT , D-TYPE REGISTER

A2

MODULE PAGE: 3 OF 4
OVERALL PAGE: 3 OF 4

TOTAL NO. OF SHEETS

