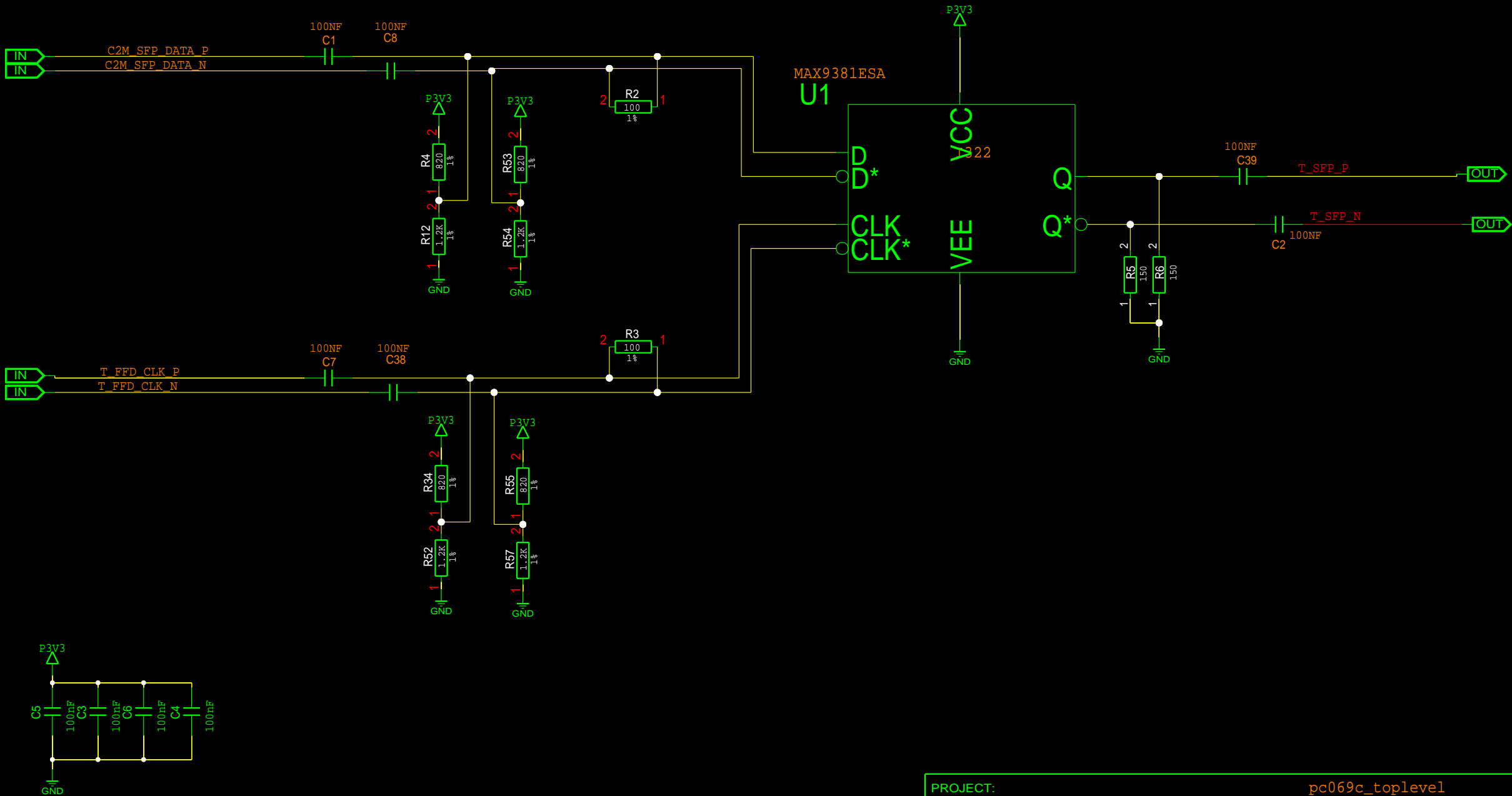


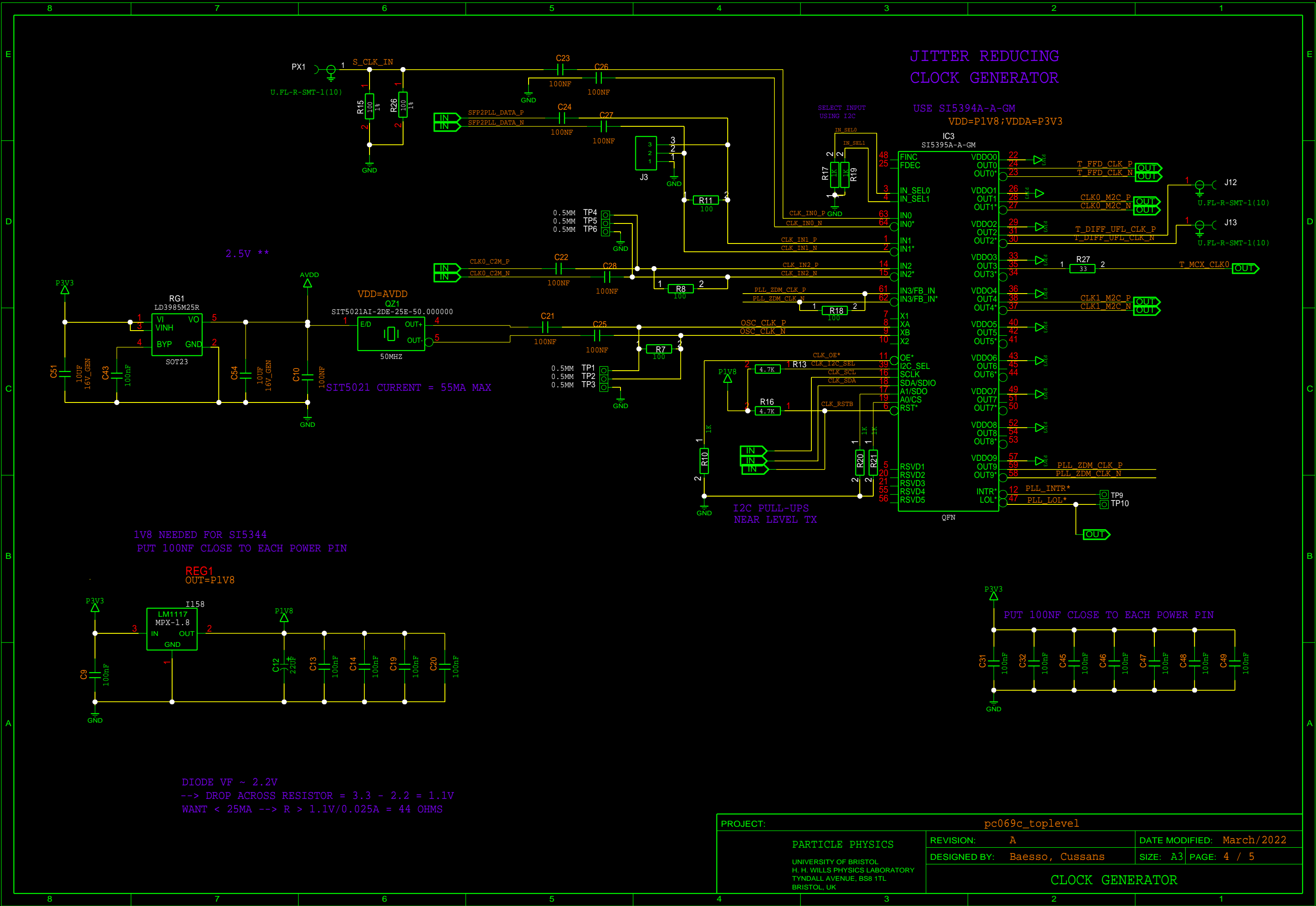


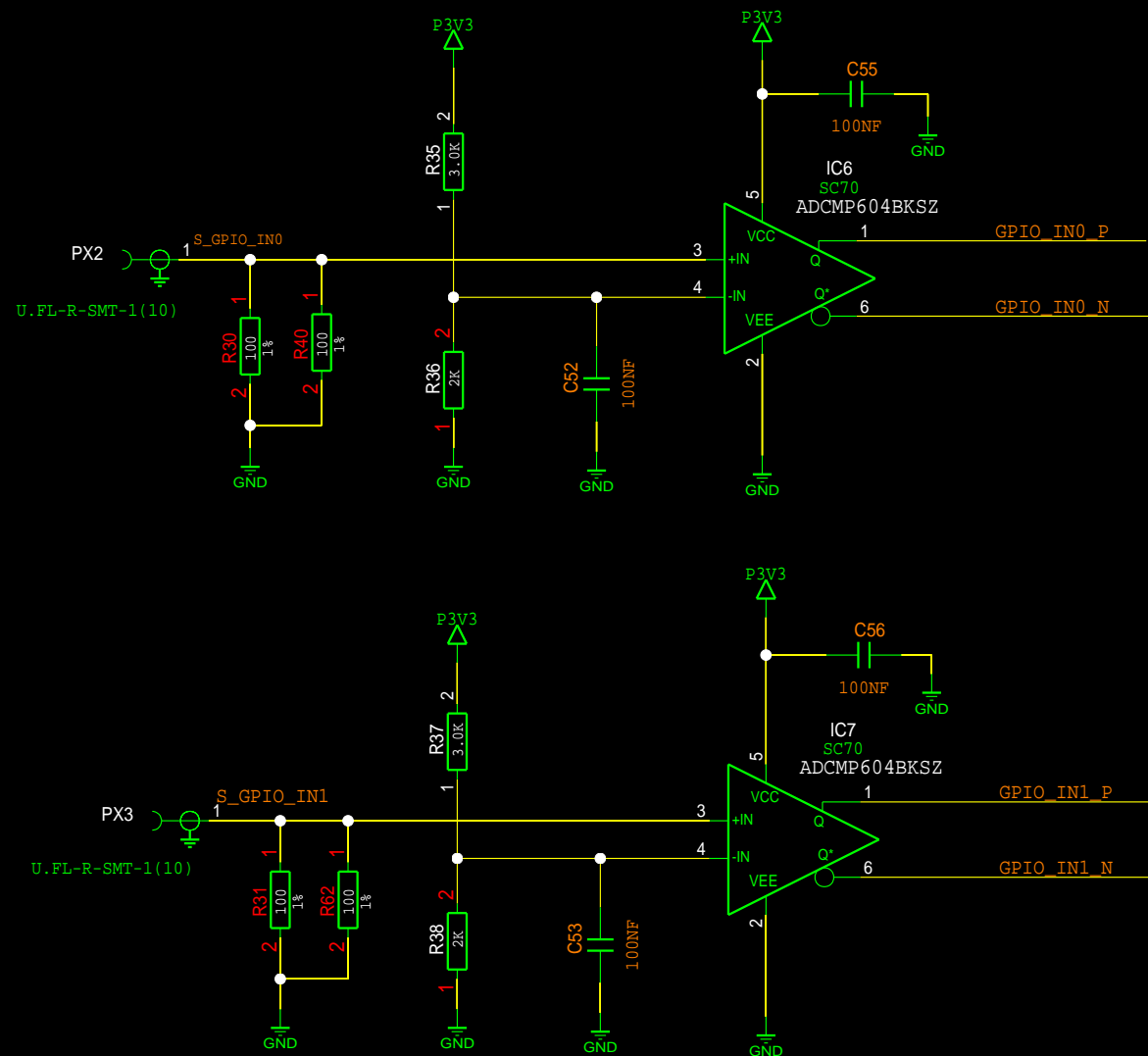


HIGH SPEED D-TYPE FLIP-FLOP  
(TO REDUCE JITTER ON ENCODED DATA)

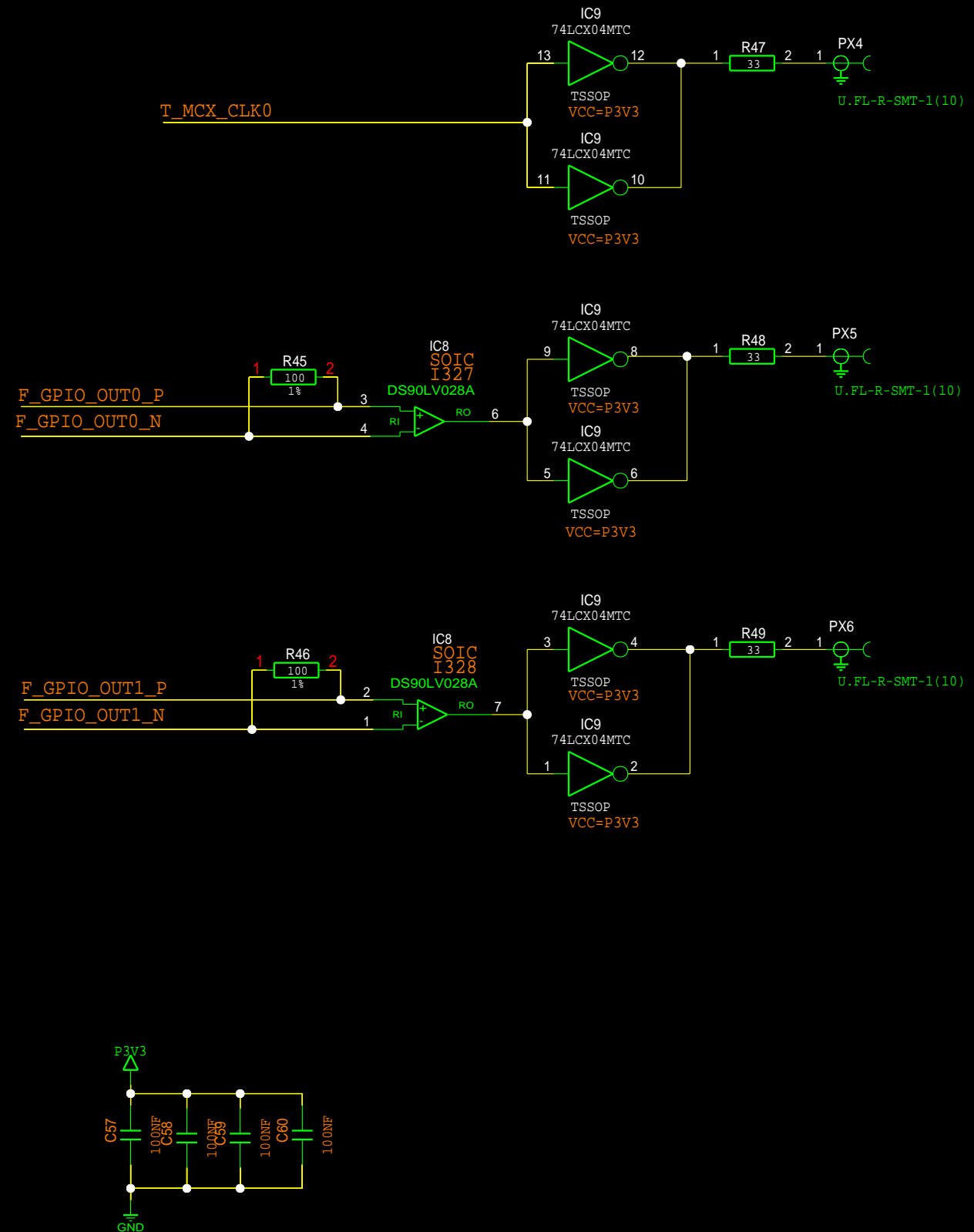


PROJECT:		pc069c_toplevel	
PARTICLE PHYSICS  UNIVERSITY OF BRISTOL H. H. WILLS PHYSICS LABORATORY TYNDALL AVENUE, BS8 1TL BRISTOL, UK	REVISION:	A	DATE MODIFIED: March/2022
	DESIGNED BY:	Baesso, Cussans	SIZE: A3 PAGE: 3 / 5
	D-TYPE REGISTER		





THRESHOLD = 0.825V  
E.G. SERIES TERMINATED 3.3V TTL  
INTO 50 OHMS



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		CLOCK I/O AND GPIO			