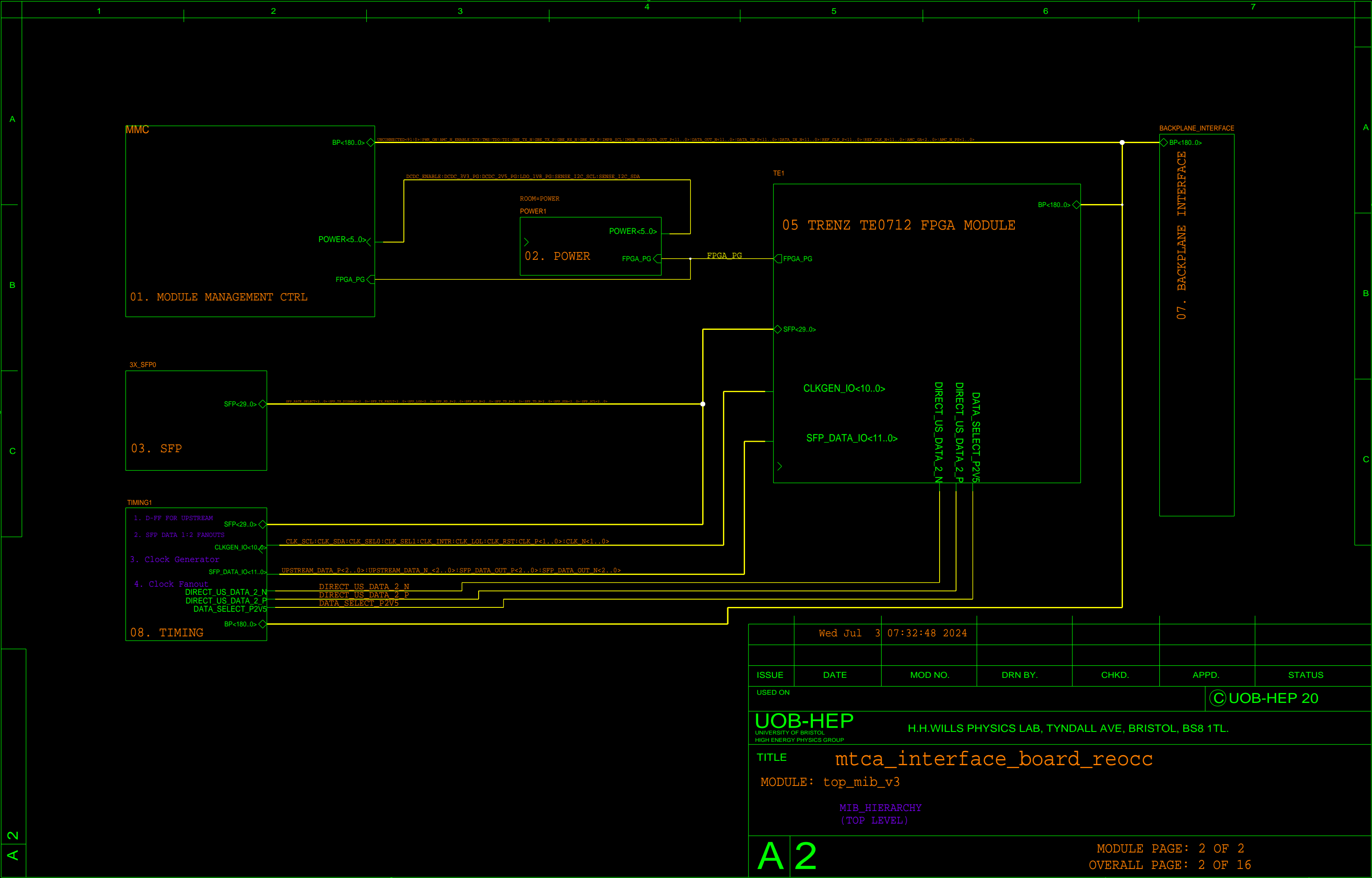


N.B. THIS IS A DRAWING, NO CONNECTIVITY

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TITLE mtca\_interface\_board\_reocc  
MODULE: top\_mib\_v3



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TITLE

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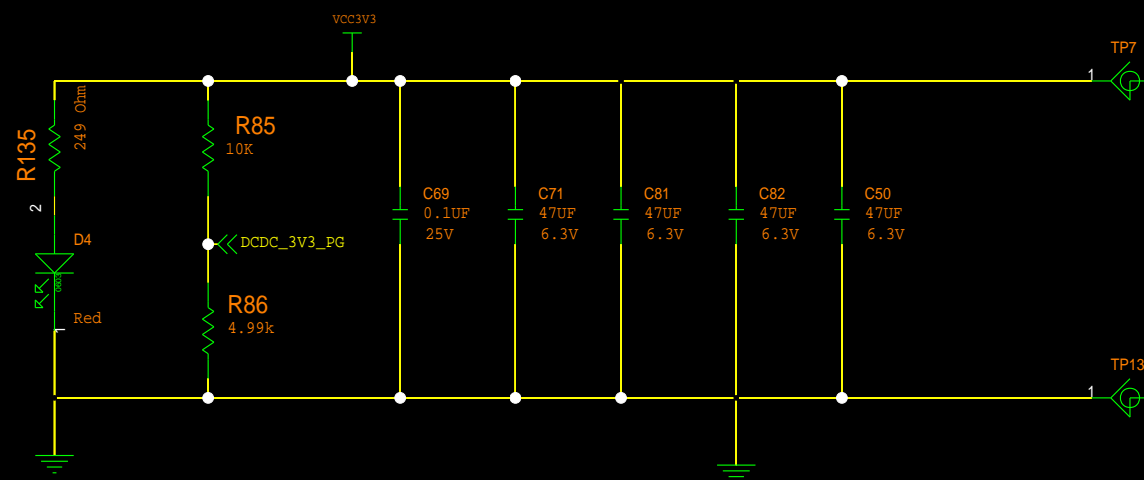
MODULE: top\_mib\_v3

MIB\_HIERARCHY  
(TOP LEVEL)

A 2

MODULE PAGE: 2 OF 2  
OVERALL PAGE: 2 OF 16

TOTAL NO. OF SHEETS



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TITLE            mtca_interface_board_reocc MODULE: 02_ power DC-DC (12V --> 3.3V)						
<b>A</b> <b>2</b>			MODULE PAGE: 1 OF 3 OVERALL PAGE: 3 OF 16			



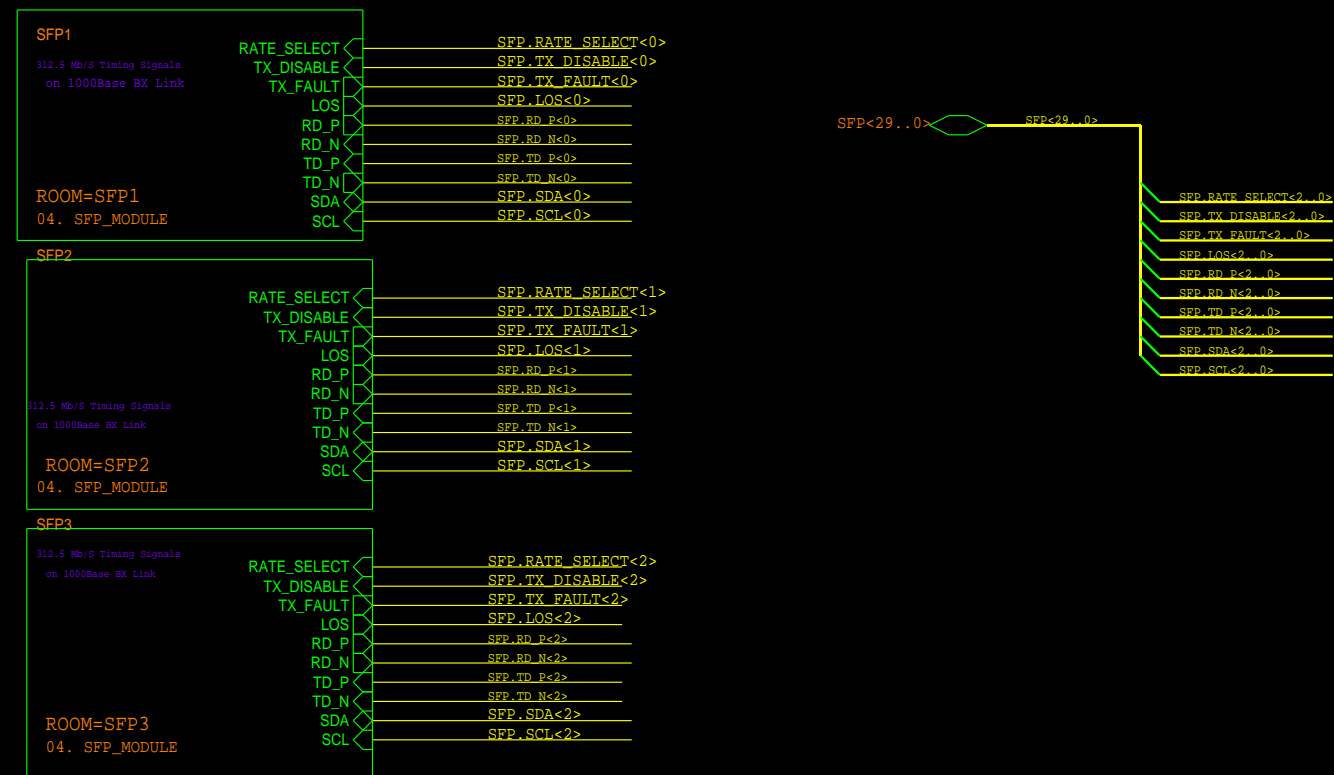












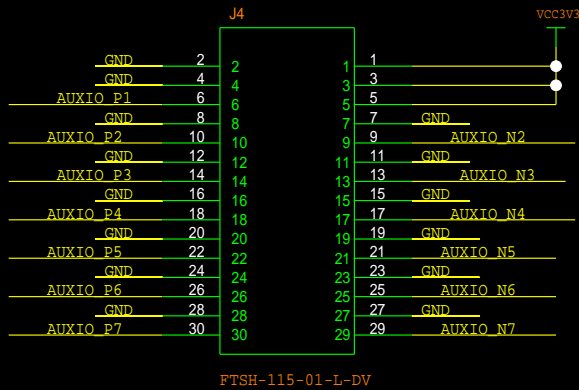
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TITLE            mtca_interface_board_reocc MODULE: 03_ sfp						
<div>A2</div>		MODULE PAGE: 1 OF 1 OVERALL PAGE: 9 OF 16				



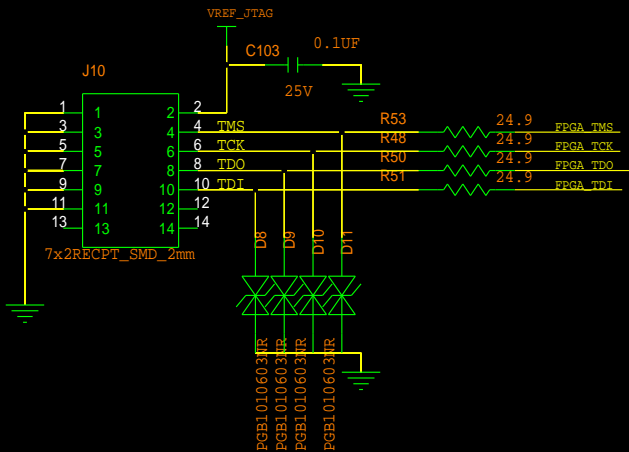
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A2

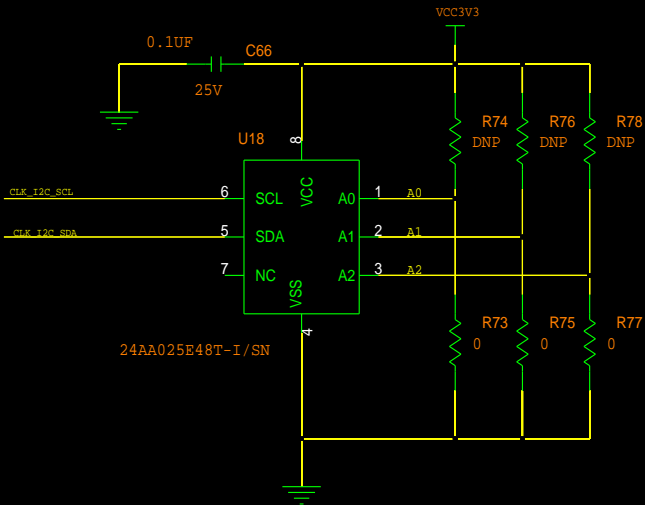
AUX IO INTERFACE



JTAG CONNECTOR

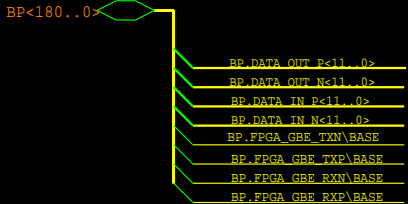
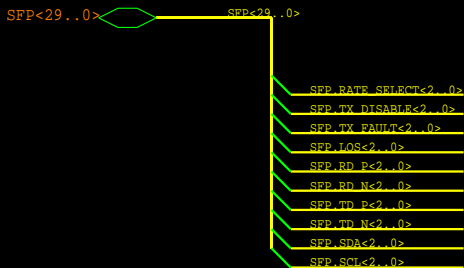


EEPROM

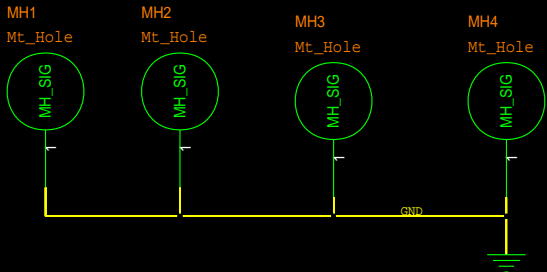


I2C ADDRESS = 0X50

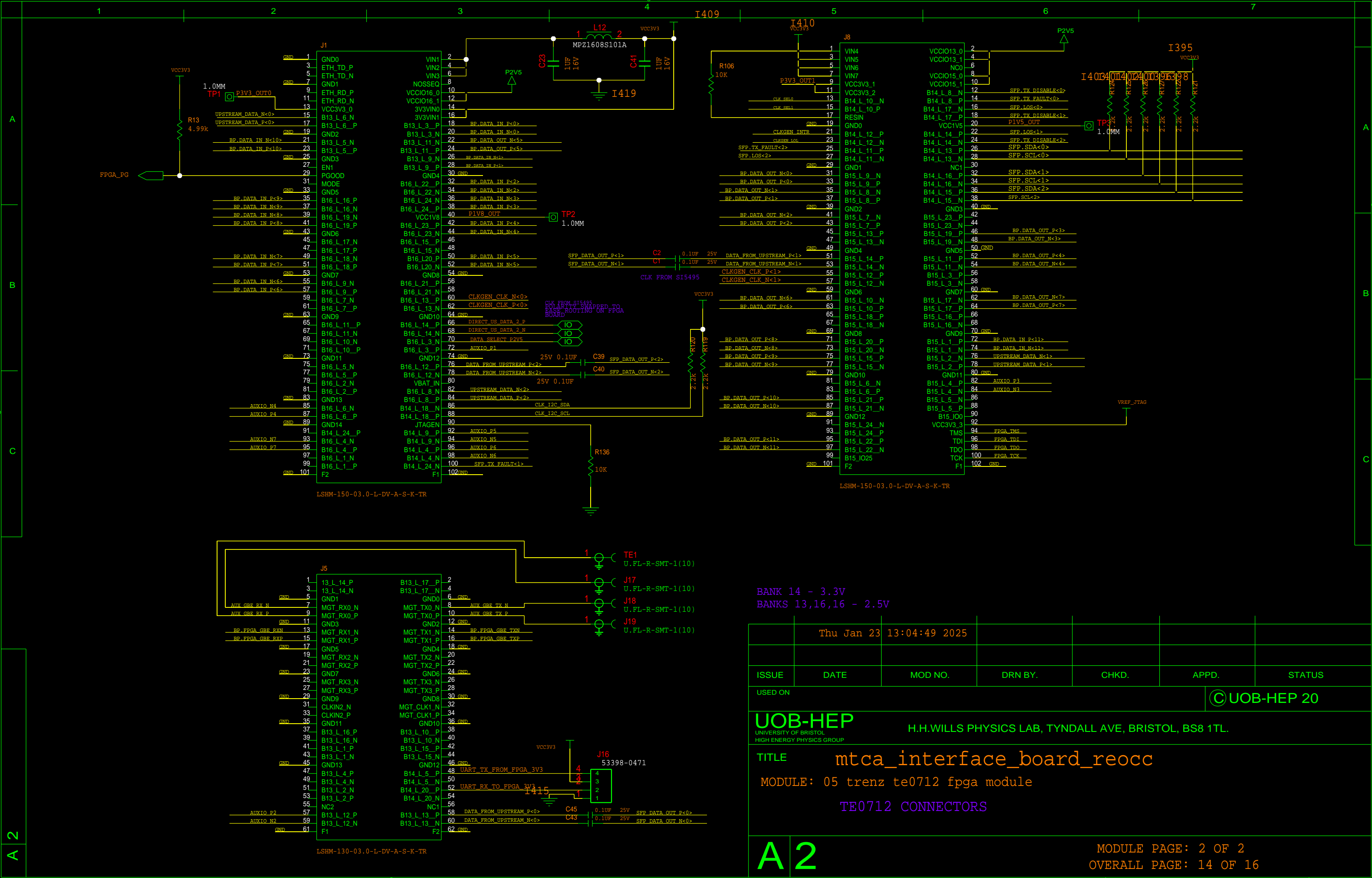
IO INTERFACE



TRENZ TE0712 FPGA MODULE MOUNT HOLES

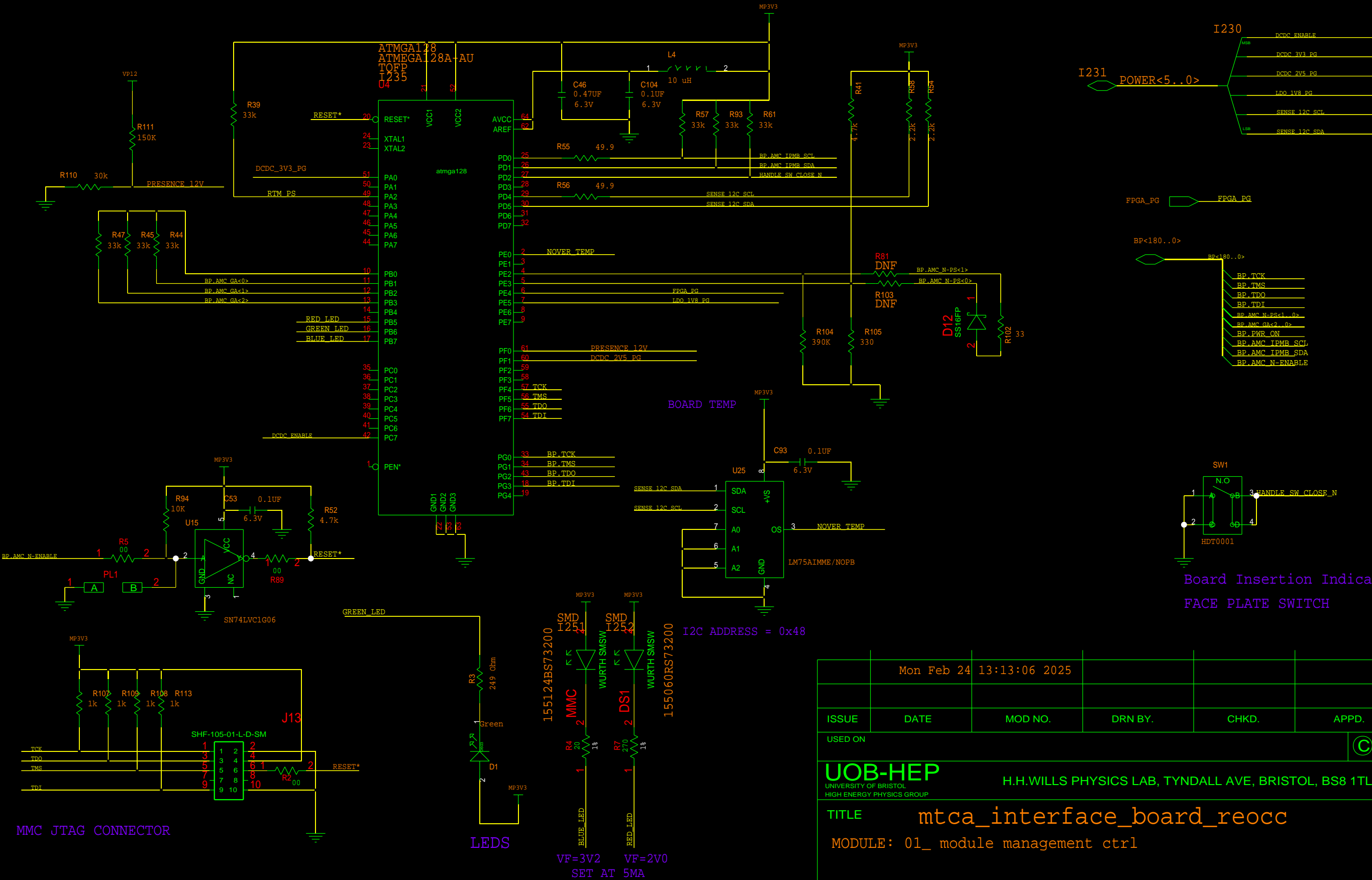


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TITLE            mtca_interface_board_reocc						
MODULE: 05 trenz te0712 fpga module						



MODULE MANAGEMENT CONTROL

IO INTERFACE



Board Insertion Indicator  
FACE PLATE SWITCH

I2C ADDRESS = 0x48

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TITLE mtca\_interface\_board\_reocc  
MODULE: 01\_module management ctrl

