

N.B. THIS IS A DRAWING, NO CONNECTIVITY

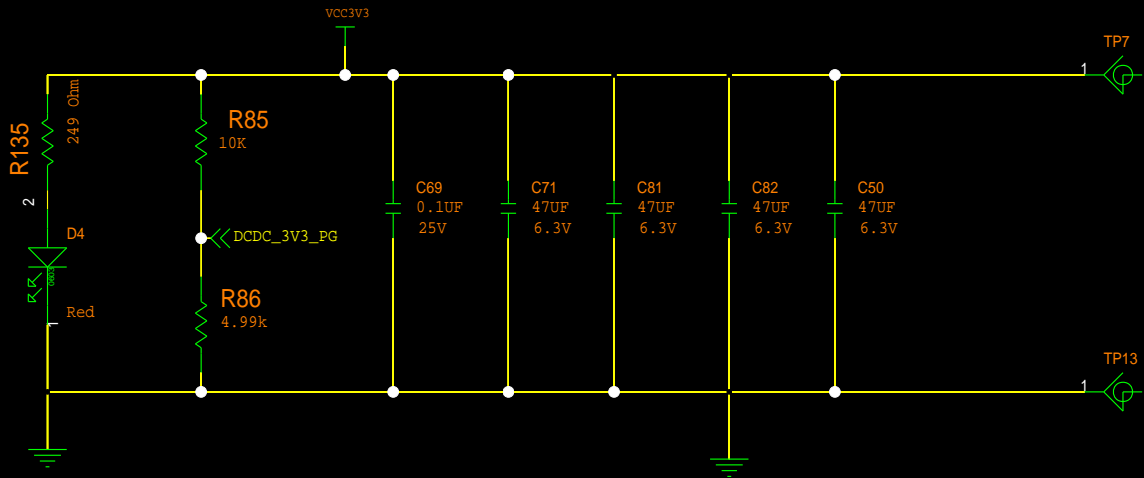
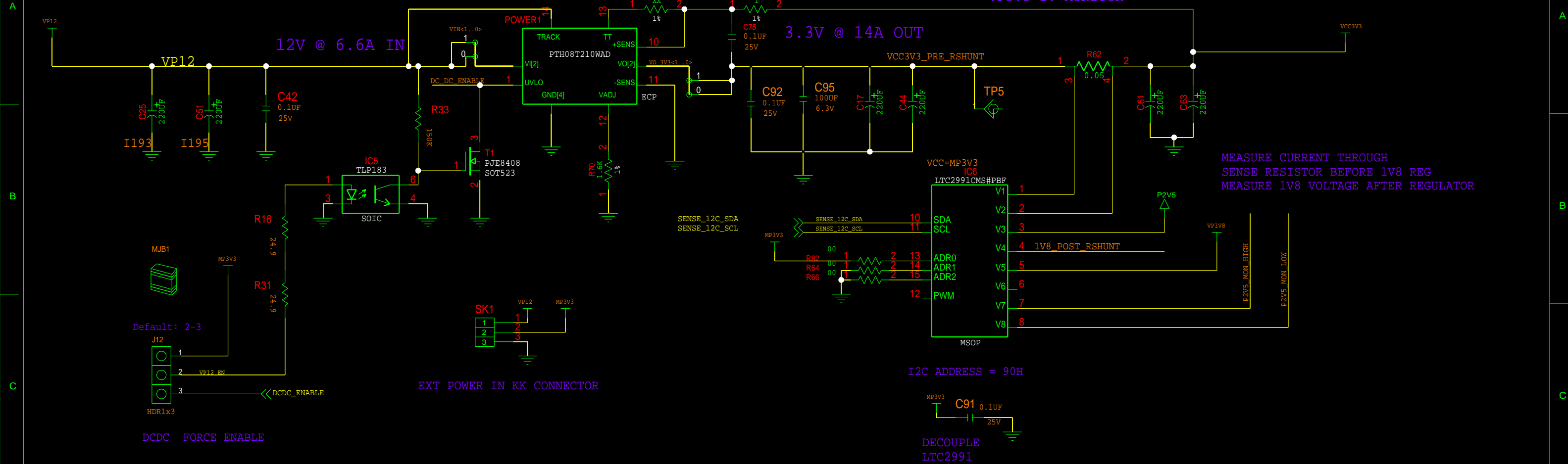
	Fri Jun 7 16:04:20 2024					
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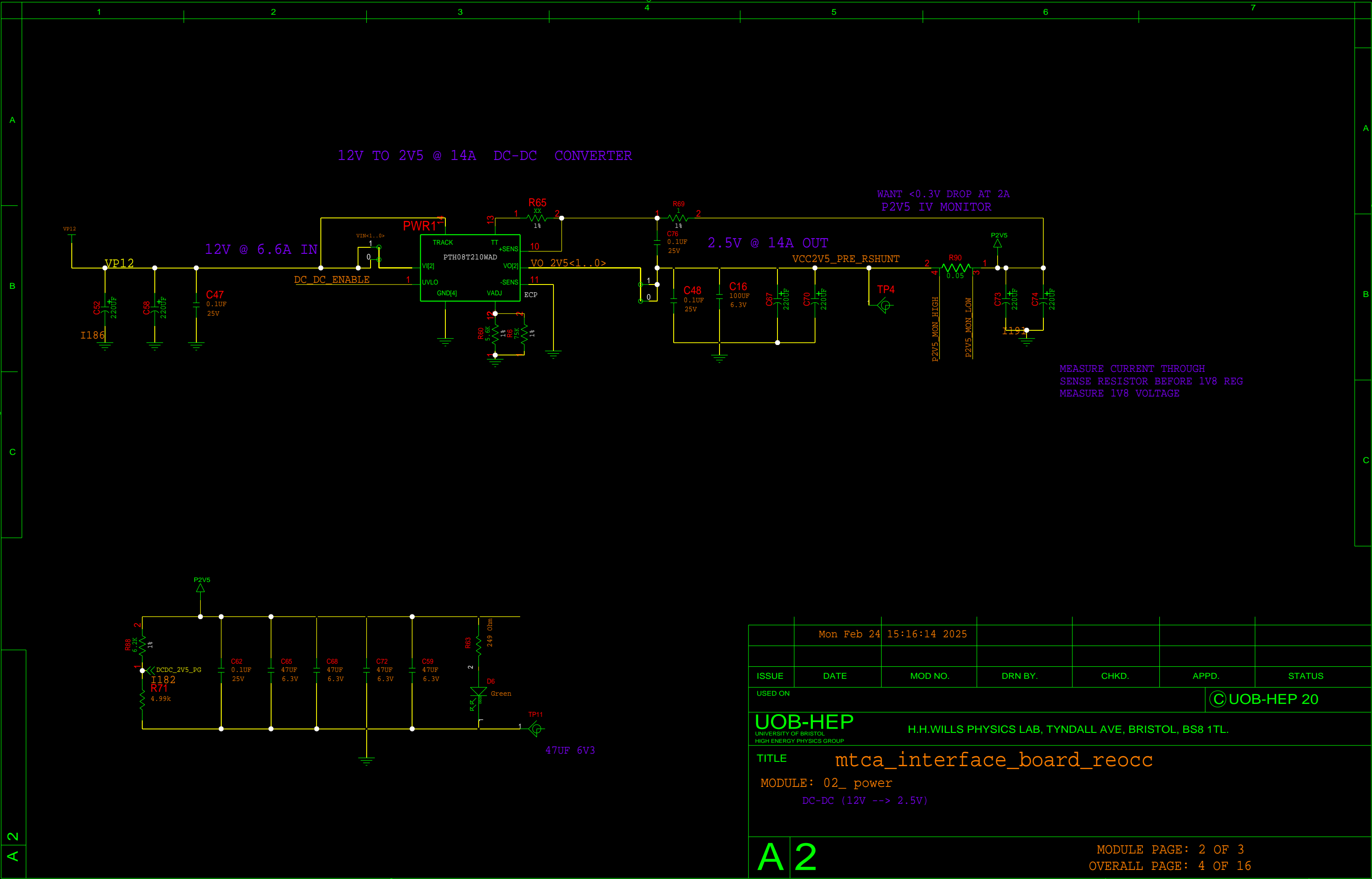
TITLE **mtca_interface_board_reocc**
MODULE: top_mib_v3

12V TO 3V3 @ 14A DC-DC CONVERTER
MIN ESR OF 3 MILLI-OHMS ON OUTPUT OF PTH08T210WAD

WANT < 0.3V DROP AT 2A
VP3V3 IV MONITOR

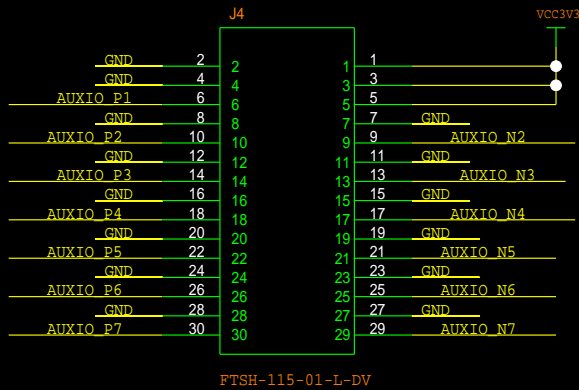


	Thu Jan 23	13:09:22 2025				
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TITLE		mtca_interface_board_reocc				
MODULE: 02_ power						
		DC-DC (12V --> 3.3V)				
A2		MODULE PAGE: 1 OF 3 OVERALL PAGE: 3 OF 16				

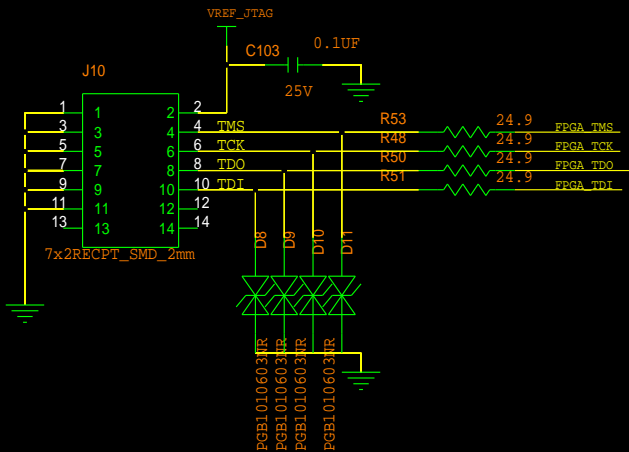




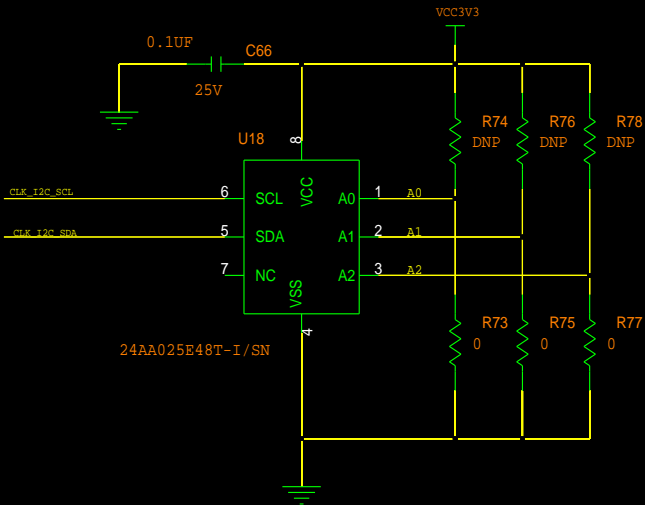
AUX IO INTERFACE



JTAG CONNECTOR

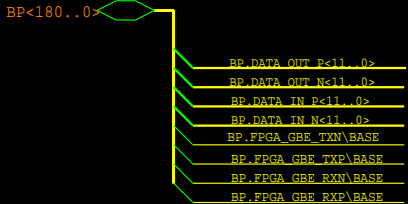
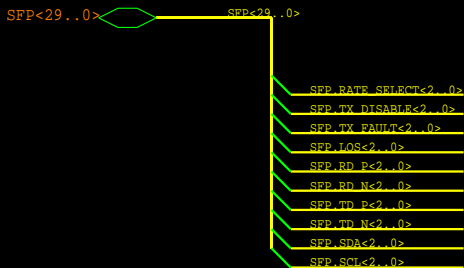


EEPROM

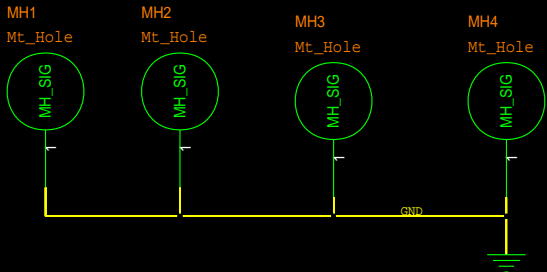


I2C ADDRESS = 0X50

IO INTERFACE



TRENZ TE0712 FPGA MODULE MOUNT HOLES



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TITLE mtca_interface_board_reocc						
MODULE: 05 trenz te0712 fpga module						

