1. Using a timer clock source of 8 MHz, calculate PSC and ARR values to get a 60 Hz interrupt.

ARR = 221.22 PSC = 600

2. Look through the Table 13 "STM32F072x8/xB pin definitions" in the chip datasheet and list all pins that can have the timer 3 capture/compare channel 1 alternate function.

PE3, PC6, PA6, PB4

3. List your measured value of the timer UEV interrupt period from first experiment.

256ms

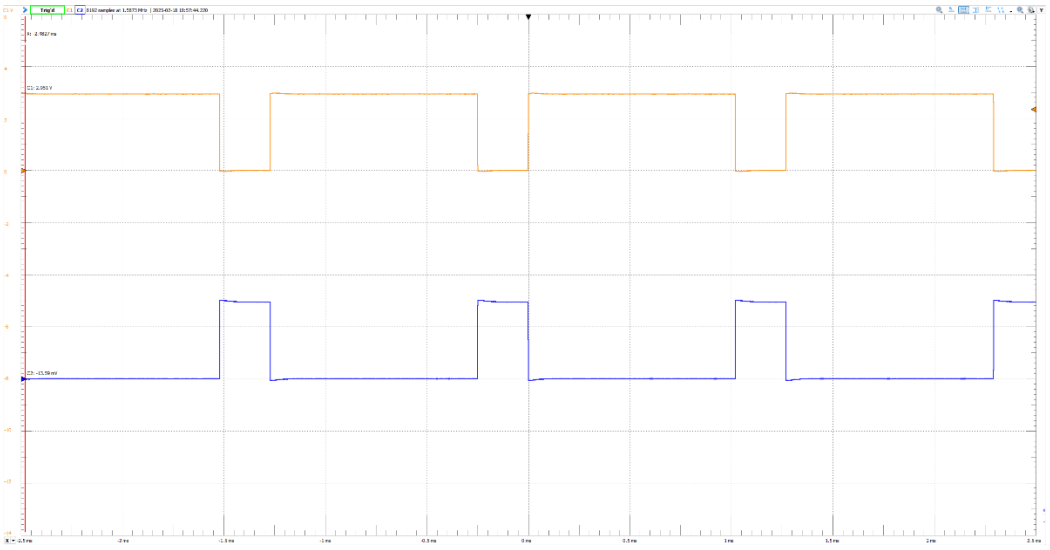
4. Describe what happened to the measured duty-cycle as the CCRx value increased in PWM mode 1.

As CCRx increase, duty cycle increases in PWM mode 1.

5. Describe what happened to the measured duty-cycle as the CCRx value increased in PWM mode 2.

As CCRx increases, duty cycle decreases in PWM mode 2.

6. Include at least one logic analyzer screenshot of a PWM capture.



7. What PWM mode is shown in figure 3.6 of the lab manual (PWM mode 1 or 2)?

PWM Mode 1