

Internship Notification Form, IIT Delhi

About Organisation

Name of Company:	Intel India Ltd.
Date of Establishment:	1968-07-18
Number of Employees:	-
Social Media Page Link:	-
Website:	https://www.intel.com/content/www/us/en/homepage.html
Type of Organization:	MNC (Foreign Origin)
Location of Head office:	Santa Clara, California
Nature of Business:	Core Engineering & Technology

Internship Profile

Job Title:	Intern
Job Description:	<ul style="list-style-type: none">- Hardware Engineer conducts research, definition, design, verification & documentation for SoC (System on a Chip) development.- Performs Logic design for integration of cell libraries, functional units, and sub-systems into SoC full chip designs. Register Transfer Level coding. and simulation for SoCs- Contributes to the development of multidimensional designs involving the layout of complex integrated circuits- Will be working on aspects of the SoC design flow from high-level design to synthesis, place and route, timing, and power to create a design database that is ready for manufacturing.- Initiates, guides, and coordinates overall design and development of new ideas and products- He/She should be willing to ramp up quickly on required areas like RTL, Verification/Validation, Structural Design / Physical Design, Analog Firmware, Embedded etc
Minimum No. of Hires:	5
Expected No. of Hires:	5
Location(s)/Place of Posting/Online:	Bangalore / Hyderabad
Skillset:	-
Minimum CGPA:	7
Students with backlog eligible:	No

Selection Process

Resume Shortlist:	Yes
Mode of Selection:	Virtual
Resume shortlisting before test?:	No
Test:	No
Group Discussion:	No
Personal Interview:	No
Technical Round:	Yes
HR Round:	No
Medical Test:	No

Eligible Academic Programs

Diversity Recruiting:	No
Eligible Years:	Graduating in 2026 (Pre-Final Year Students) - B.Tech / Dual / Master's
Eligible Departments:	B.Tech in Computer Science & Engineering, B.Tech and M.Tech in Computer Science & Engineering

Stipend Details

Stipend (per month) (In INR Per Month):	40,000 INR Per Month
Accommodation:	No