

# Internship Notification Form, IIT Delhi

## About Organisation

<b>Name of Company:</b>	Qualcomm
<b>Date of Establishment:</b>	1985-01-07
<b>Number of Employees:</b>	47000
<b>Social Media Page Link:</b>	<a href="http://www.qualcomm.com">www.qualcomm.com</a>
<b>Website:</b>	<a href="http://www.qualcomm.com">www.qualcomm.com</a>
<b>Type of Organization:</b>	MNC (Foreign Origin)
<b>Location of Head office:</b>	USA
<b>Nature of Business:</b>	Core Engineering & Technology

## Internship Profile

<b>Job Title:</b>	Interim Engineering Intern- HW
<b>Job Description:</b>	<p><b>Job Area :</b>Engineering</p> <p><b>Job Location :</b> Bangalore / Hyderabad /Chennai / Noida</p> <p>We know our employees' ideas change the world. For more than three decades, we've been a global leader in mobile technology, continually pushing the boundaries of what's possible.</p> <p>Working with customers across industries — from automotive to health care, from smart cities to robotics— we continue to accelerate innovation and unlock new possibilities in a time where everything is connected. By joining the Qualcomm family, you too can bring the future forward faster.</p> <p>Qualcomm is looking for an energetic, creative and self-driven engineer to work in Modem , Multimedia , Connectivity , Computer Vision and Image Processing , software implementation and hardware acceleration. The work will directly influence the various subsystems within the SoC. The ideal candidate would have very strong problem solving and analytical skills combined with creativity and a passion for innovation. They would be able to carry forward that new idea, concept, and/or application that will propel systems to new levels of effectiveness and efficiency. At Qualcomm you will perform detailed technical analysis, translate ideas into models, SW and/or HW and work closely with other teams to help deliver real products.</p> <p>At Qualcomm, the sky's the limit. College Graduates play important roles everywhere in the</p>

company. Many of our 27,000+ employees join us right out of school because we're working on the cutting edge in wireless. Complex wireless devices are only as powerful as the software that runs them. As a software engineer, you will develop, implement and maintain multimedia, gaming and application software for the world's leading-edge mobile devices.

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- SOC & Hard Macro Physical Design
- SOC Validation & Debug
- RF & Analog Layout
- RF/Analog/Mixed Signal/Power IC Design
- Low Power Design
- Board and FPGA Design\
- Digital ASIC Design
- Design/SOC Verification
- CAD Solution Engineer
- Design for Test (DFT)
- CPU Desig

Must have educational background in one or more of the following areas:

- Verifying SoC with embedded RISC/DSP processors, communications/ networking ASICs.
- Verilog or VHDL, C/C++, Tcl/Perl/shell-scripting. RTL design experience and/or strong OO programming knowledge
- Knowledge of wireless/wired communications and protocols or graphics/video multi-media is a plus.
- Knowledge in PLL, LNA, OpAmp, CMOS, ADC/DAC, Cadence, SpectreRF, or Layout is required in RF/Analog/Mixed Signal IC Design.
- Excellent analytical and problem solving skills.
- Ability to collaborate and work in teams.
- Good verbal and written communication skill

Educational Background:

Masters, Bachelors: Electrical Engineering , VLSI , Embedded and VLSI , ECE

<b>Minimum No. of Hires:</b>	2
<b>Expected No. of Hires:</b>	5
<b>Location(s)/Place of Posting/Online:</b>	Bangalore, HYD, Noida
<b>Skillset:</b>	VLSI, Electrical eng
<b>Minimum CGPA:</b>	7.5
<b>Students with backlog eligible:</b>	No

## Selection Process

<b>Resume Shortlist:</b>	Yes
<b>Mode of Selection:</b>	Hybrid
<b>Resume shortlisting before test?:</b>	No
<b>Test:</b>	Yes
<b>Mode of Test:</b>	Online
<b>Test duration (minutes):</b>	90
<b>Aptitude/Psycometric:</b>	Yes
<b>Technical:</b>	Yes
<b>Group Discussion:</b>	No
<b>Personal Interview:</b>	Yes
<b>Technical Round:</b>	Yes
<b>HR Round:</b>	Yes
<b>Medical Test:</b>	No

## Eligible Academic Programs

<b>Diversity Recruiting:</b>	No
<b>Eligible Years:</b>	Graduating in 2026 (Pre-Final Year Students) - B.Tech / Dual / Master's
<b>Eligible Departments:</b>	B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation)

## Stipend Details

<b>Stipend (per month) (In INR Per Month):</b>	45,000 INR Per Month
<b>Accommodation:</b>	yes
<b>Any other perks/ benefits/ components:</b>	Medical insurance
<b>Provision of PPO based on performance?</b>	Yes
<b>Tentative CTC for PPO select:</b>	29LPA, 35LPA INR Per Annum

# Others

Medical Requirements: No

Other Requirements: No