X:

VA [0x55db1b25e123] -> PA [9223372037819258983]

VA (in	010101011 101101100 011011001 001011110 000100100011				
binary)					
Layer	PML4 Index	PDPT index	PD index	PT index	Page offset
Binary	010101011	101101100	011011001	001011110	000100100011
Hex	0xAB	0x16C	0xD9	0x5E	0x123

CR3 value: [2084306944]

	Table Base Address	Table Entry Address	Entry Content
PML4	2084306944	2084308312	9223372038845345895
PDPT	1990569984	1990572896	2013409383
PD	2013409280	2013411016	2071232615
PT	2071232512	2071233264	9223372037819258983

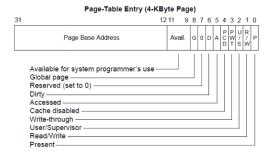
Y:

VA [0x55db1b260123] -> PA [9223372038089050215]

VA (in	010101011 101101100 011011001 001100000 000100100011				
binary)					
Layer	PML4 Index	PDPT index	PD index	PT index	Page offset
Binary	010101011	101101100	011011001	001100000	000100100011
Hex	0xAB	0x16C	0xD9	0x60	0x123

CR3 value: [2084306944]

	Table Base Address	Table Entry Address	Entry Content
PML4	2084306944	2084308312	9223372038845345895
PDPT	1990569984	1990572896	2013409383
PD	2013409280	2013411016	2071232615
PT	2071232512	2071233280	9223372038089050215



- 31-12: Page-Table Base Address
- 11-9: Available for system programmer's use
- 8: Global page (Ignored)
- 7: Page size (0 indicates 4 Kbytes)
- 6: Reserved (set to 0)
- 5: Accessed
- 4: Cache disabled
- 3: Write-through
- 2: User/Supervisor
- 1: Read/Write
- 0: Present

(Reference: http://www.renyujie.net/articles/article_ca_x86_5.php)