

TABLE OF CONTENTS

ADC Offset2

Figures

No table of figures entries found.

Tables

Table 1. Board #8 2

Table 1. Board #10 2

Table 1. Board #5 2

Table 1. Board #3 2

Table 1. Board #2 3

Table 1. Board #1 3

Table 1. Board #7 3

Table 1. Board #6 4

ADC OFFSET

R[71205d037008]+

Table 1. Board #8

Channel #	Bit 2	Bit 1	Bit 0
0	0	1	0
1	0	1	0
2	0	0	1
3	0	0	1

PER = 1 in 5003

BER = 1 in 201

Table 2. Board #10

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	0	
1	0	0	0	
2	0	0	1	45%
		1	0	45%
		1	1	10%
3	0	0	0	

PER = 1 in 531

BER = 1 in 225

Table 3. Board #5

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	0	
1	0	0	0	
2	0	0	0	
3	0	0	0	

Not working

Table 4. Board #3

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	1	0	
1	0	1	0	
2	0	1	0	
3	0	1	0	

Not working

Table 5. Board #2

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	1	
1	0	0	1	50%
		1	0	50%
2	0	0	0	
3	0	0	0	25%
			1	75%

PER = 1 in 15000+

BER = 1 in 62

Table 6. Board #1

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	0	
1	0	0	1	50%
			0	50%
2	0	1	1	50%
	1	0	0	50%
3	0	0	0	

PER = 1 in 15000+

BER = 1 in 133

Table 7. Board #7

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	0	
1	0	0	0	
2	0	0	0	
3	0	0	0	

PER = 1 in 272

BER = 1 in 20

Table 8. Board #6

Channel #	Bit 2	Bit 1	Bit 0	Frequency
0	0	0	0	
1	0	0	0	
2	0	0	0 1	95% 5%
3	0	0	0	

PER = 1 in 20

BER = 1 in 30