Veriopt Theories

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1	Conditional Elimination Phase	

theory ConditionalElimination
imports
Semantics.IRTreeEvalThms
Proofs.Rewrites
Proofs.Bisimulation
begin

1.1 Individual Elimination Rules

The set of rules used for determining whether a condition q1::'a implies another condition q2::'a or its negation. These rules are used for conditional elimination.

```
inductive impliesx :: IRExpr \Rightarrow IRExpr \Rightarrow bool \ (- \Rightarrow -) \ and implies not :: IRExpr \Rightarrow IRExpr \Rightarrow bool \ (- \Rightarrow -) \ where \ q-imp-q: \ q \Rightarrow q \ | \ eq-implies not-less: \ (BinaryExpr BinIntegerEquals x y) \Rightarrow \neg \ (BinaryExpr BinIntegerLessThan x y) \ | \ eq-implies not-less-rev: \ (BinaryExpr BinIntegerEquals x y) \Rightarrow \neg \ (BinaryExpr BinIntegerLessThan y x) \ | \ less-implies not-rev-less: \ (BinaryExpr BinIntegerLessThan x y) \Rightarrow \neg \ (BinaryExpr BinIntegerLessThan y x) \ | \ less-implies not-eq: \ (BinaryExpr BinIntegerLessThan x y) \Rightarrow \neg \ (BinaryExpr BinIntegerEquals x y) \ | \ less-implies not-eq-rev: \ (BinaryExpr BinIntegerLessThan x y) \Rightarrow \neg \ (BinaryExpr BinIntegerEquals y x) \ | \ negate-true:
```

The relation $q1::IRExpr \Rightarrow q2::IRExpr$ indicates that the implication $(q1::bool) \rightarrow (q2::bool)$ is known true (i.e. universally valid), and the relation $q1::IRExpr \Rightarrow q2::IRExpr$ indicates that the implication $(q1::bool) \rightarrow (q2::bool)$ is known false (i.e. $(q1::bool) \rightarrow \neg (q2::bool)$ is universally valid. If neither $q1::IRExpr \Rightarrow q2::IRExpr$ nor $q1::IRExpr \Rightarrow q2::IRExpr$ then the status is unknown. Only the known true and known false cases can be used for conditional elimination.

```
fun implies-valid :: IRExpr \Rightarrow IRExpr \Rightarrow bool (infix \rightarrow 50) where
  implies-valid q1 q2 =
    (\forall m \ p \ v1 \ v2. \ ([m, p] \vdash q1 \mapsto v1) \land ([m, p] \vdash q2 \mapsto v2) \longrightarrow
             (val-to-bool\ v1 \longrightarrow val-to-bool\ v2))
fun implies not-valid :: IRExpr \Rightarrow IRExpr \Rightarrow bool (infix \Rightarrow 50) where
  implies not-valid \ q1 \ q2 =
    (\forall m \ p \ v1 \ v2. \ ([m, p] \vdash q1 \mapsto v1) \land ([m, p] \vdash q2 \mapsto v2) \longrightarrow
             (val\text{-}to\text{-}bool\ v1 \longrightarrow \neg val\text{-}to\text{-}bool\ v2))
The relation (q1::IRExpr) \rightarrow (q2::IRExpr) means (q1::bool) \rightarrow (q2::bool)
is universally valid, and the relation (q1::IRExpr) \mapsto (q2::IRExpr) means
(q1::bool) \longrightarrow \neg (q2::bool) is universally valid.
\mathbf{lemma}\ \textit{eq-implies} not\textit{-less-helper} \colon
  v1 = v2 \longrightarrow \neg (int\text{-}signed\text{-}value\ b\ v1 < int\text{-}signed\text{-}value\ b\ v2)
  by force
lemma eq-impliesnot-less-val:
  val-to-bool(intval-equals v1 v2) \longrightarrow \neg val-to-bool(intval-less-than v1 v2)
 \textbf{using} \ \textit{eq-implies} not-\textit{less-helper} \ \textit{bool-to-val.simps} \ \textit{bool-to-val-bin.simps} \ \textit{intval-equals.simps}
    intval-less-than.elims val-to-bool.elims val-to-bool.simps
  by (smt\ (verit))
{f lemma}\ eq\hbox{-}impliesnot\hbox{-}less\hbox{-}rev\hbox{-}val:
  val-to-bool(intval-equals v1 v2) \longrightarrow \neg val-to-bool(intval-less-than v2 v1)
proof -
  have a: intval-equals v1 v2 = intval-equals v2 v1
    using bool-to-val-bin.simps intval-equals.simps intval-equals.elims
    by (smt (verit))
  show ?thesis using a eq-implies not-less-val by presburger
```

 $\mathbf{lemma}\ \mathit{less-implies} \mathit{not-rev-less-val} :$

qed

```
val-to-bool(intval-less-than v1 v2) \longrightarrow \neg val-to-bool(intval-less-than v2 v1) 
by (smt (verit, del-insts) Value.exhaust Value.inject(1) bool-to-val.simps(2) 
bool-to-val-bin.simps intval-less-than.simps(1) intval-less-than.simps(5) 
intval-less-than.simps(6) intval-less-than.simps(7) val-to-bool.elims(2))
```

```
\mathbf{lemma}\ less-implies not-eq-val:
  val-to-bool(intval-less-than v1 v2) \longrightarrow \neg val-to-bool(intval-equals v1 v2)
  using eq-impliesnot-less-val by blast
lemma logic-negate-type:
  assumes [m, p] \vdash UnaryExpr\ UnaryLogicNegation\ x \mapsto v
  shows \exists b \ v2. \ [m, \ p] \vdash x \mapsto IntVal \ b \ v2
  using assms
  by (metis\ UnaryExprE\ intval-logic-negation.elims\ unary-eval.simps(4))
lemma intval-logic-negation-inverse:
  assumes b > 0
  assumes x = IntVal \ b \ v
  shows val-to-bool (intval-logic-negation x) \longleftrightarrow \neg (val\text{-to-bool}\ x)
  using assms by (cases x; auto simp: logic-negate-def)
{\bf lemma}\ logic {\it -negation-relation-tree}:
  assumes [m, p] \vdash y \mapsto val
  \mathbf{assumes}\ [m,\ p] \vdash \mathit{UnaryExpr}\ \mathit{UnaryLogicNegation}\ y \mapsto \mathit{invval}
 shows val-to-bool val \longleftrightarrow \neg(val-to-bool invval)
 using assms using intval-logic-negation-inverse
 by (metis\ UnaryExprE\ evalDet\ eval-bits-1-64\ logic-negate-type\ unary-eval.simps(4))
The following theorem shows that the known true/false rules are valid.
theorem implies-impliesnot-valid:
 shows ((q1 \Rightarrow q2) \longrightarrow (q1 \mapsto q2)) \land
        ((q1 \Longrightarrow \neg q2) \longrightarrow (q1 \rightarrowtail q2))
         (is (?imp \longrightarrow ?val) \land (?notimp \longrightarrow ?notval))
proof (induct q1 q2 rule: impliesx-impliesnot.induct)
  case (q\text{-}imp\text{-}q \ q)
  then show ?case
    using evalDet by fastforce
next
  case (eq\text{-}impliesnot\text{-}less \ x \ y)
  then show ?case apply auto using eq-impliesnot-less-val evalDet by blast
next
  case (eq\text{-}impliesnot\text{-}less\text{-}rev \ x \ y)
  then show ?case apply auto using eq-impliesnot-less-rev-val evalDet by blast
next
  case (less-impliesnot-rev-less x y)
 then show ?case apply auto using less-impliesnot-rev-less-val evalDet by blast
  case (less-impliesnot-eq x y)
  then show ?case apply auto using less-implies not-eq-val eval Det by blast
next
  case (less-impliesnot-eq-rev x y)
 then show ?case apply auto using eq-implies not-less-rev-val eval Det by metis
next
```

```
case (negate-true x y)
then show ?case apply auto
by (metis logic-negation-relation-tree unary-eval.simps(4) unfold-unary)
next
case (negate-false x y)
then show ?case apply auto
by (metis UnaryExpr logic-negation-relation-tree unary-eval.simps(4))
ged
```

We introduce a type TriState::'a (as in the GraalVM compiler) to represent when static analysis can tell us information about the value of a Boolean expression. If Unknown::'a then no information can be inferred and if Known-True::'a/KnownFalse::'a one can infer the expression is always true/false.

```
\mathbf{datatype} \ \mathit{TriState} = \mathit{Unknown} \mid \mathit{KnownTrue} \mid \mathit{KnownFalse}
```

The implies relation corresponds to the LogicNode.implies method from the compiler which attempts to infer when one logic nodes value can be inferred from a known logic node.

```
inductive implies :: IRGraph \Rightarrow IRNode \Rightarrow IRNode \Rightarrow TriState \Rightarrow bool
  (-\vdash - \& - \hookrightarrow -) for g where
  eq-imp-less:
  g \vdash (IntegerEqualsNode \ x \ y) \& (IntegerLessThanNode \ x \ y) \hookrightarrow KnownFalse \mid
  eq-imp-less-rev:
  g \vdash (IntegerEqualsNode \ x \ y) \ \& \ (IntegerLessThanNode \ y \ x) \hookrightarrow KnownFalse \ |
  less-imp-rev-less:
  g \vdash (IntegerLessThanNode\ x\ y) \& (IntegerLessThanNode\ y\ x) \hookrightarrow KnownFalse \mid
  less-imp-not-eq:
  g \vdash (IntegerLessThanNode \ x \ y) \ \& \ (IntegerEqualsNode \ x \ y) \hookrightarrow KnownFalse \ |
  less-imp-not-eq-rev:
  g \vdash (\mathit{IntegerLessThanNode}\ x\ y)\ \&\ (\mathit{IntegerEqualsNode}\ y\ x) \hookrightarrow \mathit{KnownFalse}\ |
  x-imp-x:
  q \vdash x \& x \hookrightarrow KnownTrue \mid
  negate-false:
   \llbracket g \vdash x \& (kind \ g \ y) \hookrightarrow KnownTrue \rrbracket \implies g \vdash x \& (LogicNegationNode \ y) \hookrightarrow
KnownFalse |
  negate-true:
   \llbracket g \vdash x \ \& \ (\mathit{kind} \ g \ y) \ \hookrightarrow \ \mathit{KnownFalse} \rrbracket \implies g \vdash x \ \& \ (\mathit{LogicNegationNode} \ y) \ \hookrightarrow \\
Known True
```

Total relation over partial implies relation

```
inductive condition-implies :: IRGraph \Rightarrow IRNode \Rightarrow IRNode \Rightarrow TriState \Rightarrow bool

(- \vdash - \& - \rightharpoonup -) for g where

\llbracket \neg (g \vdash a \& b \hookrightarrow imp) \rrbracket \implies (g \vdash a \& b \rightharpoonup Unknown) \mid

\llbracket (g \vdash a \& b \hookrightarrow imp) \rrbracket \implies (g \vdash a \& b \rightharpoonup imp)
```

```
inductive implies-tree :: IRExpr \Rightarrow IRExpr \Rightarrow bool \Rightarrow bool
  (- \& - \hookrightarrow -) where
  eq	ext{-}imp	ext{-}less:
  (BinaryExpr\ BinIntegerEquals\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ x\ y)\hookrightarrow
False |
  eq-imp-less-rev:
  (BinaryExpr\ BinIntegerEquals\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ y\ x)\hookrightarrow
False |
  less-imp-rev-less:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ y\ x)
\hookrightarrow False \mid
  less-imp-not-eq:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerEquals\ x\ y)\hookrightarrow
False |
  less-imp-not-eq-rev:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerEquals\ y\ x)\hookrightarrow
False |
  x-imp-x:
  x \& x \hookrightarrow True \mid
  negate-false:
  \llbracket x \& y \hookrightarrow True \rrbracket \Longrightarrow x \& (UnaryExpr\ UnaryLogicNegation\ y) \hookrightarrow False \mid
  negate\text{-}true\text{:}
  \llbracket x \ \& \ y \hookrightarrow False \rrbracket \Longrightarrow x \ \& \ (UnaryExpr\ UnaryLogicNegation\ y) \hookrightarrow True
Proofs that the implies relation is correct with respect to the existing eval-
uation semantics.
lemma logic-negation-relation:
  assumes [g, m, p] \vdash y \mapsto val
  \mathbf{assumes}\ kind\ g\ neg = LogicNegationNode\ y
  \mathbf{assumes}\ [g,\ m,\ p] \ \vdash \ neg \mapsto \ invval
  assumes invval \neq UndefVal
  shows val-to-bool val \longleftrightarrow \neg(val-to-bool invval)
  using assms
  by (metis LogicNegationNode encodeeval-def logic-negation-relation-tree repDet)
lemma implies-valid:
  assumes x \& y \hookrightarrow imp
  assumes [m, p] \vdash x \mapsto v1
  assumes [m, p] \vdash y \mapsto v2
  shows (imp \longrightarrow (val\text{-}to\text{-}bool\ v1 \longrightarrow val\text{-}to\text{-}bool\ v2)) \land
         (\neg imp \longrightarrow (val\text{-}to\text{-}bool\ v1 \longrightarrow \neg(val\text{-}to\text{-}bool\ v2)))
    (is (?TP \longrightarrow ?TC) \land (?FP \longrightarrow ?FC))
  apply (intro\ conjI; rule\ impI)
proof -
  assume KnownTrue: ?TP
  show ?TC
 using assms(1) KnownTrue assms(2-) proof (induct x y imp rule: implies-tree.induct)
    case (eq\text{-}imp\text{-}less\ x\ y)
```

```
then show ?case by simp
  next
   case (eq\text{-}imp\text{-}less\text{-}rev \ x \ y)
   then show ?case by simp
   case (less-imp-rev-less \ x \ y)
   then show ?case by simp
   case (less-imp-not-eq x y)
   then show ?case by simp
 next
   case (less-imp-not-eq-rev \ x \ y)
   then show ?case by simp
 next
   case (x-imp-x)
   then show ?case
     by (metis evalDet)
 next
   case (negate-false x1)
   then show ?case using evalDet
     using assms(2,3) by blast
 next
   case (negate-true \ x \ y)
   then show ?case
     using logic-negation-relation-tree sorry
 qed
next
 assume KnownFalse: ?FP
 show ?FC using assms KnownFalse proof (induct x y imp rule: implies-tree.induct)
   case (eq\text{-}imp\text{-}less \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using eq\text{-}imp\text{-}less(1) eq\text{-}imp\text{-}less.prems(3)
     by blast
   then obtain yval where yval: [m, p] \vdash y \mapsto yval
     using eq-imp-less.prems(3)
     using eq-imp-less.prems(2) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals\ }xval
yval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(11) eq-imp-less.prems(1) evalDet)
  have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval\text{-less-than}
xval yval
     using xval yval evaltree.BinaryExpr
     by (metis\ BinaryExprE\ bin-eval.simps(12)\ eq-imp-less.prems(2)\ evalDet)
  have val-to-bool (intval-equals xval yval) \longrightarrow \neg(val-to-bool (intval-less-than xval
yval))
     apply (cases xval; cases yval; auto)
     by (smt\ (verit,\ best)\ bool-to-val.simps(2)\ val-to-bool.simps(1))
   then show ?case
```

```
using eqeval lesseval
     by (metis\ eq\text{-}imp\text{-}less.prems(1)\ eq\text{-}imp\text{-}less.prems(2)\ evalDet)
 \mathbf{next}
   case (eq\text{-}imp\text{-}less\text{-}rev \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using eq-imp-less-rev.prems(3)
     using eq-imp-less-rev.prems(2) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using eq-imp-less-rev.prems(3)
     using eq\text{-}imp\text{-}less\text{-}rev.prems(2) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals\ } xval
yval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(11) eq-imp-less-rev.prems(1) evalDet)
   have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto intval\text{-less-than}
yval xval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(12) eq-imp-less-rev.prems(2) evalDet)
   have val-to-bool (intval-equals xval yval) \longrightarrow \neg(val-to-bool (intval-less-than yval
xval)
     apply (cases xval; cases yval; auto)
     by (metis\ (full-types)\ bool-to-val.simps(2)\ less-irrefl\ val-to-bool.simps(1))
   then show ?case
     using eqeval lesseval
     by (metis eq-imp-less-rev.prems(1) eq-imp-less-rev.prems(2) evalDet)
 next
   case (less-imp-rev-less \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-rev-less.prems(3)
     using less-imp-rev-less.prems(2) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-rev-less.prems(3)
     using less-imp-rev-less.prems(2) by blast
   \textbf{have} \ \textit{lesseval:} \ [m, \, p] \vdash (\textit{BinaryExpr} \ \textit{BinIntegerLessThan} \ x \ y) \mapsto \textit{intval-less-than}
xval yval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(12) evalDet less-imp-rev-less.prems(1))
  have revlesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto intval-less-than
yval xval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(12) evalDet less-imp-rev-less.prems(2))
    have val-to-bool (intval-less-than xval yval) \longrightarrow \neg (val\text{-to-bool} (intval\text{-less-than}))
yval xval)
     apply (cases xval; cases yval; auto)
     by (smt\ (verit)\ bool-to-val.simps(2)\ val-to-bool.simps(1))
   then show ?case
     by (metis evalDet less-imp-rev-less.prems(1) less-imp-rev-less.prems(2) lesse-
val revlesseval)
 next
```

```
case (less-imp-not-eq x y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-not-eq.prems(3)
     using less-imp-not-eq.prems(1) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-not-eq.prems(3)
     using less-imp-not-eq.prems(1) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals}\ xval
yval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(11) evalDet less-imp-not-eq.prems(2))
  have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval-less-than
xval yval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(12) evalDet less-imp-not-eq.prems(1))
   have val-to-bool (intval-less-than xval yval) \longrightarrow \neg (val-to-bool (intval-equals xval
yval))
     apply (cases xval; cases yval; auto)
     by (smt\ (verit,\ best)\ bool-to-val.simps(2)\ val-to-bool.simps(1))
   then show ?case
      by (metis eqeval evalDet less-imp-not-eq.prems(1) less-imp-not-eq.prems(2)
lesseval)
  next
   case (less-imp-not-eq-rev \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-not-eq-rev.prems(3)
     using less-imp-not-eq-rev.prems(1) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-not-eq-rev.prems(3)
     using less-imp-not-eq-rev.prems(1) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ y\ x) \mapsto intval\text{-equals\ }yval
xval
     using xval yval evaltree.BinaryExpr
   by (metis\ BinaryExprE\ bin-eval.simps(11)\ evalDet\ less-imp-not-eq-rev.prems(2))
  have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval\text{-less-than}
xval yval
     using xval yval evaltree.BinaryExpr
   by (metis BinaryExprE bin-eval.simps(12) evalDet less-imp-not-eq-rev.prems(1))
   have val-to-bool (intval-less-than xval yval) \longrightarrow \neg(val-to-bool (intval-equals yval)
xval)
     apply (cases xval; cases yval; auto)
     by (smt\ (verit,\ best)\ bool-to-val.simps(2)\ val-to-bool.simps(1))
   then show ?case
   by (metis\ eqeval\ evalDet\ less-imp-not-eq-rev.prems(1)\ less-imp-not-eq-rev.prems(2)
lesseval)
  next
   case (x-imp-x x1)
   then show ?case by simp
 next
```

```
case (negate-false \ x \ y)
   then show ?case sorry
  next
   case (negate-true x1)
   then show ?case by simp
  ged
qed
lemma implies-true-valid:
  \mathbf{assumes}\ x\ \&\ y\hookrightarrow imp
  assumes imp
 assumes [m, p] \vdash x \mapsto v1
  assumes [m, p] \vdash y \mapsto v2
  shows val-to-bool v1 \longrightarrow val-to-bool v2
  using assms implies-valid
  by blast
lemma implies-false-valid:
  assumes x \& y \hookrightarrow imp
  assumes \neg imp
  assumes [m, p] \vdash x \mapsto v1
  assumes [m, p] \vdash y \mapsto v2
 shows val-to-bool v1 \longrightarrow \neg(val-to-bool v2)
  using assms implies-valid by blast
```

The following relation corresponds to the UnaryOpLogicNode.tryFold and BinaryOpLogicNode.tryFold methods and their associated concrete implementations.

The relation determines if a logic operation can be shown true or false through the stamp typing information.

```
inductive tryFold :: IRNode \Rightarrow (ID \Rightarrow Stamp) \Rightarrow bool \Rightarrow bool where

[alwaysDistinct \ (stamps \ x) \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ False \ |
[neverDistinct \ (stamps \ x) \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ True \ |
[is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ x) \ < stpi-lower \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ True \ |
[is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ y);
stpi-lower \ (stamps \ x) \ \geq stpi-upper \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ False
```

Proofs that show that when the stamp lookup function is well-formed, the tryFold relation correctly predicts the output value with respect to our evaluation semantics.

```
lemma
 assumes kind \ g \ nid = IntegerEqualsNode \ x \ y
 assumes [g, m, p] \vdash nid \mapsto v
 assumes ([q, m, p] \vdash x \mapsto xval) \land ([q, m, p] \vdash y \mapsto yval)
 shows val-to-bool (intval-equals xval yval) \longleftrightarrow v = IntVal \ 32 \ 1
proof -
  have v = intval\text{-}equals xval yval
   using assms(1, 2, 3) BinaryExprE IntegerEqualsNode bin-eval.simps(7)
   by (smt (verit) bin-eval.simps(11) encodeeval-def evalDet repDet)
  then show ?thesis using intval-equals.simps val-to-bool.simps
   by (smt\ (verit)\ bool-to-val.simps(1)\ bool-to-val.simps(2)\ bool-to-val-bin.simps
       intval-equals.elims one-neq-zero)
qed
{\bf lemma}\ tryFoldIntegerEqualsAlwaysDistinct:
 assumes wf-stamp q stamps
 assumes kind \ q \ nid = (IntegerEqualsNode \ x \ y)
 assumes [g, m, p] \vdash nid \mapsto v
 assumes alwaysDistinct\ (stamps\ x)\ (stamps\ y)
 shows v = IntVal \ 32 \ 0
proof -
  have \forall val. \neg (valid\text{-}value\ val\ (join\ (stamps\ x)\ (stamps\ y)))
   using assms(1,4) unfolding alwaysDistinct.simps
   by (smt\ (verit,\ best)\ is\mbox{-}stamp\mbox{-}empty.elims(2)\ valid\mbox{-}int\ valid\mbox{-}value.simps(1))
  obtain xv where [g, m, p] \vdash x \mapsto xv
   using assms using assms(2,3) unfolding encodeeval-def sorry
 have \neg(\exists val . ([g, m, p] \vdash x \mapsto val) \land ([g, m, p] \vdash y \mapsto val))
     {\bf using} \ \ assms(1,4) \ \ {\bf unfolding} \ \ always Distinct. simps \ \ wf\mbox{-}stamp. simps \ \ encodee-\mbox{-}stamp. \\
val-def sorry
 then show ?thesis sorry
qed
\mathbf{lemma}\ tryFoldIntegerEqualsNeverDistinct:
 assumes wf-stamp g stamps
 assumes kind \ g \ nid = (IntegerEqualsNode \ x \ y)
 assumes [q, m, p] \vdash nid \mapsto v
 assumes neverDistinct\ (stamps\ x)\ (stamps\ y)
 shows v = IntVal \ 32 \ 1
 using assms IntegerEqualsNodeE sorry
{\bf lemma}\ tryFoldIntegerLessThanTrue:
  assumes wf-stamp g stamps
 assumes kind\ g\ nid = (IntegerLessThanNode\ x\ y)
 assumes [g, m, p] \vdash nid \mapsto v
 assumes stpi-upper\ (stamps\ x) < stpi-lower\ (stamps\ y)
 shows v = IntVal \ 32 \ 1
proof -
 have stamp-type: is-IntegerStamp (stamps x)
   using assms
```

```
sorry
 obtain xval where xval: [g, m, p] \vdash x \mapsto xval
   using assms(2,3) sorry
  obtain yval where yval: [g, m, p] \vdash y \mapsto yval
   using assms(2,3) sorry
 have is-IntegerStamp (stamps x) \land is-IntegerStamp (stamps y)
   using assms(4)
   sorry
  then have val-to-bool (intval-less-than xval yval)
   sorry
 then show ?thesis
   sorry
\mathbf{qed}
\mathbf{lemma} \ tryFoldIntegerLessThanFalse:
 assumes wf-stamp q stamps
 assumes kind\ g\ nid = (IntegerLessThanNode\ x\ y)
 \mathbf{assumes}\ [g,\ m,\ p] \vdash \mathit{nid} \mapsto v
 assumes stpi-lower (stamps x) \ge stpi-upper (stamps y)
 shows v = IntVal \ 32 \ 0
 proof -
 have stamp-type: is-IntegerStamp (stamps x)
   using assms
   sorry
 obtain xval where xval: [g, m, p] \vdash x \mapsto xval
   using assms(2,3) sorry
 obtain yval where yval: [g, m, p] \vdash y \mapsto yval
   using assms(2,3) sorry
 have is-IntegerStamp (stamps x) \land is-IntegerStamp (stamps y)
   using assms(4)
   sorry
  then have \neg(val\text{-}to\text{-}bool\ (intval\text{-}less\text{-}than\ xval\ yval))
   sorry
 then show ?thesis
   sorry
\mathbf{qed}
theorem tryFoldProofTrue:
 assumes wf-stamp g stamps
 assumes tryFold (kind g nid) stamps True
 assumes [g, m, p] \vdash nid \mapsto v
 shows val-to-bool v
 using assms(2) proof (induction kind g nid stamps True rule: tryFold.induct)
case (1 stamps x y)
 \textbf{then show}~? case~\textbf{using}~try Fold Integer Equals Always Distinct~assms
   by force
next
 case (2 stamps x y)
 then show ?case using tryFoldIntegerEqualsAlwaysDistinct\ assms
```

```
by (smt (verit, best) one-neq-zero tryFold.cases tryFoldIntegerEqualsNeverDis-
tinct \ tryFoldIntegerLessThanTrue \ val-to-bool.simps(1))
next
 case (3 stamps x y)
 then show ?case using tryFoldIntegerLessThanTrue assms
   by (smt (verit, best) one-neg-zero tryFold.cases tryFoldIntegerEqualsNeverDis-
tinct\ val-to-bool.simps(1))
next
case (4 stamps x y)
 then show ?case using tryFoldIntegerLessThanFalse assms sorry
qed
theorem tryFoldProofFalse:
 assumes wf-stamp g stamps
 assumes tryFold (kind g nid) stamps False
 assumes [q, m, p] \vdash nid \mapsto v
 shows \neg(val\text{-}to\text{-}bool\ v)
using assms(2) proof (induction kind g nid stamps False rule: tryFold.induct)
case (1 stamps x y)
 then show ?case using tryFoldIntegerEqualsAlwaysDistinct assms sorry
case (2 stamps x y)
 then show ?case using tryFoldIntegerEqualsNeverDistinct assms sorry
next
 case (3 stamps x y)
 then show ?case using tryFoldIntegerLessThanTrue assms sorry
next
 case (4 stamps x y)
 then show ?case using tryFoldIntegerLessThanFalse assms sorry
qed
```

```
inductive-cases StepE:

g, p \vdash (nid, m, h) \rightarrow (nid', m', h)
```

Perform conditional elimination rewrites on the graph for a particular node. In order to determine conditional eliminations appropriately the rule needs two data structures produced by static analysis. The first parameter is the set of IRNodes that we know result in a true value when evaluated. The second parameter is a mapping from node identifiers to the flow-sensitive stamp.

The relation transforms the third parameter to the fifth parameter for a node identifier which represents the fourth parameter.

```
inductive Conditional Elimination Step:: IRExpr\ set \Rightarrow (ID \Rightarrow Stamp) \Rightarrow IRGraph \Rightarrow ID \Rightarrow IRGraph \Rightarrow bool\  where implies\ True:
```

```
\llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
 g \vdash cid \simeq cond;
 \exists ce \in conds . (ce \Rightarrow cond);
 g' = constantCondition True if cond (kind g if cond) g
 ] \implies Conditional Elimination Step conds stamps g if cond g' |
impliesFalse:
\llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
 g \vdash cid \simeq cond;
 \exists ce \in conds . (ce \Rightarrow \neg cond);
 g' = constantCondition False if cond (kind g if cond) g
 ] \implies Conditional Elimination Step conds stamps g if cond g' |
tryFoldTrue:
\llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
 cond = kind \ q \ cid;
 tryFold (kind g cid) stamps True;
 g' = constantCondition True if cond (kind g if cond) g
 ] \implies Conditional Elimination Step conds stamps g if cond g' |
tryFoldFalse:
\llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
  cond = kind \ g \ cid;
 tryFold (kind g cid) stamps False;
 g' = constantCondition False if cond (kind g if cond) g
 \rrbracket \implies Conditional Elimination Step \ conds \ stamps \ g \ if cond \ g'
```

code-pred (modes: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$) ConditionalEliminationStep.

 ${f thm}\ Conditional Elimination Step. equation$

1.2 Control-flow Graph Traversal

```
type-synonym Seen = ID set
type-synonym Condition = IRExpr
type-synonym Conditions = Condition list
type-synonym StampFlow = (ID \Rightarrow Stamp) list
```

nextEdge helps determine which node to traverse next by returning the first successor edge that isn't in the set of already visited nodes. If there is not an appropriate successor, None is returned instead.

```
fun nextEdge :: Seen \Rightarrow ID \Rightarrow IRGraph \Rightarrow ID option where 
 <math>nextEdge \ seen \ nid \ g = 
 (let \ nids = (filter \ (\lambda nid'. \ nid' \notin seen) \ (successors-of \ (kind \ g \ nid))) \ in 
 (if \ length \ nids > 0 \ then \ Some \ (hd \ nids) \ else \ None))
```

pred determines which node, if any, acts as the predecessor of another. Merge nodes represent a special case where-in the predecessor exists as an input edge of the merge node, to simplify the traversal we treat only the first input end node as the predecessor, ignoring that multiple nodes may act as a successor.

For all other nodes, the predecessor is the first element of the predecessors set. Note that in a well-formed graph there should only be one element in the predecessor set.

```
fun pred :: IRGraph ⇒ ID ⇒ ID option where

pred g nid = (case kind g nid of

(MergeNode ends - -) ⇒ Some (hd ends) |

- ⇒

(if IRGraph.predecessors g nid = {}

then None else

Some (hd (sorted-list-of-set (IRGraph.predecessors g nid)))

)
```

When the basic block of an if statement is entered, we know that the condition of the preceding if statement must be true. As in the GraalVM compiler, we introduce the registerNewCondition function which roughly corresponds to the ConditionalEliminationPhase.registerNewCondition. This method updates the flow-sensitive stamp information based on the condition which we know must be true.

```
fun clip-upper :: Stamp \Rightarrow int \Rightarrow Stamp where
  clip-upper (IntegerStamp b l h) c = (IntegerStamp b l c) |
  clip-upper s c = s
fun clip-lower :: Stamp \Rightarrow int \Rightarrow Stamp where
  clip-lower (IntegerStamp \ b \ l \ h) \ c = (IntegerStamp \ b \ c \ h) \ |
  clip-lower s c = s
fun registerNewCondition :: IRGraph \Rightarrow IRNode \Rightarrow (ID \Rightarrow Stamp) \Rightarrow (ID \Rightarrow
Stamp) where
  registerNewCondition\ g\ (IntegerEqualsNode\ x\ y)\ stamps =
     (x := join (stamps x) (stamps y)))
     (y := join (stamps x) (stamps y)) \mid
  registerNewCondition\ q\ (IntegerLessThanNode\ x\ y)\ stamps =
    (stamps
     (x := clip\text{-}upper\ (stamps\ x)\ (stpi\text{-}lower\ (stamps\ y))))
     (y := clip-lower (stamps y) (stpi-upper (stamps x)))
  registerNewCondition\ g\ -\ stamps = stamps
fun hdOr :: 'a \ list \Rightarrow 'a \Rightarrow 'a \ \mathbf{where}
  hdOr (x \# xs) de = x \mid
  hdOr [] de = de
```

The Step relation is a small-step traversal of the graph which handles tran-

sitions between individual nodes of the graph.

It relates a pairs of tuple of the current node, the set of seen nodes, the always true stack of IfNode conditions, and the flow-sensitive stamp information.

inductive Step

```
:: IRGraph \Rightarrow (ID \times Seen \times Conditions \times StampFlow) \Rightarrow (ID \times Seen \times Conditions \times StampFlow) option \Rightarrow bool
```

for g where

— Hit a BeginNode with an IfNode predecessor which represents the start of a basic block for the IfNode. 1. nid' will be the successor of the begin node. 2. Find the first and only predecessor. 3. Extract condition from the preceding IfNode. 4. Negate condition if the begin node is second branch (we've taken the else branch of the condition) 5. Add the condition or the negated condition to stack 6. Perform any stamp updates based on the condition using the registerNewCondition function and place them on the top of the stack of stamp information

 $[kind\ g\ nid = BeginNode\ nid';$

```
nid \notin seen;
seen' = \{nid\} \cup seen;
    Some if cond = pred q nid;
   kind\ g\ if cond = If Node\ cond\ t\ f;
   i = find\text{-}index\ nid\ (successors\text{-}of\ (kind\ g\ ifcond));
   c = (if \ i = 0 \ then \ kind \ g \ cond \ else \ LogicNegationNode \ cond);
    rep \ g \ cond \ ce;
    ce' = (if \ i = 0 \ then \ ce \ else \ UnaryExpr \ UnaryLogicNegation \ ce);
   conds' = ce' \# conds;
   flow' = registerNewCondition \ g \ c \ (hdOr \ flow \ (stamp \ g))
   \implies Step g (nid, seen, conds, flow) (Some (nid', seen', conds', flow' # flow)) |
   — Hit an EndNode 1. nid' will be the usage of EndNode 2. pop the conditions
and stamp stack
  [kind\ g\ nid = EndNode;]
   nid \notin seen;
   seen' = \{nid\} \cup seen;
   nid' = any-usage q nid;
   conds' = tl \ conds;
   flow' = tl \ flow
   \implies Step g (nid, seen, conds, flow) (Some (nid', seen', conds', flow'))
   — We can find a successor edge that is not in seen, go there
  [\neg (is\text{-}EndNode\ (kind\ g\ nid));
    \neg (is\text{-}BeginNode\ (kind\ g\ nid));
```

```
nid \notin seen;
   seen' = \{nid\} \cup seen;
   Some nid' = nextEdge seen' nid g
  \implies Step g (nid, seen, conds, flow) (Some (nid', seen', conds, flow))
  — We can cannot find a successor edge that is not in seen, give back None
  [\neg (is\text{-}EndNode\ (kind\ g\ nid));
   \neg (is\text{-}BeginNode\ (kind\ g\ nid));
   nid \not\in seen;
   seen' = \{nid\} \cup seen;
   None = nextEdge seen' nid g
   \implies Step q (nid, seen, conds, flow) None
  — We've already seen this node, give back None
  [nid \in seen] \implies Step \ g \ (nid, seen, conds, flow) \ None
code-pred (modes: i \Rightarrow i \Rightarrow o \Rightarrow bool) Step.
The Conditional Elimination Phase relation is responsible for combining the
individual traversal steps from the Step relation and the optimizations from
the Conditional Elimination Step relation to perform a transformation of the
whole graph.
{\bf inductive}\ {\it Conditional Elimination Phase}
  :: IRGraph \Rightarrow (ID \times Seen \times Conditions \times StampFlow) \Rightarrow IRGraph \Rightarrow bool
where
  — Can do a step and optimise for the current node
  [Step q (nid, seen, conds, flow) (Some (nid', seen', conds', flow'));
   ConditionalEliminationStep (set conds) (hdOr flow (stamp g)) g nid g';
   Conditional Elimination Phase g' (nid', seen', conds', flow') g''
   \implies Conditional Elimination Phase g (nid, seen, conds, flow) g'' |
 — Can do a step, matches whether optimised or not causing non-determinism We
need to find a way to negate Conditional Elimination Step
  [Step g (nid, seen, conds, flow) (Some (nid', seen', conds', flow'));
   Conditional Elimination Phase \ g \ (nid', seen', conds', flow') \ g'
   \implies Conditional Elimination Phase g (nid, seen, conds, flow) g'
  — Can't do a step but there is a predecessor we can backtrace to
  [Step g (nid, seen, conds, flow) None;
   Some nid' = pred \ g \ nid;
   seen' = \{nid\} \cup seen;
   Conditional Elimination Phase g (nid', seen', conds, flow) g'
   \implies Conditional Elimination Phase g (nid, seen, conds, flow) g'
```

 $\quad \text{end} \quad$