Veriopt Theories

March 18, 2022

Contents

1	Cor. 1.1 1.2	Individual Elimination Rules	1 1 12
1	$\mathbf{C}_{\mathbf{c}}$	onditional Elimination Phase	
\mathbf{th}	eory	Conditional Elimination	
i	mpor	rts	
	Sem c	$antics. IR {\it GraphFrames}$	
	$Proo_{j}$	fs. Stuttering	
	Proof	fs.Rewrites	
	Proo	fs. Bisimulation	
be	gin		

1.1 Individual Elimination Rules

We introduce a TriState as in the Graal compiler to represent when static analysis can tell us information about the value of a boolean expression. Unknown = No information can be inferred KnownTrue/KnownFalse = We can infer the expression will always be true or false.

```
datatype TriState = Unknown | KnownTrue | KnownFalse
```

The implies relation corresponds to the LogicNode.implies method from the compiler which attempts to infer when one logic nodes value can be inferred from a known logic node.

```
inductive implies :: IRGraph \Rightarrow IRNode \Rightarrow IRNode \Rightarrow TriState \Rightarrow bool

(-\vdash - \& - \hookrightarrow -) for g where

eq\text{-}imp\text{-}less:

g \vdash (IntegerEqualsNode \ x \ y) \& (IntegerLessThanNode \ x \ y) \hookrightarrow KnownFalse \mid

eq\text{-}imp\text{-}less\text{-}rev:

g \vdash (IntegerEqualsNode \ x \ y) \& (IntegerLessThanNode \ y \ x) \hookrightarrow KnownFalse \mid

less\text{-}imp\text{-}rev\text{-}less:

g \vdash (IntegerLessThanNode \ x \ y) \& (IntegerLessThanNode \ y \ x) \hookrightarrow KnownFalse \mid
```

```
less-imp-not-eq:
  g \vdash (IntegerLessThanNode \ x \ y) \ \& \ (IntegerEqualsNode \ x \ y) \hookrightarrow KnownFalse \mid
  less-imp-not-eq-rev:
  g \vdash (IntegerLessThanNode \ x \ y) \ \& \ (IntegerEqualsNode \ y \ x) \hookrightarrow KnownFalse \ |
  x-imp-x:
  g \vdash x \& x \hookrightarrow KnownTrue \mid
  negate-false:
   \llbracket g \vdash x \& (kind \ g \ y) \hookrightarrow KnownTrue \rrbracket \implies g \vdash x \& (LogicNegationNode \ y) \hookrightarrow
KnownFalse |
  negate-true:
   \llbracket g \vdash x \ \& \ (kind \ g \ y) \hookrightarrow KnownFalse \rrbracket \implies g \vdash x \ \& \ (LogicNegationNode \ y) \hookrightarrow
Known True
Total relation over partial implies relation
inductive\ condition\ -implies::IRGraph \Rightarrow IRNode \Rightarrow IRNode \Rightarrow TriState \Rightarrow bool
  (-\vdash - \& - \rightharpoonup -) for g where
  \llbracket \neg (g \vdash a \& b \hookrightarrow imp) \rrbracket \Longrightarrow (g \vdash a \& b \rightharpoonup Unknown) \mid
  \llbracket (g \vdash a \& b \hookrightarrow imp) \rrbracket \Longrightarrow (g \vdash a \& b \rightharpoonup imp)
\mathbf{inductive} \ \mathit{implies-tree} :: \mathit{IRExpr} \Rightarrow \mathit{IRExpr} \Rightarrow \mathit{bool} \Rightarrow \mathit{bool}
  (- \& - \hookrightarrow -) where
  eq-imp-less:
  (BinaryExpr\ BinIntegerEquals\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ x\ y)\hookrightarrow
False |
  eq-imp-less-rev:
  (BinaryExpr\ BinIntegerEquals\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ y\ x)\hookrightarrow
False |
  less-imp-rev-less:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerLessThan\ y\ x)
\hookrightarrow False |
  less-imp-not-eq:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerEquals\ x\ y)\hookrightarrow
False |
  less-imp-not-eq-rev:
  (BinaryExpr\ BinIntegerLessThan\ x\ y)\ \&\ (BinaryExpr\ BinIntegerEquals\ y\ x)\hookrightarrow
False |
  x-imp-x:
  x \& x \hookrightarrow True \mid
  negate-false:
  \llbracket x \& y \hookrightarrow True \rrbracket \Longrightarrow x \& (UnaryExpr\ UnaryLogicNegation\ y) \hookrightarrow False \mid
  negate-true:
  \llbracket x \& y \hookrightarrow False \rrbracket \Longrightarrow x \& (UnaryExpr\ UnaryLogicNegation\ y) \hookrightarrow True
```

Proofs that the implies relation is correct with respect to the existing eval-

```
uation semantics.
experiment begin
lemma logic-negate-type:
 assumes [m, p] \vdash UnaryExpr\ UnaryLogicNegation\ x \mapsto v
 assumes v \neq UndefVal
 shows \exists v2. [m, p] \vdash x \mapsto IntVal32 v2
proof -
  obtain ve where ve: [m, p] \vdash x \mapsto ve
   using assms(1) by blast
 then have [m, p] \vdash UnaryExpr\ UnaryLogicNegation\ x \mapsto unary-eval\ UnaryLog-
icNegation ve
   by (metis UnaryExprE assms(1) evalDet)
 then show ?thesis using assms unary-eval.elims evalDet ve IRUnaryOp.distinct
   sorry
qed
{\bf lemma}\ logic {\it -negation-relation-tree}:
 assumes [m, p] \vdash y \mapsto val
 assumes [m, p] \vdash UnaryExpr\ UnaryLogicNegation\ y \mapsto invval
 assumes intval \neq UndefVal
 shows val-to-bool val \longleftrightarrow \neg(val-to-bool invval)
proof -
 obtain v where invval = unary-eval\ UnaryLogicNegation\ v
   using assms(2) by blast
  then have [m, p] \vdash y \mapsto v using UnaryExprE \ assms(1,2) sorry
 then show ?thesis sorry
 qed
lemma logic-negation-relation:
  assumes [q, m, p] \vdash y \mapsto val
 assumes kind \ g \ neg = LogicNegationNode \ y
 \mathbf{assumes}\ [g,\ m,\ p] \vdash neg \mapsto invval
 assumes intval \neq UndefVal
 shows val-to-bool val \longleftrightarrow \neg(val-to-bool invval)
proof -
  obtain yencode where g \vdash y \simeq yencode
   using assms(1) encodeeval-def by auto
  then have g \vdash neg \simeq UnaryExpr\ UnaryLogicNegation\ yencode
   using rep.intros(7) assms(2) by simp
  then have [m, p] \vdash UnaryExpr\ UnaryLogicNegation\ yencode \mapsto invval
   using assms(3) encodeeval-def
   by (metis repDet)
  obtain v1 where [g, m, p] \vdash y \mapsto IntVal32 v1
   using assms(1,2,3,4) using logic-negate-type sorry
  have invval = bool-to-val (\neg(val-to-bool\ val))
   using assms(1,2,3) evalDet unary-eval.simps(4)
   by (smt (verit, ccfv-SIG) \ UnaryExprE \ ([m,p] \vdash UnaryExpr \ UnaryLogicNegation)
yencode \mapsto invval \land g \vdash y \simeq yencode \land bool-to-val.simps(1) \ bool-to-val.simps(2) \ en
```

```
codeval\text{-}def intval\text{-}logic\text{-}negation.simps(1) logic\text{-}negate\text{-}type repDet val\text{-}to\text{-}bool.simps(1)
  have val-to-bool invval \longleftrightarrow \neg(val-to-bool val)
    \mathbf{using} \ \langle invval = bool\text{-}to\text{-}val \ (\neg \ val\text{-}to\text{-}bool \ val) \rangle \ \mathbf{by} \ force
  then show ?thesis
    by simp
\mathbf{qed}
end
lemma implies-valid:
  \mathbf{assumes}\ x\ \&\ y\hookrightarrow imp
  assumes [m, p] \vdash x \mapsto v1
  assumes [m, p] \vdash y \mapsto v2
  assumes v1 \neq UndefVal \land v2 \neq UndefVal
  shows (imp \longrightarrow (val\text{-}to\text{-}bool\ v1 \longrightarrow val\text{-}to\text{-}bool\ v2)) \land
    (\neg imp \longrightarrow (val\text{-}to\text{-}bool\ v1 \longrightarrow \neg(val\text{-}to\text{-}bool\ v2)))
(\mathbf{is}\ (?TP \longrightarrow ?TC) \land (?FP \longrightarrow ?FC))
  apply (intro conjI; rule impI)
proof -
  assume KnownTrue: ?TP
  show ?TC
 using assms(1) KnownTrue assms(2-) proof (induct x y imp rule: implies-tree.induct)
    case (eq\text{-}imp\text{-}less \ x \ y)
    then show ?case by simp
  next
    case (eq\text{-}imp\text{-}less\text{-}rev \ x \ y)
    then show ?case by simp
    case (less-imp-rev-less \ x \ y)
    then show ?case by simp
  next
    case (less-imp-not-eq x y)
    then show ?case by simp
  next
    case (less-imp-not-eq-rev \ x \ y)
    then show ?case by simp
    case (x-imp-x)
    then show ?case
      by (metis evalDet)
  next
    case (negate-false x1)
    then show ?case using evalDet
      using assms(2,3) by blast
  next
    case (negate-true\ y)
    then show ?case
      sorry
  \mathbf{qed}
next
```

```
assume KnownFalse: ?FP
 show ?FC using assms KnownFalse proof (induct x y imp rule: implies-tree.induct)
   case (eq\text{-}imp\text{-}less \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using eq-imp-less(1) eq-imp-less.prems(3)
     by blast
   then obtain yval where yval: [m, p] \vdash y \mapsto yval
     using eq-imp-less.prems(3)
     using eq-imp-less.prems(2) by blast
    have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals}\ xval
yval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(10) eq-imp-less.prems(1) evalDet)
   \mathbf{have}\ less eval: [m,\,p] \vdash (Binary Expr\ BinInteger Less Than\ x\ y) \mapsto intval\text{-}less\text{-}than
xval yval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(11) eq-imp-less.prems(2) evalDet)
   have val-to-bool (intval-equals xval yval) \longrightarrow \neg(val-to-bool (intval-less-than xval
     using assms(4) apply (cases xval; cases yval; auto)
        apply (metis\ (full-types)\ val-to-bool.simps(1)\ Values.bool-to-val.simps(2)
signed.less-irrefl)
   \textbf{by} \ (metis \ (mono-tags) \ val-to-bool.simps (1) \ Values.bool-to-val.elims \ signed.order.strict-implies-not-eq)
   then show ?case
     using eqeval lesseval
     by (metis eq-imp-less.prems(1) eq-imp-less.prems(2) evalDet)
  next
   case (eq\text{-}imp\text{-}less\text{-}rev \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using eq-imp-less-rev.prems(3)
     using eq-imp-less-rev.prems(2) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using eq-imp-less-rev.prems(3)
     using eq-imp-less-rev.prems(2) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals}\ xval
yval
     \mathbf{using}\ \mathit{xval}\ \mathit{yval}\ \mathit{evaltree}. \mathit{BinaryExpr}
     by (metis\ BinaryExprE\ bin-eval.simps(10)\ eq-imp-less-rev.prems(1)\ evalDet)
   have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto intval-less-than
yval xval
     using xval yval evaltree.BinaryExpr
     by (metis\ BinaryExprE\ bin-eval.simps(11)\ eq-imp-less-rev.prems(2)\ evalDet)
   have val-to-bool (intval-equals xval yval) \longrightarrow \neg (val-to-bool (intval-less-than yval
xval)
     using assms(4) apply (cases xval; cases yval; auto)
        apply (metis (full-types) val-to-bool.simps(1) Values.bool-to-val.simps(2)
signed.less-irrefl)
   by (metis (full-types) val-to-bool.simps(1) Values.bool-to-val.elims signed.order.strict-implies-not-eq)
   then show ?case
```

```
using eqeval lesseval
     by (metis eq-imp-less-rev.prems(1) eq-imp-less-rev.prems(2) evalDet)
  next
   case (less-imp-rev-less \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-rev-less.prems(3)
     using less-imp-rev-less.prems(2) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-rev-less.prems(3)
     using less-imp-rev-less.prems(2) by blast
   have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval\text{-less-than}
xval yval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(11) evalDet less-imp-rev-less.prems(1))
     have revlesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto int-
val-less-than yval xval
     using xval yval evaltree.BinaryExpr
    by (metis BinaryExprE bin-eval.simps(11) evalDet less-imp-rev-less.prems(2))
    have val-to-bool (intval-less-than xval yval) \longrightarrow \neg (val\text{-to-bool} (intval\text{-less-than}))
yval xval)
     using assms(4) apply (cases xval; cases yval; auto)
   apply (metis \ val-to-bool.simps(1) \ Values.bool-to-val.elims \ signed.not-less-iff-gr-or-eq)
     by (metis val-to-bool.simps(1) Values.bool-to-val.elims signed.less-asym')
   then show ?case
    by (metis evalDet less-imp-rev-less.prems(1) less-imp-rev-less.prems(2) lesseval
revlesseval)
  next
   case (less-imp-not-eq x y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-not-eq.prems(3)
     using less-imp-not-eq.prems(1) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-not-eq.prems(3)
     using less-imp-not-eq.prems(1) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval\text{-equals}\ xval
uval
     \mathbf{using}\ \mathit{xval}\ \mathit{yval}\ \mathit{evaltree}. \mathit{BinaryExpr}
     by (metis BinaryExprE bin-eval.simps(10) evalDet less-imp-not-eq.prems(2))
   have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval-less-than
xval yval
     using xval yval evaltree.BinaryExpr
     by (metis BinaryExprE bin-eval.simps(11) evalDet less-imp-not-eq.prems(1))
   have val-to-bool (intval-less-than xval yval) \longrightarrow \neg(val-to-bool (intval-equals xval
yval))
     using assms(4) apply (cases xval; cases yval; auto)
    apply (metis (full-types) bool-to-val.simps(2) signed.less-imp-not-eq val-to-bool.simps(1))
   \textbf{by} \ (\textit{metis} \ (\textit{full-types}) \ \textit{bool-to-val.simps}(\textit{2}) \ \textit{signed.less-imp-not-eq2} \ \textit{val-to-bool.simps}(\textit{1}))
   then show ?case
      by (metis eqeval evalDet less-imp-not-eq.prems(1) less-imp-not-eq.prems(2)
```

```
lesseval)
 next
   case (less-imp-not-eq-rev \ x \ y)
   obtain xval where xval: [m, p] \vdash x \mapsto xval
     using less-imp-not-eq-rev.prems(3)
     using less-imp-not-eq-rev.prems(1) by blast
   obtain yval where yval: [m, p] \vdash y \mapsto yval
     using less-imp-not-eq-rev.prems(3)
     using less-imp-not-eq-rev.prems(1) by blast
   have eqeval: [m, p] \vdash (BinaryExpr\ BinIntegerEquals\ y\ x) \mapsto intval\text{-equals\ }yval
xval
     using xval yval evaltree.BinaryExpr
   by (metis\ BinaryExprE\ bin-eval.simps(10)\ evalDet\ less-imp-not-eq-rev.prems(2))
  have lesseval: [m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval\text{-less-than}
xval yval
     using xval yval evaltree.BinaryExpr
   by (metis BinaryExprE bin-eval.simps(11) evalDet less-imp-not-eq-rev.prems(1))
  have val-to-bool (intval-less-than xval yval) \longrightarrow \neg(val-to-bool (intval-equals yval)
     using assms(4) apply (cases xval; cases yval; auto)
   apply (metis (full-types) bool-to-val.simps(2) signed.less-imp-not-eq2 val-to-bool.simps(1))
   by (metis (full-types, opaque-lifting) val-to-bool.simps(1) Values.bool-to-val.elims
signed.dual-order.strict-implies-not-eq)
   then show ?case
   by (metis eqeval evalDet less-imp-not-eq-rev.prems(1) less-imp-not-eq-rev.prems(2)
lesseval)
 next
   case (x-imp-x x1)
   then show ?case by simp
 next
   case (negate-false \ x \ y)
   then show ?case sorry
 next
   case (negate-true x1)
   then show ?case by simp
 qed
qed
lemma implies-true-valid:
 assumes x \& y \hookrightarrow imp
 assumes imp
 assumes [m, p] \vdash x \mapsto v1
 assumes [m, p] \vdash y \mapsto v2
 assumes v1 \neq UndefVal \land v2 \neq UndefVal
 shows val-to-bool v1 \longrightarrow val-to-bool v2
  using assms implies-valid
  by blast
```

7

lemma *implies-false-valid*:

```
assumes x \& y \hookrightarrow imp
assumes \neg imp
assumes [m, p] \vdash x \mapsto v1
assumes [m, p] \vdash y \mapsto v2
assumes v1 \neq UndefVal \land v2 \neq UndefVal
shows val-to-bool v1 \longrightarrow \neg(val-to-bool v2)
using assms\ implies-valid by blast
```

The following relation corresponds to the UnaryOpLogicNode.tryFold and BinaryOpLogicNode.tryFold methods and their associated concrete implementations.

The relation determines if a logic operation can be shown true or false through the stamp typing information.

```
inductive tryFold :: IRNode \Rightarrow (ID \Rightarrow Stamp) \Rightarrow bool \Rightarrow bool
where

[alwaysDistinct \ (stamps \ x) \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ False \ |
[neverDistinct \ (stamps \ x) \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ True \ |
[is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ x) \ < stpi-lower \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ True \ |
[is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ x);
is-IntegerStamp \ (stamps \ y);
stpi-lower \ (stamps \ x) \ \geq stpi-upper \ (stamps \ y)]
\Rightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ False
```

Proofs that show that when the stamp lookup function is well-formed, the tryFold relation correctly predicts the output value with respect to our evaluation semantics.

lemma

```
assumes kind\ g\ nid=IntegerEqualsNode\ x\ y assumes [g,\ m,\ p]\vdash nid\mapsto v assumes ([g,\ m,\ p]\vdash x\mapsto xval)\land ([g,\ m,\ p]\vdash y\mapsto yval) shows val\text{-}to\text{-}bool\ (intval\text{-}equals\ xval\ yval)}\longleftrightarrow v=IntVal32\ 1 proof — have v=intval\text{-}equals\ xval\ yval using assms(1,\ 2,\ 3,\ 4)\ BinaryExprE\ IntegerEqualsNode\ bin\text{-}eval.simps(7) by (smt\ (verit)\ bin\text{-}eval.simps(10)\ encodeeval\text{-}def\ evalDet\ repDet) then show ?thesis\ using\ intval\text{-}equals.simps\ val\text{-}to\text{-}bool.simps\ sorry qed lemma tryFoldIntegerEqualsAlwaysDistinct: assumes wf-stamp g\ stamps assumes kind\ g\ nid\ =\ (IntegerEqualsNode\ x\ y) assumes [g,\ m,\ p]\vdash nid\mapsto v
```

```
assumes alwaysDistinct\ (stamps\ x)\ (stamps\ y)
 shows v = IntVal32 0
proof -
 have \forall val. \neg (valid\text{-}value\ val\ (join\ (stamps\ x)\ (stamps\ y)))
   using assms(1,4) unfolding alwaysDistinct.simps
  \textbf{by} \ (met is \ is-stamp-empty.elims(2) \ le-less-trans \ not-less \ valid32 or 64 \ valid-value.simps(1)
valid-value.simps(2))
 have \neg(\exists val . ([g, m, p] \vdash x \mapsto val) \land ([g, m, p] \vdash y \mapsto val))
     using assms(1,4) unfolding alwaysDistinct.simps wf-stamp.simps encodee-
val-def sorry
 then show ?thesis sorry
qed
{\bf lemma}\ tryFoldIntegerEqualsNeverDistinct:
 assumes wf-stamp g stamps
 assumes kind \ q \ nid = (IntegerEqualsNode \ x \ y)
 assumes [g, m, p] \vdash nid \mapsto v
 assumes neverDistinct\ (stamps\ x)\ (stamps\ y)
 shows v = Int Val 32 1
 using assms IntegerEqualsNodeE sorry
\mathbf{lemma} \ tryFoldIntegerLessThanTrue:
  assumes wf-stamp g stamps
 assumes kind \ g \ nid = (IntegerLessThanNode \ x \ y)
 assumes [g, m, p] \vdash nid \mapsto v
 assumes stpi-upper (stamps\ x) < stpi-lower (stamps\ y)
 shows v = Int Val 32 1
proof -
 have stamp-type: is-IntegerStamp (stamps x)
   using assms
   sorry
  obtain xval where xval: [g, m, p] \vdash x \mapsto xval
   using assms(2,3) sorry
  obtain yval where yval: [g, m, p] \vdash y \mapsto yval
   using assms(2,3) sorry
 have is-IntegerStamp (stamps x) \land is-IntegerStamp (stamps y)
   using assms(4)
   sorry
  then have val-to-bool (intval-less-than xval yval)
   sorry
 then show ?thesis
   sorry
qed
{\bf lemma}\ tryFoldIntegerLessThanFalse:
 assumes wf-stamp g stamps
 assumes kind\ g\ nid = (IntegerLessThanNode\ x\ y)
 assumes [g, m, p] \vdash nid \mapsto v
 assumes stpi-lower (stamps x) \geq stpi-upper (stamps y)
```

```
shows v = Int Val32 0
 proof -
 have stamp-type: is-IntegerStamp (stamps x)
   using assms
   sorry
 obtain xval where xval: [g, m, p] \vdash x \mapsto xval
   using assms(2,3) sorry
 obtain yval where yval: [g, m, p] \vdash y \mapsto yval
   using assms(2,3) sorry
 have is-IntegerStamp (stamps x) \land is-IntegerStamp (stamps y)
   using assms(4)
   sorry
 then have \neg(val\text{-}to\text{-}bool\ (intval\text{-}less\text{-}than\ xval\ yval))
   sorry
 then show ?thesis
   sorry
\mathbf{qed}
theorem tryFoldProofTrue:
 assumes wf-stamp q stamps
 assumes tryFold (kind g nid) stamps True
 assumes [g, m, p] \vdash nid \mapsto v
 shows val-to-bool v
 using assms(2) proof (induction kind g nid stamps True rule: tryFold.induct)
case (1 stamps x y)
 then show ?case using tryFoldIntegerEqualsAlwaysDistinct assms sorry
next
 case (2 stamps x y)
 then show ?case using tryFoldIntegerEqualsAlwaysDistinct assms sorry
next
 case (3 stamps x y)
 then show ?case using tryFoldIntegerLessThanTrue assms sorry
next
case (4 stamps x y)
 then show ?case using tryFoldIntegerLessThanFalse assms sorry
qed
theorem tryFoldProofFalse:
 assumes wf-stamp g stamps
 assumes tryFold (kind g nid) stamps False
 assumes [g, m, p] \vdash nid \mapsto v
 shows \neg(val\text{-}to\text{-}bool\ v)
using assms(2) proof (induction kind q nid stamps False rule: tryFold.induct)
case (1 stamps x y)
 then show ?case using tryFoldIntegerEqualsAlwaysDistinct assms sorry
\mathbf{next}
case (2 stamps x y)
 then show ?case using tryFoldIntegerEqualsNeverDistinct assms sorry
next
```

```
case (3 stamps x y)
then show ?case using tryFoldIntegerLessThanTrue assms sorry
next
case (4 stamps x y)
then show ?case using tryFoldIntegerLessThanFalse assms sorry
qed
```

```
inductive-cases StepE:

g, p \vdash (nid, m, h) \rightarrow (nid', m', h)
```

Perform conditional elimination rewrites on the graph for a particular node. In order to determine conditional eliminations appropriately the rule needs two data structures produced by static analysis. The first parameter is the set of IRNodes that we know result in a true value when evaluated. The second parameter is a mapping from node identifiers to the flow-sensitive stamp.

The relation transforms the third parameter to the fifth parameter for a node identifier which represents the fourth parameter.

```
{\bf inductive} \ {\it Conditional Elimination Step}::
  IRExpr\ set \Rightarrow (ID \Rightarrow Stamp) \Rightarrow IRGraph \Rightarrow ID \Rightarrow IRGraph \Rightarrow bool\ \mathbf{where}
  implies True:
  [kind \ g \ ifcond = (IfNode \ cid \ t \ f);
    g \vdash cid \simeq cond;
    \exists ce \in conds . (ce \& cond \hookrightarrow True);
    g' = constantCondition True if cond (kind g if cond) g
    ] \implies Conditional Elimination Step conds stamps g if cond g' |
  impliesFalse:
  \llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
    g \vdash cid \simeq cond;
    \exists \ ce \in conds \ . \ (ce \ \& \ cond \hookrightarrow False);
    g' = constantCondition False if cond (kind g if cond) g
    ] \implies Conditional Elimination Step conds stamps g if cond g' |
  tryFoldTrue:
  \llbracket kind \ q \ if cond = (If Node \ cid \ t \ f);
    cond = kind \ q \ cid;
    tryFold (kind g cid) stamps True;
    g' = constantCondition True if cond (kind g if cond) g
    ] \implies Conditional Elimination Step conds stamps g if cond g' |
  tryFoldFalse:
  \llbracket kind \ g \ if cond = (If Node \ cid \ t \ f);
    cond = kind \ g \ cid;
    tryFold (kind g cid) stamps False;
```

```
g' = constantCondition \ False \ if cond \ (kind \ g \ if cond) \ g
 \implies Conditional Elimination Step \ conds \ stamps \ g \ if cond \ g'
```

code-pred (modes: $i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$) Conditional Elimination Step.

 ${\bf thm}\ \ Conditional Elimination Step.\ equation$

1.2 Control-flow Graph Traversal

```
type-synonym Seen = ID set
type-synonym Condition = IRNode
type-synonym Conditions = Condition list
type-synonym StampFlow = (ID \Rightarrow Stamp) list
```

nextEdge helps determine which node to traverse next by returning the first successor edge that isn't in the set of already visited nodes. If there is not an appropriate successor, None is returned instead.

```
fun nextEdge :: Seen \Rightarrow ID \Rightarrow IRGraph \Rightarrow ID option where 
 <math>nextEdge \ seen \ nid \ g = 
 (let \ nids = (filter \ (\lambda nid'. \ nid' \notin seen) \ (successors-of \ (kind \ g \ nid))) \ in 
 (if \ length \ nids > 0 \ then \ Some \ (hd \ nids) \ else \ None))
```

pred determines which node, if any, acts as the predecessor of another.

Merge nodes represent a special case where-in the predecessor exists as an input edge of the merge node, to simplify the traversal we treat only the first input end node as the predecessor, ignoring that multiple nodes may act as a successor.

For all other nodes, the predecessor is the first element of the predecessors set. Note that in a well-formed graph there should only be one element in the predecessor set.

```
fun pred :: IRGraph ⇒ ID ⇒ ID option where

pred g nid = (case kind g nid of

(MergeNode ends - -) ⇒ Some (hd ends) |

- ⇒

(if IRGraph.predecessors g nid = {}

then None else

Some (hd (sorted-list-of-set (IRGraph.predecessors g nid)))

)
```

When the basic block of an if statement is entered, we know that the condition of the preceding if statement must be true. As in the GraalVM compiler, we introduce the registerNewCondition function which roughly corresponds to the ConditionalEliminationPhase.registerNewCondition. This method updates the flow-sensitive stamp information based on the condition which we know must be true.

```
fun clip-upper :: Stamp \Rightarrow int \Rightarrow Stamp where
             clip-upper\ (IntegerStamp\ b\ l\ h)\ c = (IntegerStamp\ b\ l\ c)\ |
             clip-upper s c = s
 fun clip-lower :: Stamp \Rightarrow int \Rightarrow Stamp where
             clip-lower (IntegerStamp b l h) c = (IntegerStamp b c h) |
             clip-lower s c = s
 fun registerNewCondition :: IRGraph <math>\Rightarrow Condition \Rightarrow (ID \Rightarrow Stamp) \Rightarrow (ID \Rightarrow Stamp
 Stamp) where
             registerNewCondition\ g\ (IntegerEqualsNode\ x\ y)\ stamps =
                         (stamps(x := join (stamps x) (stamps y)))(y := join (stamps x) (stamps y)) \mid
             registerNewCondition\ g\ (IntegerLessThanNode\ x\ y)\ stamps =
                         (stamps
                                   (x := clip\text{-}upper\ (stamps\ x)\ (stpi\text{-}lower\ (stamps\ y))))
                                   (y := clip-lower (stamps y) (stpi-upper (stamps x)))
             registerNewCondition\ g - stamps = stamps
fun hdOr :: 'a \ list \Rightarrow 'a \Rightarrow 'a \ \mathbf{where}
             hdOr(x \# xs) de = x \mid
             hdOr [] de = de
```

The Step relation is a small-step traversal of the graph which handles transitions between individual nodes of the graph.

It relates a pairs of tuple of the current node, the set of seen nodes, the always true stack of IfNode conditions, and the flow-sensitive stamp information.

inductive Step

:: $IRGraph \Rightarrow (ID \times Seen \times Conditions \times StampFlow) \Rightarrow (ID \times Seen \times Conditions \times StampFlow) \ option \Rightarrow bool$

for g where

— Hit a BeginNode with an IfNode predecessor which represents the start of a basic block for the IfNode. 1. nid' will be the successor of the begin node. 2. Find the first and only predecessor. 3. Extract condition from the preceding IfNode. 4. Negate condition if the begin node is second branch (we've taken the else branch of the condition) 5. Add the condition or the negated condition to stack 6. Perform any stamp updates based on the condition using the registerNewCondition function and place them on the top of the stack of stamp information

 $[kind\ g\ nid = BeginNode\ nid';$

```
nid \notin seen;
seen' = \{nid\} \cup seen;
Some if cond = pred g nid;
kind g if cond = IfNode cond t f;
i = find-index nid (successors-of (kind g if cond));
c = (if i = 0 then kind g cond else LogicNegationNode cond);
conds' = c \# conds;
```

```
flow' = registerNewCondition \ g \ c \ (hdOr \ flow \ (stamp \ g))
  \implies Step g (nid, seen, conds, flow) (Some (nid', seen', conds', flow' # flow)) |
  — Hit an EndNode 1. nid' will be the usage of EndNode 2. pop the conditions
and stamp stack
  [kind\ g\ nid = EndNode;]
   nid \not\in seen;
   seen' = \{nid\} \cup seen;
   nid' = any-usage g nid;
   conds' = tl \ conds;
   flow' = tl \ flow
   \implies Step q (nid, seen, conds, flow) (Some (nid', seen', conds', flow'))
  — We can find a successor edge that is not in seen, go there
  [\neg (is\text{-}EndNode\ (kind\ g\ nid));
    \neg (is\text{-}BeginNode\ (kind\ g\ nid));
   nid \notin seen;
   seen' = \{nid\} \cup seen;
   Some nid' = nextEdge seen' nid g
  \implies Step g (nid, seen, conds, flow) (Some (nid', seen', conds, flow)) |
  — We can cannot find a successor edge that is not in seen, give back None
  [\neg(is\text{-}EndNode\ (kind\ g\ nid));
    \neg (is\text{-}BeginNode\ (kind\ g\ nid));
   nid \notin seen;
   seen' = \{nid\} \cup seen;
   None = nextEdge seen' nid g
   \implies Step q (nid, seen, conds, flow) None
 — We've already seen this node, give back None
 [nid \in seen] \implies Step \ g \ (nid, seen, conds, flow) \ None
code-pred (modes: i \Rightarrow i \Rightarrow o \Rightarrow bool) Step.
```

The ConditionalEliminationPhase relation is responsible for combining the individual traversal steps from the Step relation and the optimizations from the ConditionalEliminationStep relation to perform a transformation of the whole graph.

 \mathbf{end}