

Veriopt Theories

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1 Conditional Elimination Phase

```
theory ConditionalElimination
imports
  Semantics.IRTreeEvalThms
  Proofs.Rewrites
  Proofs.Bisimulation
begin
```

1.1 Individual Elimination Rules

The set of rules used for determining whether a condition $q1::'a$ implies another condition $q2::'a$ or its negation. These rules are used for conditional elimination.

```
inductive implies :: IRExpr  $\Rightarrow$  IRExpr  $\Rightarrow$  bool ( $- \Rightarrow -$ ) and
  impliesnot :: IRExpr  $\Rightarrow$  IRExpr  $\Rightarrow$  bool ( $- \Rightarrow \neg -$ ) where
  q-imp-q:
     $q \Rightarrow q$  |
  eq-impliesnot-less:
     $(\text{BinaryExpr BinIntegerEquals } x \ y) \Rightarrow \neg (\text{BinaryExpr BinIntegerLessThan } x \ y)$  |
  eq-impliesnot-less-rev:
     $(\text{BinaryExpr BinIntegerEquals } x \ y) \Rightarrow \neg (\text{BinaryExpr BinIntegerLessThan } y \ x)$  |
  less-impliesnot-rev-less:
     $(\text{BinaryExpr BinIntegerLessThan } x \ y) \Rightarrow \neg (\text{BinaryExpr BinIntegerLessThan } y \ x)$ 
  |
  less-impliesnot-eq:
     $(\text{BinaryExpr BinIntegerLessThan } x \ y) \Rightarrow \neg (\text{BinaryExpr BinIntegerEquals } x \ y)$  |
  less-impliesnot-eq-rev:
     $(\text{BinaryExpr BinIntegerLessThan } x \ y) \Rightarrow \neg (\text{BinaryExpr BinIntegerEquals } y \ x)$  |
  negate-true:
```

$\llbracket x \Rightarrow \neg y \rrbracket \Longrightarrow x \Rightarrow (\text{UnaryExpr } \text{UnaryLogicNegation } y) \mid$
negate-false:
 $\llbracket x \Rightarrow y \rrbracket \Longrightarrow x \Rightarrow \neg (\text{UnaryExpr } \text{UnaryLogicNegation } y)$

The relation $q1::IRExpr \Rightarrow q2::IRExpr$ indicates that the implication $(q1::bool) \longrightarrow (q2::bool)$ is known true (i.e. universally valid), and the relation $q1::IRExpr \Rightarrow \neg q2::IRExpr$ indicates that the implication $(q1::bool) \longrightarrow (q2::bool)$ is known false (i.e. $(q1::bool) \longrightarrow \neg (q2::bool)$ is universally valid). If neither $q1::IRExpr \Rightarrow q2::IRExpr$ nor $q1::IRExpr \Rightarrow \neg q2::IRExpr$ then the status is unknown. Only the known true and known false cases can be used for conditional elimination.

fun *implies-valid* :: *IRExpr* \Rightarrow *IRExpr* \Rightarrow *bool* (**infix** \rightsquigarrow 50) **where**
implies-valid *q1* *q2* =
 $(\forall m\ p\ v1\ v2. ([m, p] \vdash q1 \mapsto v1) \wedge ([m, p] \vdash q2 \mapsto v2) \longrightarrow$
 $(\text{val-to-bool } v1 \longrightarrow \text{val-to-bool } v2))$

fun *impliesnot-valid* :: *IRExpr* \Rightarrow *IRExpr* \Rightarrow *bool* (**infix** \rightsquigarrow 50) **where**
impliesnot-valid *q1* *q2* =
 $(\forall m\ p\ v1\ v2. ([m, p] \vdash q1 \mapsto v1) \wedge ([m, p] \vdash q2 \mapsto v2) \longrightarrow$
 $(\text{val-to-bool } v1 \longrightarrow \neg \text{val-to-bool } v2))$

The relation $(q1::IRExpr) \rightsquigarrow (q2::IRExpr)$ means $(q1::bool) \longrightarrow (q2::bool)$ is universally valid, and the relation $(q1::IRExpr) \rightsquigarrow \neg (q2::IRExpr)$ means $(q1::bool) \longrightarrow \neg (q2::bool)$ is universally valid.

lemma *eq-impliesnot-less-helper*:
 $v1 = v2 \longrightarrow \neg(\text{int-signed-value } b\ v1 < \text{int-signed-value } b\ v2)$
by *force*

lemma *eq-impliesnot-less-val*:
 $\text{val-to-bool}(\text{intval-equals } v1\ v2) \longrightarrow \neg \text{val-to-bool}(\text{intval-less-than } v1\ v2)$

proof –

have *unfoldEqualDefined*: $(\text{intval-equals } v1\ v2 \neq \text{UndefVal}) \Longrightarrow$
 $(\text{val-to-bool}(\text{intval-equals } v1\ v2) \longrightarrow (\neg(\text{val-to-bool}(\text{intval-less-than } v1\ v2))))$

subgoal *premises* *p*

proof –

obtain *v1b v1v* **where** *v1v*: $v1 = \text{IntVal } v1b\ v1v$

by $(\text{metis } \text{array-length.cases } \text{intval-equals.simps}(2,3,4,5)\ p)$

obtain *v2b v2v* **where** *v2v*: $v2 = \text{IntVal } v2b\ v2v$

by $(\text{metis } \text{Value.exhaust-sel } \text{intval-equals.simps}(6,7,8,9)\ p)$

have *sameWidth*: $v1b=v2b$

by $(\text{metis } \text{bool-to-val-bin.simps } \text{intval-equals.simps}(1)\ p\ v1v\ v2v)$

have *unfoldEqual*: $\text{intval-equals } v1\ v2 = (\text{bool-to-val } (v1v=v2v))$

by $(\text{simp add: sameWidth } v1v\ v2v)$

have *unfoldLessThan*: $\text{intval-less-than } v1\ v2 = (\text{bool-to-val } (\text{int-signed-value } v1b\ v1v < \text{int-signed-value } v2b\ v2v))$

by $(\text{simp add: sameWidth } v1v\ v2v)$

have *val*: $((v1v=v2v)) \longrightarrow (\neg((\text{int-signed-value } v1b\ v1v < \text{int-signed-value } v2b\ v2v)))$

```

    using sameWidth by auto
    have doubleCast0: val-to-bool (bool-to-val ((v1v = v2v))) = (v1v = v2v)
    using bool-to-val.elims val-to-bool.simps(1) by fastforce
    have doubleCast1: val-to-bool (bool-to-val ((int-signed-value v1b v1v < int-signed-value
v2b v2v))) =
                                                    (int-signed-value v1b v1v < int-signed-value
v2b v2v)
    using bool-to-val.elims val-to-bool.simps(1) by fastforce
    then show ?thesis
    using p val unfolding unfoldEqual unfoldLessThan doubleCast0 doubleCast1
by blast
qed done
show ?thesis
by (metis Value.distinct(1) val-to-bool.elims(2) unfoldEqualDefined)
qed

```

lemma *eq-impliesnot-less-rev-val*:

```

val-to-bool(intval-equals v1 v2)  $\longrightarrow$   $\neg$ val-to-bool(intval-less-than v2 v1)
proof -
  have a: intval-equals v1 v2 = intval-equals v2 v1
  apply (cases intval-equals v1 v2 =.UndefVal)
  apply (smt (z3) bool-to-val-bin.simps intval-equals.elims intval-equals.simps)
  subgoal premises p
  proof -
    obtain v1b v1v where v1v: v1 = IntVal v1b v1v
    by (metis Value.exhaust-sel intval-equals.simps(2,3,4,5) p)
    obtain v2b v2v where v2v: v2 = IntVal v2b v2v
    by (metis Value.exhaust-sel intval-equals.simps(6,7,8,9) p)
    then show ?thesis
    by (smt (verit) bool-to-val-bin.simps intval-equals.simps(1) v1v)
  qed done
  show ?thesis
  using a eq-impliesnot-less-val by presburger
qed

```

lemma *less-impliesnot-rev-less-val*:

```

val-to-bool(intval-less-than v1 v2)  $\longrightarrow$   $\neg$ val-to-bool(intval-less-than v2 v1)
apply (rule impI)
subgoal premises p
proof -
  obtain v1b v1v where v1v: v1 = IntVal v1b v1v
  by (metis Value.exhaust-sel intval-less-than.simps(2,3,4,5) p val-to-bool.simps(2))
  obtain v2b v2v where v2v: v2 = IntVal v2b v2v
  by (metis Value.exhaust-sel intval-less-than.simps(6,7,8,9) p val-to-bool.simps(2))
  then have unfoldLessThanRHS: intval-less-than v2 v1 =
                                                    (bool-to-val (int-signed-value v2b v2v < int-signed-value
v1b v1v))
  using p v1v by force
  then have unfoldLessThanLHS: intval-less-than v1 v2 =

```

```

      (bool-to-val (int-signed-value v1b v1v < int-signed-value
v2b v2v))
    using bool-to-val-bin.simps intval-less-than.simps(1) p v1v v2v val-to-bool.simps(2)
  by auto
    then have symmetry: (int-signed-value v2b v2v < int-signed-value v1b v1v) →
      (¬(int-signed-value v1b v1v < int-signed-value v2b v2v))
      by simp
    then show ?thesis
      using p unfoldLessThanLHS unfoldLessThanRHS by fastforce
  qed done

```

lemma *less-impliesnot-eq-val*:
 $val\text{-}to\text{-}bool(intval\text{-}less\text{-}than\ v1\ v2) \longrightarrow \neg val\text{-}to\text{-}bool(intval\text{-}equals\ v1\ v2)$
 using eq-impliesnot-less-val by blast

lemma *logic-negate-type*:
 assumes $[m, p] \vdash UnaryExpr\ UnaryLogicNegation\ x \mapsto v$
 shows $\exists b\ v2. [m, p] \vdash x \mapsto IntVal\ b\ v2$
 by (metis assms UnaryExprE intval-logic-negation.elims unary-eval.simps(4))

lemma *intval-logic-negation-inverse*:
 assumes $b > 0$
 assumes $x = IntVal\ b\ v$
 shows $val\text{-}to\text{-}bool\ (intval\text{-}logic\text{-}negation\ x) \longleftrightarrow \neg(val\text{-}to\text{-}bool\ x)$
 by (cases x; auto simp: logic-negate-def assms)

lemma *logic-negation-relation-tree*:
 assumes $[m, p] \vdash y \mapsto val$
 assumes $[m, p] \vdash UnaryExpr\ UnaryLogicNegation\ y \mapsto invval$
 shows $val\text{-}to\text{-}bool\ val \longleftrightarrow \neg(val\text{-}to\text{-}bool\ invval)$
 by (metis UnaryExprE evalDet eval-bits-1-64 logic-negate-type unary-eval.simps(4) assms intval-logic-negation-inverse)

The following theorem shows that the known true/false rules are valid.

theorem *implies-impliesnot-valid*:
 shows $((q1 \Rightarrow q2) \longrightarrow (q1 \mapsto q2)) \wedge$
 $((q1 \Rightarrow \neg q2) \longrightarrow (q1 \mapsto \neg q2))$
 $(is\ (?imp \longrightarrow ?val) \wedge (?notimp \longrightarrow ?notval))$
proof (induct q1 q2 rule: impliesx-impliesnot.induct)
 case (q-imp-q q)
 then show ?case
 using evalDet by fastforce
next
 case (eq-impliesnot-less x y)
 then show ?case
 apply auto using eq-impliesnot-less-val evalDet by blast
next
 case (eq-impliesnot-less-rev x y)

```

    then show ?case
      apply auto using eq-impliesnot-less-rev-val evalDet by blast
next
case (less-impliesnot-rev-less x y)
then show ?case
  apply auto using less-impliesnot-rev-less-val evalDet by blast
next
case (less-impliesnot-eq x y)
then show ?case
  apply auto using less-impliesnot-eq-val evalDet by blast
next
case (less-impliesnot-eq-rev x y)
then show ?case
  apply auto by (metis eq-impliesnot-less-rev-val evalDet)
next
case (negate-true x y)
then show ?case
  apply auto by (metis logic-negation-relation-tree unary-eval.simps(4) unfold-unary)
next
case (negate-false x y)
then show ?case
  apply auto by (metis UnaryExpr logic-negation-relation-tree unary-eval.simps(4))
qed

```

We introduce a type *TriState::'a* (as in the GraalVM compiler) to represent when static analysis can tell us information about the value of a Boolean expression. If *Unknown::'a* then no information can be inferred and if *KnownTrue::'a*/*KnownFalse::'a* one can infer the expression is always true/false.

datatype *TriState* = *Unknown* | *KnownTrue* | *KnownFalse*

The implies relation corresponds to the *LogicNode.implies* method from the compiler which attempts to infer when one logic nodes value can be inferred from a known logic node.

```

inductive implies :: IRGraph  $\Rightarrow$  IRNode  $\Rightarrow$  IRNode  $\Rightarrow$  TriState  $\Rightarrow$  bool
  (-  $\vdash$  - & -  $\hookrightarrow$  -) for g where
    eq-imp-less:
       $g \vdash (\text{IntegerEqualsNode } x \ y) \ \& \ (\text{IntegerLessThanNode } x \ y) \hookrightarrow \text{KnownFalse} \mid$ 
    eq-imp-less-rev:
       $g \vdash (\text{IntegerEqualsNode } x \ y) \ \& \ (\text{IntegerLessThanNode } y \ x) \hookrightarrow \text{KnownFalse} \mid$ 
    less-imp-rev-less:
       $g \vdash (\text{IntegerLessThanNode } x \ y) \ \& \ (\text{IntegerLessThanNode } y \ x) \hookrightarrow \text{KnownFalse} \mid$ 
    less-imp-not-eq:
       $g \vdash (\text{IntegerLessThanNode } x \ y) \ \& \ (\text{IntegerEqualsNode } x \ y) \hookrightarrow \text{KnownFalse} \mid$ 
    less-imp-not-eq-rev:
       $g \vdash (\text{IntegerLessThanNode } x \ y) \ \& \ (\text{IntegerEqualsNode } y \ x) \hookrightarrow \text{KnownFalse} \mid$ 

    x-imp-x:
       $g \vdash x \ \& \ x \hookrightarrow \text{KnownTrue} \mid$ 

```

negate-false:
 $\llbracket g \vdash x \ \& \ (\text{kind } g \ y) \hookrightarrow \text{KnownTrue} \rrbracket \implies g \vdash x \ \& \ (\text{LogicNegationNode } y) \hookrightarrow \text{KnownFalse} \mid$
negate-true:
 $\llbracket g \vdash x \ \& \ (\text{kind } g \ y) \hookrightarrow \text{KnownFalse} \rrbracket \implies g \vdash x \ \& \ (\text{LogicNegationNode } y) \hookrightarrow \text{KnownTrue}$

Total relation over partial implies relation

inductive *condition-implies* :: *IRGraph* \Rightarrow *IRNode* \Rightarrow *IRNode* \Rightarrow *TriState* \Rightarrow *bool*
 $(- \vdash - \ \& \ - \hookrightarrow -)$ **for** *g* **where**
 $\llbracket \neg(g \vdash a \ \& \ b \hookrightarrow \text{imp}) \rrbracket \implies (g \vdash a \ \& \ b \hookrightarrow \text{Unknown}) \mid$
 $\llbracket (g \vdash a \ \& \ b \hookrightarrow \text{imp}) \rrbracket \implies (g \vdash a \ \& \ b \hookrightarrow \text{imp})$

inductive *implies-tree* :: *IRExpr* \Rightarrow *IRExpr* \Rightarrow *bool* \Rightarrow *bool*
 $(- \ \& \ - \hookrightarrow -)$ **where**
eq-imp-less:
 $(\text{BinaryExpr } \text{BinIntegerEquals } x \ y) \ \& \ (\text{BinaryExpr } \text{BinIntegerLessThan } x \ y) \hookrightarrow \text{False} \mid$
eq-imp-less-rev:
 $(\text{BinaryExpr } \text{BinIntegerEquals } x \ y) \ \& \ (\text{BinaryExpr } \text{BinIntegerLessThan } y \ x) \hookrightarrow \text{False} \mid$
less-imp-rev-less:
 $(\text{BinaryExpr } \text{BinIntegerLessThan } x \ y) \ \& \ (\text{BinaryExpr } \text{BinIntegerLessThan } y \ x) \hookrightarrow \text{False} \mid$
less-imp-not-eq:
 $(\text{BinaryExpr } \text{BinIntegerLessThan } x \ y) \ \& \ (\text{BinaryExpr } \text{BinIntegerEquals } x \ y) \hookrightarrow \text{False} \mid$
less-imp-not-eq-rev:
 $(\text{BinaryExpr } \text{BinIntegerLessThan } x \ y) \ \& \ (\text{BinaryExpr } \text{BinIntegerEquals } y \ x) \hookrightarrow \text{False} \mid$
x-imp-x:
 $x \ \& \ x \hookrightarrow \text{True} \mid$
negate-false:
 $\llbracket x \ \& \ y \hookrightarrow \text{True} \rrbracket \implies x \ \& \ (\text{UnaryExpr } \text{UnaryLogicNegation } y) \hookrightarrow \text{False} \mid$
negate-true:
 $\llbracket x \ \& \ y \hookrightarrow \text{False} \rrbracket \implies x \ \& \ (\text{UnaryExpr } \text{UnaryLogicNegation } y) \hookrightarrow \text{True}$

Proofs that the implies relation is correct with respect to the existing evaluation semantics.

lemma *logic-negation-relation:*
assumes $[g, m, p] \vdash y \mapsto \text{val}$
assumes $\text{kind } g \ \text{neg} = \text{LogicNegationNode } y$
assumes $[g, m, p] \vdash \text{neg} \mapsto \text{invval}$
assumes $\text{invval} \neq \text{UndefVal}$
shows $\text{val-to-bool } \text{val} \longleftrightarrow \neg(\text{val-to-bool } \text{invval})$
by (*metis* *assms*(1,2,3) *LogicNegationNode encodeeval-def logic-negation-relation-tree repDet*)

```

lemma implies-valid:
  assumes  $x \ \& \ y \hookrightarrow \text{imp}$ 
  assumes  $[m, p] \vdash x \mapsto v1$ 
  assumes  $[m, p] \vdash y \mapsto v2$ 
  shows  $(\text{imp} \longrightarrow (\text{val-to-bool } v1 \longrightarrow \text{val-to-bool } v2)) \wedge$ 
     $(\neg \text{imp} \longrightarrow (\text{val-to-bool } v1 \longrightarrow \neg(\text{val-to-bool } v2)))$ 
     $(\text{is } (?TP \longrightarrow ?TC) \wedge (?FP \longrightarrow ?FC))$ 
  apply (intro conjI; rule impI)
proof –
  assume KnownTrue: ?TP
  show ?TC
  using assms(1) KnownTrue assms(2–) proof (induct x y imp rule: implies-tree.induct)
    case (eq-imp-less x y)
    then show ?case
    by simp
  next
    case (eq-imp-less-rev x y)
    then show ?case
    by simp
  next
    case (less-imp-rev-less x y)
    then show ?case
    by simp
  next
    case (less-imp-not-eq x y)
    then show ?case
    by simp
  next
    case (less-imp-not-eq-rev x y)
    then show ?case
    by simp
  next
    case (x-imp-x)
    then show ?case
    by (metis evalDet)
  next
    case (negate-false x1)
    then show ?case
    using evalDet assms(2,3) by fast
  next
    case (negate-true x y)
    then show ?case
    using logic-negation-relation-tree sorry
  qed
next
  assume KnownFalse: ?FP
  show ?FC using assms KnownFalse proof (induct x y imp rule: implies-tree.induct)
    case (eq-imp-less x y)
    obtain xval where xval:  $[m, p] \vdash x \mapsto xval$ 

```

```

    using eq-imp-less(1) by blast
  then obtain yval where yval:  $[m, p] \vdash y \mapsto yval$ 
    using eq-imp-less.prem(2) by blast
  have egeval:  $[m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval-equals\ xval\ yval$ 
    by (metis xval yval BinaryExprE bin-eval.simps(13) eq-imp-less.prem(1) evalDet)
  have lesseval:  $[m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval-less-than\ xval\ yval$ 
    by (metis xval yval BinaryExprE bin-eval.simps(14) eq-imp-less.prem(2) evalDet)
  have val-to-bool (intval-equals xval yval)  $\longrightarrow \neg(val-to-bool\ (intval-less-than\ xval\ yval))$ 
    apply (cases xval; cases yval; auto)
    by (smt (verit, best) bool-to-val.simps(2) val-to-bool.simps(1))
  then show ?case
    by (metis egeval lesseval eq-imp-less.prem(1,2) evalDet)
next
case (eq-imp-less-rev x y)
obtain xval where xval:  $[m, p] \vdash x \mapsto xval$ 
  using eq-imp-less-rev.prem(2) by blast
obtain yval where yval:  $[m, p] \vdash y \mapsto yval$ 
  using eq-imp-less-rev.prem(2) by blast
have egeval:  $[m, p] \vdash (BinaryExpr\ BinIntegerEquals\ x\ y) \mapsto intval-equals\ xval\ yval$ 
  by (metis xval yval BinaryExprE bin-eval.simps(13) eq-imp-less-rev.prem(1) evalDet)
have lesseval:  $[m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto intval-less-than\ yval\ xval$ 
  by (metis xval yval BinaryExprE bin-eval.simps(14) eq-imp-less-rev.prem(2) evalDet)
have val-to-bool (intval-equals xval yval)  $\longrightarrow \neg(val-to-bool\ (intval-less-than\ yval\ xval))$ 
  apply (cases xval; cases yval; auto)
  by (metis (full-types) bool-to-val.simps(2) less-irrefl val-to-bool.simps(1))
then show ?case
  by (metis eq-imp-less-rev.prem(1) eq-imp-less-rev.prem(2) evalDet egeval lesseval)
next
case (less-imp-rev-less x y)
obtain xval where xval:  $[m, p] \vdash x \mapsto xval$ 
  using less-imp-rev-less.prem(2) by blast
obtain yval where yval:  $[m, p] \vdash y \mapsto yval$ 
  using less-imp-rev-less.prem(2) by blast
have lesseval:  $[m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ x\ y) \mapsto intval-less-than\ xval\ yval$ 
  by (metis BinaryExprE bin-eval.simps(14) evalDet less-imp-rev-less.prem(1) xval yval)
have revlesseval:  $[m, p] \vdash (BinaryExpr\ BinIntegerLessThan\ y\ x) \mapsto intval-less-than$ 

```



```

yval xval
  by (metis BinaryExprE bin-eval.simps(14) evalDet less-imp-rev-less.prem(2)
xval yval)
  have val-to-bool (intval-less-than xval yval)  $\longrightarrow$   $\neg$ (val-to-bool (intval-less-than
yval xval))
  apply (cases xval; cases yval; auto)
  by (smt (verit) bool-to-val.simps(2) val-to-bool.simps(1))
  then show ?case
  by (metis evalDet less-imp-rev-less.prem(1,2) lesseval revlesseval)
next
case (less-imp-not-eq x y)
obtain xval where xval:  $[m, p] \vdash x \mapsto xval$ 
  using less-imp-not-eq.prem(1) by blast
obtain yval where yval:  $[m, p] \vdash y \mapsto yval$ 
  using less-imp-not-eq.prem(1) by blast
have egeval:  $[m, p] \vdash (BinaryExpr BinIntegerEquals x y) \mapsto intval-equals xval$ 
yval
  by (metis BinaryExprE bin-eval.simps(13) evalDet less-imp-not-eq.prem(2)
xval yval)
  have lesseval:  $[m, p] \vdash (BinaryExpr BinIntegerLessThan x y) \mapsto intval-less-than$ 
xval yval
  by (metis BinaryExprE bin-eval.simps(14) evalDet less-imp-not-eq.prem(1)
xval yval)
  have val-to-bool (intval-less-than xval yval)  $\longrightarrow$   $\neg$ (val-to-bool (intval-equals xval
yval))
  apply (cases xval; cases yval; auto)
  by (smt (verit, best) bool-to-val.simps(2) val-to-bool.simps(1))
  then show ?case
  by (metis egeval evalDet less-imp-not-eq.prem(1,2) lesseval)
next
case (less-imp-not-eq-rev x y)
obtain xval where xval:  $[m, p] \vdash x \mapsto xval$ 
  using less-imp-not-eq-rev.prem(1) by blast
obtain yval where yval:  $[m, p] \vdash y \mapsto yval$ 
  using less-imp-not-eq-rev.prem(1) by blast
have egeval:  $[m, p] \vdash (BinaryExpr BinIntegerEquals y x) \mapsto intval-equals yval$ 
xval
  by (metis xval yval BinaryExprE bin-eval.simps(13) evalDet less-imp-not-eq-rev.prem(2))
  have lesseval:  $[m, p] \vdash (BinaryExpr BinIntegerLessThan x y) \mapsto intval-less-than$ 
xval yval
  by (metis xval yval BinaryExprE bin-eval.simps(14) evalDet less-imp-not-eq-rev.prem(1))
  have val-to-bool (intval-less-than xval yval)  $\longrightarrow$   $\neg$ (val-to-bool (intval-equals yval
xval))
  apply (cases xval; cases yval; auto)
  by (smt (verit, best) bool-to-val.simps(2) val-to-bool.simps(1))
  then show ?case
  by (metis egeval evalDet less-imp-not-eq-rev.prem(1,2) lesseval)
next
case (x-imp-x x1)

```

```

    then show ?case
    by simp
next
  case (negate-false x y)
  then show ?case sorry
next
  case (negate-true x1)
  then show ?case
    by simp
qed
qed

```

```

lemma implies-true-valid:
  assumes  $x \ \& \ y \hookrightarrow imp$ 
  assumes  $imp$ 
  assumes  $[m, p] \vdash x \mapsto v1$ 
  assumes  $[m, p] \vdash y \mapsto v2$ 
  shows  $val\text{-}to\text{-}bool \ v1 \longrightarrow val\text{-}to\text{-}bool \ v2$ 
  using assms implies-valid by blast

```

```

lemma implies-false-valid:
  assumes  $x \ \& \ y \hookrightarrow imp$ 
  assumes  $\neg imp$ 
  assumes  $[m, p] \vdash x \mapsto v1$ 
  assumes  $[m, p] \vdash y \mapsto v2$ 
  shows  $val\text{-}to\text{-}bool \ v1 \longrightarrow \neg(val\text{-}to\text{-}bool \ v2)$ 
  using assms implies-valid by blast

```

The following relation corresponds to the `UnaryOpLogicNode.tryFold` and `BinaryOpLogicNode.tryFold` methods and their associated concrete implementations.

The relation determines if a logic operation can be shown true or false through the stamp typing information.

```

inductive tryFold ::  $IRNode \Rightarrow (ID \Rightarrow Stamp) \Rightarrow bool \Rightarrow bool$ 
  where
     $\llbracket alwaysDistinct \ (stamps \ x) \ (stamps \ y) \rrbracket$ 
       $\Longrightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ False \mid$ 
     $\llbracket neverDistinct \ (stamps \ x) \ (stamps \ y) \rrbracket$ 
       $\Longrightarrow tryFold \ (IntegerEqualsNode \ x \ y) \ stamps \ True \mid$ 
     $\llbracket is\text{-}IntegerStamp \ (stamps \ x);$ 
       $is\text{-}IntegerStamp \ (stamps \ y);$ 
       $stpi\text{-}upper \ (stamps \ x) < stpi\text{-}lower \ (stamps \ y) \rrbracket$ 
       $\Longrightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ True \mid$ 
     $\llbracket is\text{-}IntegerStamp \ (stamps \ x);$ 
       $is\text{-}IntegerStamp \ (stamps \ y);$ 
       $stpi\text{-}lower \ (stamps \ x) \geq stpi\text{-}upper \ (stamps \ y) \rrbracket$ 
       $\Longrightarrow tryFold \ (IntegerLessThanNode \ x \ y) \ stamps \ False$ 

```

Proofs that show that when the stamp lookup function is well-formed, the

tryFold relation correctly predicts the output value with respect to our evaluation semantics.

lemma

assumes *kind g nid = IntegerEqualsNode x y*
assumes $[g, m, p] \vdash \text{nid} \mapsto v$
assumes $([g, m, p] \vdash x \mapsto \text{xval}) \wedge ([g, m, p] \vdash y \mapsto \text{yval})$
shows $\text{val-to-bool} (\text{intval-equals } \text{xval } \text{yval}) \longleftrightarrow v = \text{IntVal } 32 \ 1$

proof –

have $v = \text{intval-equals } \text{xval } \text{yval}$
by (*smt (verit) bin-eval.simps(13) encodeeval-def evalDet repDet IntegerEqualsNode BinaryExprE*
assms)
then show *?thesis*
by (*metis bool-to-val.simps(1,2) one-neq-zero val-to-bool.simps(1,2) intval-equals-result*)
qed

lemma *tryFoldIntegerEqualsAlwaysDistinct:*

assumes *wf-stamp g stamps*
assumes *kind g nid = (IntegerEqualsNode x y)*
assumes $[g, m, p] \vdash \text{nid} \mapsto v$
assumes *alwaysDistinct (stamps x) (stamps y)*
shows $v = \text{IntVal } 32 \ 0$

proof –

have $\forall \text{ val. } \neg(\text{valid-value } \text{val} (\text{join } (\text{stamps } x) (\text{stamps } y)))$
by (*smt (verit, best) is-stamp-empty.elims(2) valid-int valid-value.simps(1)*
assms(1,4)
alwaysDistinct.simps)
obtain xv **where** $[g, m, p] \vdash x \mapsto xv$
using *assms unfolding encodeeval-def sorry*
have $\neg(\exists \text{ val} . ([g, m, p] \vdash x \mapsto \text{val}) \wedge ([g, m, p] \vdash y \mapsto \text{val}))$
using *assms(1,4) unfolding alwaysDistinct.simps wf-stamp.simps encodeeval-def sorry*
then show *?thesis*
sorry
qed

lemma *tryFoldIntegerEqualsNeverDistinct:*

assumes *wf-stamp g stamps*
assumes *kind g nid = (IntegerEqualsNode x y)*
assumes $[g, m, p] \vdash \text{nid} \mapsto v$
assumes *neverDistinct (stamps x) (stamps y)*
shows $v = \text{IntVal } 32 \ 1$
using *assms IntegerEqualsNodeE sorry*

lemma *tryFoldIntegerLessThanTrue:*

assumes *wf-stamp g stamps*
assumes *kind g nid = (IntegerLessThanNode x y)*
assumes $[g, m, p] \vdash \text{nid} \mapsto v$
assumes *stpi-upper (stamps x) < stpi-lower (stamps y)*

```

shows  $v = \text{IntVal } 32 \ 1$ 
proof -
  have stamp-type: is-IntegerStamp (stamps x)
    using assms
  sorry
  obtain xval where xval:  $[g, m, p] \vdash x \mapsto xval$ 
    using assms(2,3) sorry
  obtain yval where yval:  $[g, m, p] \vdash y \mapsto yval$ 
    using assms(2,3) sorry
  have is-IntegerStamp (stamps x)  $\wedge$  is-IntegerStamp (stamps y)
    using assms(4)
  sorry
  then have val-to-bool (intval-less-than xval yval)
    sorry
  then show ?thesis
    sorry
qed

```

```

lemma tryFoldIntegerLessThanFalse:
  assumes wf-stamp g stamps
  assumes kind g nid = (IntegerLessThanNode x y)
  assumes  $[g, m, p] \vdash \text{nid} \mapsto v$ 
  assumes stpi-lower (stamps x)  $\geq$  stpi-upper (stamps y)
  shows  $v = \text{IntVal } 32 \ 0$ 
  proof -
    have stamp-type: is-IntegerStamp (stamps x)
      using assms sorry
    obtain xval where xval:  $[g, m, p] \vdash x \mapsto xval$ 
      using assms(2,3) sorry
    obtain yval where yval:  $[g, m, p] \vdash y \mapsto yval$ 
      using assms(2,3) sorry
    have is-IntegerStamp (stamps x)  $\wedge$  is-IntegerStamp (stamps y)
      using assms(4) sorry
    then have  $\neg(\text{val-to-bool } (\text{intval-less-than } xval \ yval))$ 
      sorry
    then show ?thesis
      sorry
  qed

```

```

theorem tryFoldProofTrue:
  assumes wf-stamp g stamps
  assumes tryFold (kind g nid) stamps True
  assumes  $[g, m, p] \vdash \text{nid} \mapsto v$ 
  shows val-to-bool v
  using assms(2) proof (induction kind g nid stamps True rule: tryFold.induct)
  case (1 stamps x y)
    then show ?case
      using tryFoldIntegerEqualsAlwaysDistinct assms by force
  next

```

```

    case (2 stamps x y)
    then show ?case
      by (smt (verit, best) one-neq-zero tryFold.cases tryFoldIntegerEqualsNeverDis-
        tinct assms
          tryFoldIntegerLessThanTrue val-to-bool.simps(1))
  next
    case (3 stamps x y)
    then show ?case
      by (smt (verit, best) one-neq-zero tryFold.cases tryFoldIntegerEqualsNeverDis-
        tinct assms
          val-to-bool.simps(1) tryFoldIntegerLessThanTrue)
  next
    case (4 stamps x y)
    then show ?case
      by force
qed

```

```

theorem tryFoldProofFalse:
  assumes wf-stamp g stamps
  assumes tryFold (kind g nid) stamps False
  assumes  $[g, m, p] \vdash \text{nid} \mapsto v$ 
  shows  $\neg(\text{val-to-bool } v)$ 
using assms(2) proof (induction kind g nid stamps False rule: tryFold.induct)
  case (1 stamps x y)
  then show ?case
    by (smt (verit) tryFoldIntegerLessThanFalse tryFoldIntegerEqualsAlwaysDis-
      tinct tryFold.cases
        tryFoldIntegerEqualsNeverDistinct val-to-bool.simps(1) assms)
  next
    case (2 stamps x y)
    then show ?case
      by blast
  next
    case (3 stamps x y)
    then show ?case
      by blast
  next
    case (4 stamps x y)
    then show ?case
      by (smt (verit, del-insts) tryFold.cases tryFoldIntegerEqualsAlwaysDistinct
        val-to-bool.simps(1)
          tryFoldIntegerLessThanFalse assms)
qed

```

```

inductive-cases StepE:
   $g, p \vdash (\text{nid}, m, h) \rightarrow (\text{nid}', m', h)$ 

```

Perform conditional elimination rewrites on the graph for a particular node.
 In order to determine conditional eliminations appropriately the rule needs

two data structures produced by static analysis. The first parameter is the set of IRNodes that we know result in a true value when evaluated. The second parameter is a mapping from node identifiers to the flow-sensitive stamp.

The relation transforms the third parameter to the fifth parameter for a node identifier which represents the fourth parameter.

inductive *ConditionalEliminationStep* ::

IRExpr set \Rightarrow (*ID* \Rightarrow *Stamp*) \Rightarrow *IRGraph* \Rightarrow *ID* \Rightarrow *IRGraph* \Rightarrow *bool* **where**

impliesTrue:

$\llbracket \text{kind } g \text{ ifcond} = (\text{IfNode } cid \ t \ f);$
 $g \vdash cid \simeq cond;$
 $\exists ce \in conds . (ce \Rightarrow cond);$
 $g' = \text{constantCondition } True \text{ ifcond } (kind \ g \text{ ifcond}) \ g$
 $\rrbracket \Rightarrow \text{ConditionalEliminationStep } conds \ stamps \ g \text{ ifcond } g' \mid$

impliesFalse:

$\llbracket \text{kind } g \text{ ifcond} = (\text{IfNode } cid \ t \ f);$
 $g \vdash cid \simeq cond;$
 $\exists ce \in conds . (ce \Rightarrow \neg cond);$
 $g' = \text{constantCondition } False \text{ ifcond } (kind \ g \text{ ifcond}) \ g$
 $\rrbracket \Rightarrow \text{ConditionalEliminationStep } conds \ stamps \ g \text{ ifcond } g' \mid$

tryFoldTrue:

$\llbracket \text{kind } g \text{ ifcond} = (\text{IfNode } cid \ t \ f);$
 $cond = kind \ g \ cid;$
 $\text{tryFold } (kind \ g \ cid) \ stamps \ True;$
 $g' = \text{constantCondition } True \text{ ifcond } (kind \ g \text{ ifcond}) \ g$
 $\rrbracket \Rightarrow \text{ConditionalEliminationStep } conds \ stamps \ g \text{ ifcond } g' \mid$

tryFoldFalse:

$\llbracket \text{kind } g \text{ ifcond} = (\text{IfNode } cid \ t \ f);$
 $cond = kind \ g \ cid;$
 $\text{tryFold } (kind \ g \ cid) \ stamps \ False;$
 $g' = \text{constantCondition } False \text{ ifcond } (kind \ g \text{ ifcond}) \ g$
 $\rrbracket \Rightarrow \text{ConditionalEliminationStep } conds \ stamps \ g \text{ ifcond } g' \mid$

code-pred (*modes*: $i \Rightarrow i \Rightarrow i \Rightarrow i \Rightarrow o \Rightarrow bool$) *ConditionalEliminationStep* .

thm *ConditionalEliminationStep.equation*

1.2 Control-flow Graph Traversal

type-synonym *Seen* = *ID set*

type-synonym *Condition* = *IRExpr*

type-synonym *Conditions* = *Condition list*

type-synonym *StampFlow* = (*ID* \Rightarrow *Stamp*) *list*

nextEdge helps determine which node to traverse next by returning the first

successor edge that isn't in the set of already visited nodes. If there is not an appropriate successor, None is returned instead.

```
fun nextEdge :: Seen ⇒ ID ⇒ IRGraph ⇒ ID option where
  nextEdge seen nid g =
    (let nids = (filter (λnid'. nid' ∉ seen) (successors-of (kind g nid))) in
     (if length nids > 0 then Some (hd nids) else None))
```

pred determines which node, if any, acts as the predecessor of another.

Merge nodes represent a special case where-in the predecessor exists as an input edge of the merge node, to simplify the traversal we treat only the first input end node as the predecessor, ignoring that multiple nodes may act as a successor.

For all other nodes, the predecessor is the first element of the predecessors set. Note that in a well-formed graph there should only be one element in the predecessor set.

```
fun pred :: IRGraph ⇒ ID ⇒ ID option where
  pred g nid = (case kind g nid of
    (MergeNode ends -) ⇒ Some (hd ends) |
    - ⇒
      (if IRGraph.predecessors g nid = {}
       then None else
        Some (hd (sorted-list-of-set (IRGraph.predecessors g nid)))
      )
  )
```

When the basic block of an if statement is entered, we know that the condition of the preceding if statement must be true. As in the GraalVM compiler, we introduce the registerNewCondition function which roughly corresponds to the ConditionalEliminationPhase.registerNewCondition. This method updates the flow-sensitive stamp information based on the condition which we know must be true.

```
fun clip-upper :: Stamp ⇒ int ⇒ Stamp where
  clip-upper (IntegerStamp b l h) c = (IntegerStamp b l c) |
  clip-upper s c = s
fun clip-lower :: Stamp ⇒ int ⇒ Stamp where
  clip-lower (IntegerStamp b l h) c = (IntegerStamp b c h) |
  clip-lower s c = s
```

```
fun registerNewCondition :: IRGraph ⇒ IRNode ⇒ (ID ⇒ Stamp) ⇒ (ID ⇒ Stamp) where
```

```
  registerNewCondition g (IntegerEqualsNode x y) stamps =
    (stamps
     (x := join (stamps x) (stamps y)))
    (y := join (stamps x) (stamps y)) |
```

```

registerNewCondition g (IntegerLessThanNode x y) stamps =
  (stamps
    (x := clip-upper (stamps x) (stpi-lower (stamps y))))
    (y := clip-lower (stamps y) (stpi-upper (stamps x))) |
registerNewCondition g - stamps = stamps

```

```

fun hdOr :: 'a list ⇒ 'a ⇒ 'a where
  hdOr (x # xs) de = x |
  hdOr [] de = de

```

The Step relation is a small-step traversal of the graph which handles transitions between individual nodes of the graph.

It relates a pairs of tuple of the current node, the set of seen nodes, the always true stack of IfNode conditions, and the flow-sensitive stamp information.

inductive Step

```

:: IRGraph ⇒ (ID × Seen × Conditions × StampFlow) ⇒ (ID × Seen × Con-
ditions × StampFlow) option ⇒ bool

```

for g **where**

— Hit a BeginNode with an IfNode predecessor which represents the start of a basic block for the IfNode. 1. nid' will be the successor of the begin node. 2. Find the first and only predecessor. 3. Extract condition from the preceding IfNode. 4. Negate condition if the begin node is second branch (we've taken the else branch of the condition) 5. Add the condition or the negated condition to stack 6. Perform any stamp updates based on the condition using the registerNewCondition function and place them on the top of the stack of stamp information

```

[[kind g nid = BeginNode nid';

```

```

  nid ∉ seen;
  seen' = {nid} ∪ seen;

```

```

  Some ifcond = pred g nid;
  kind g ifcond = IfNode cond t f;

```

```

  i = find-index nid (successors-of (kind g ifcond));
  c = (if i = 0 then kind g cond else LogicNegationNode cond);
  rep g cond ce;
  ce' = (if i = 0 then ce else UnaryExpr UnaryLogicNegation ce);
  conds' = ce' # conds;

```

```

  flow' = registerNewCondition g c (hdOr flow (stamp g))]]
⇒⇒ Step g (nid, seen, conds, flow) (Some (nid', seen', conds', flow' # flow)) |

```

— Hit an EndNode 1. nid' will be the usage of EndNode 2. pop the conditions and stamp stack

```

[[kind g nid = EndNode;

```

```

  nid ∉ seen;
  seen' = {nid} ∪ seen;

```


$nid' = \text{any-usage } g \text{ } nid;$

$conds' = \text{tl } conds;$

$\text{flow}' = \text{tl } \text{flow} \parallel$

$\implies \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } (\text{Some } (nid', \text{seen}', conds', \text{flow}')) \mid$

— We can find a successor edge that is not in seen, go there

$\llbracket \neg(\text{is-EndNode } (kind \text{ } g \text{ } nid));$

$\neg(\text{is-BEGINNode } (kind \text{ } g \text{ } nid));$

$nid \notin \text{seen};$

$\text{seen}' = \{nid\} \cup \text{seen};$

$\text{Some } nid' = \text{nextEdge } \text{seen}' \text{ } nid \text{ } g \parallel$

$\implies \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } (\text{Some } (nid', \text{seen}', conds', \text{flow}')) \mid$

— We cannot find a successor edge that is not in seen, give back None

$\llbracket \neg(\text{is-EndNode } (kind \text{ } g \text{ } nid));$

$\neg(\text{is-BEGINNode } (kind \text{ } g \text{ } nid));$

$nid \notin \text{seen};$

$\text{seen}' = \{nid\} \cup \text{seen};$

$\text{None} = \text{nextEdge } \text{seen}' \text{ } nid \text{ } g \parallel$

$\implies \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } \text{None} \mid$

— We've already seen this node, give back None

$\llbracket nid \in \text{seen} \rrbracket \implies \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } \text{None}$

code-pred ($\text{modes: } i \Rightarrow i \Rightarrow o \Rightarrow \text{bool}$) $\text{Step} .$

The ConditionalEliminationPhase relation is responsible for combining the individual traversal steps from the Step relation and the optimizations from the ConditionalEliminationStep relation to perform a transformation of the whole graph.

inductive ConditionalEliminationPhase

$:: \text{IRGraph} \Rightarrow (\text{ID} \times \text{Seen} \times \text{Conditions} \times \text{StampFlow}) \Rightarrow \text{IRGraph} \Rightarrow \text{bool}$

where

— Can do a step and optimise for the current node

$\llbracket \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } (\text{Some } (nid', \text{seen}', conds', \text{flow}'));$

$\text{ConditionalEliminationStep } (\text{set } conds) \text{ } (\text{hdOr } \text{flow } (\text{stamp } g)) \text{ } g \text{ } nid \text{ } g';$

$\text{ConditionalEliminationPhase } g' \text{ } (nid', \text{seen}', conds', \text{flow}') \text{ } g' \parallel$

$\implies \text{ConditionalEliminationPhase } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } g'' \mid$

— Can do a step, matches whether optimised or not causing non-determinism We need to find a way to negate ConditionalEliminationStep

$\llbracket \text{Step } g \text{ } (nid, \text{seen}, conds, \text{flow}) \text{ } (\text{Some } (nid', \text{seen}', conds', \text{flow}'));$

ConditionalEliminationPhase g (nid', seen', conds', flow') g |
 \implies *ConditionalEliminationPhase g (nid, seen, conds, flow) g'* |

— Can't do a step but there is a predecessor we can backtrace to
 \llbracket Step *g* (*nid*, *seen*, *conds*, *flow*) *None*;
Some nid' = pred g nid;
seen' = {nid} \cup seen;
ConditionalEliminationPhase g (nid', seen', conds, flow) g |
 \implies *ConditionalEliminationPhase g (nid, seen, conds, flow) g'* |

— Can't do a step and have no predecessors so terminate
 \llbracket Step *g* (*nid*, *seen*, *conds*, *flow*) *None*;
None = pred g nid |
 \implies *ConditionalEliminationPhase g (nid, seen, conds, flow) g*

code-pred (*modes*: *i* \Rightarrow *i* \Rightarrow *o* \Rightarrow *bool*) *ConditionalEliminationPhase* .

definition *runConditionalElimination* :: *IRGraph* \Rightarrow *IRGraph* **where**
runConditionalElimination g =
 (*Predicate.the* (*ConditionalEliminationPhase-i-i-o g* (*0*, *{}*, (*[]*, *[]*))))

end