



$$\begin{bmatrix} 0.2 & 0.4 & \dots & 0.1 \\ 0.1 & \ddots & 0.4 & \vdots \\ \vdots & 0.2 & 0.5 & 0.0 \\ 1.0 & \dots & \dots & 0.0 \end{bmatrix}$$

chips

size

sampling

pre-trained neural network

top level

spatial lag

modelling

8

16

32

64

baseline

sliding

proportional

EfficientNetB4

GAP2D + Dense + Softmax

GAP2D + Dense + Sigmoid
(if proportional sampling)

exclude

include

maxprob

logite

HGBC

options