2024 Digital IC Design Homework 5

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| NAME | 陳育政 | | | |
| Student ID | E24094198 | | | |
| Score |  | | | |
| **Simulation Result** | | | | |
| Functional simulation | | Completed / Fail | Gate-level simulation | Completed / Fail |
| (your functional sim result) | | | (your gate-level sim result) | |
| **Description of your design** | | | | |
|  | | | | |

The scoring standard: (The smaller, the better)

*Scoring =*

*Area cost \* Timing cost*

*Area cost =*

*Total logic elements + total memory bits + 9\*embedded multiplier 9-bit elements*

*Timing cost =*

*Simulation time*

*A screenshot of a computer program

Description automatically generated*