## Curriculum Vitae

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## **Work and Education Experience**

2010- PhD in Computer Science (ongoing)

ETH Zurich

Subject: Combination of automated theorem proving and program

verification

Advisor: Prof. Peter Müller

Developed a technique for a tighter integration between a theorem prover and a program verifier, rather than having the verifier use the

prover as a black-box.

Also did some work on the AST and type system of a verification tool

project in the group: <u>Technical report</u>

2003-2009 Verification Engineer

Intel Development Center, Haifa

Validated one subunit of a CPU by developing a logical reference model and assertions and verifying them using a bounded model checking (BMC) tool. Combined BMC with testing and coverage for validating the parts of

the unit where BMC is insufficient. Developed and maintained the

reference model for successive version of the CPU.

2005-2008 MSc in Computer Science (Cum Laude)

Tel Aviv University, Israel

Thesis: Modular Verification with Shared Abstractions

Advisor: Prof. Mooly Sagiv Workshop publication

2001-2004 BA in Mathematics with Computer Science (Cum Laude)

Technion. Israel

Academic project (with Sivan Bercovici) - physical simulation of objects

using spring systems Project page

**Programming languages:** C++, Scala, C#, x86 assembly, some experience with OpenGL and Java **Areas of interest:** Program verification, program analysis, automated theorem proving, compilation, programming languages, computer graphics - real-time rendering

## **Personal Information**

Date of Birth : 18/01/1979

Citizenship : Israeli

Languages : Hebrew(native), English(fluent)