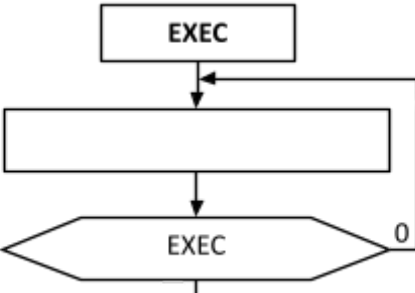
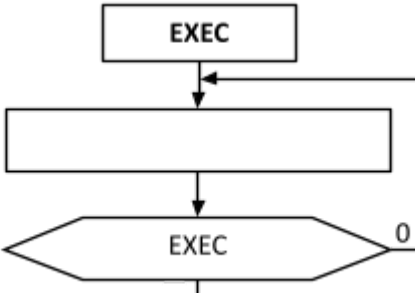
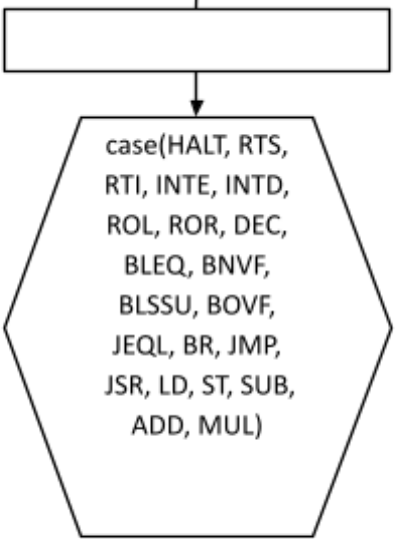
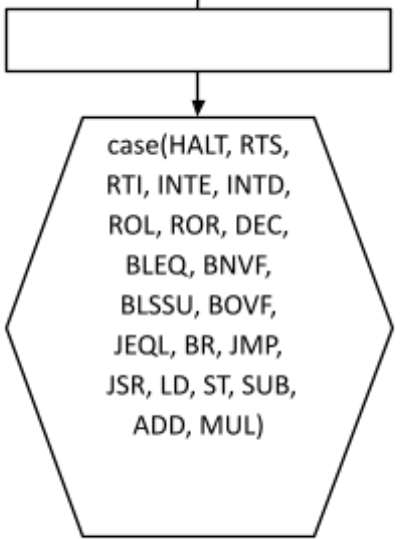
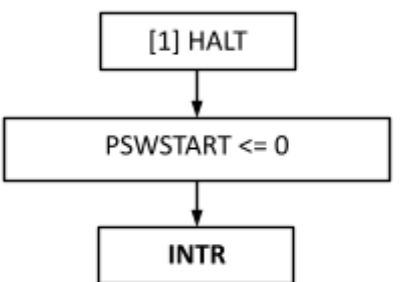
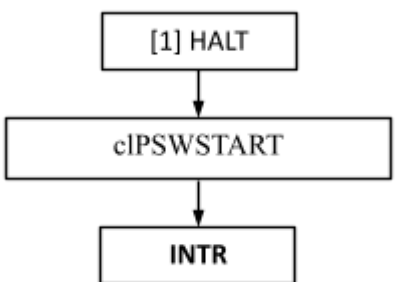
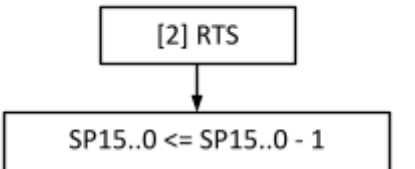
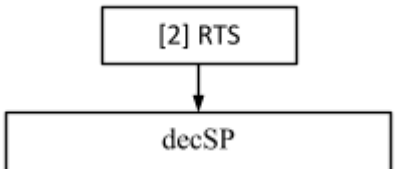
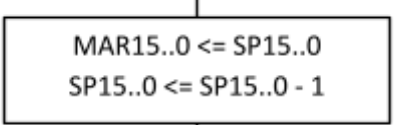
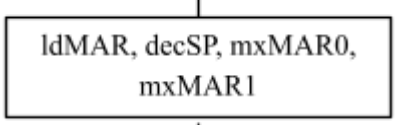
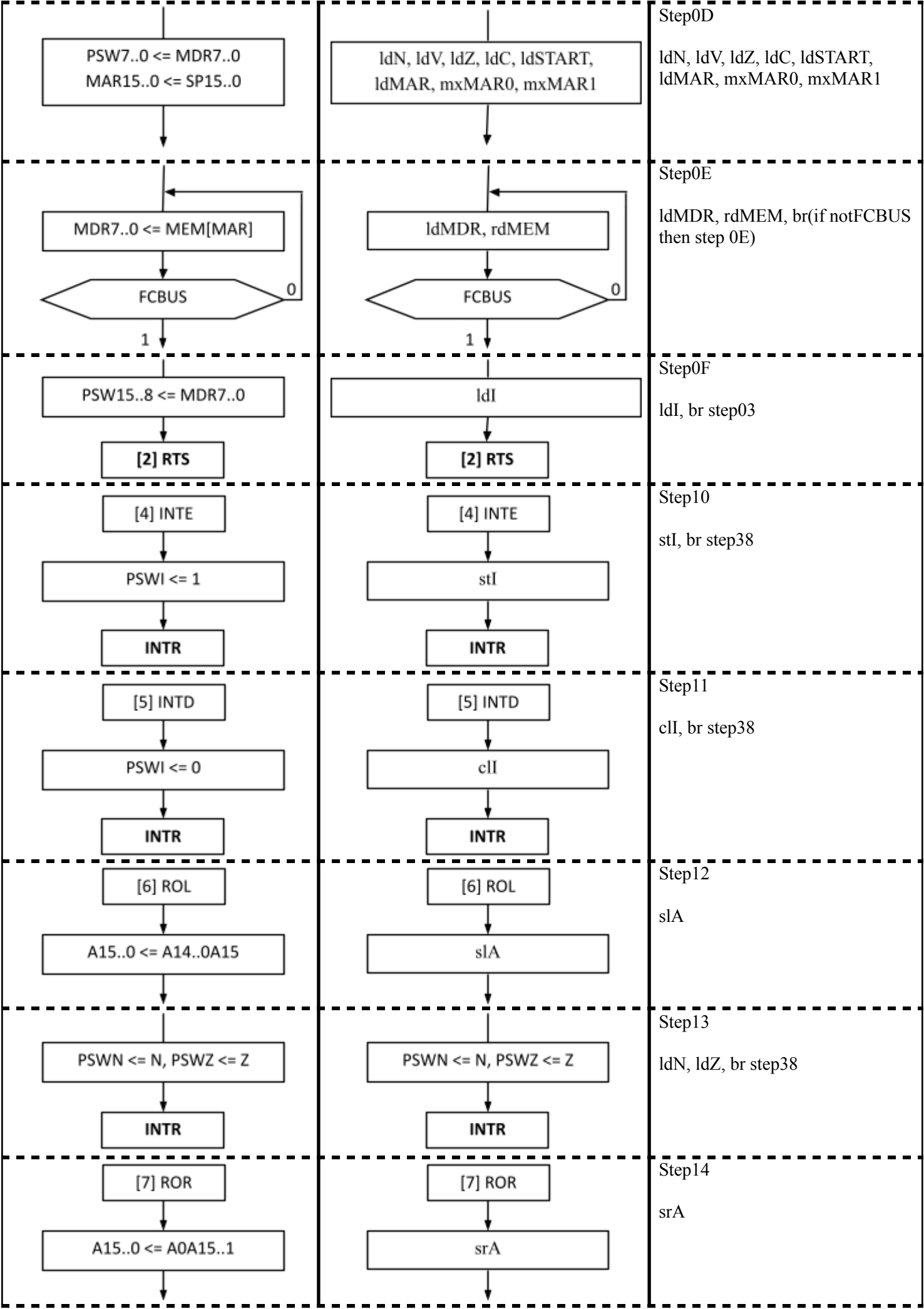
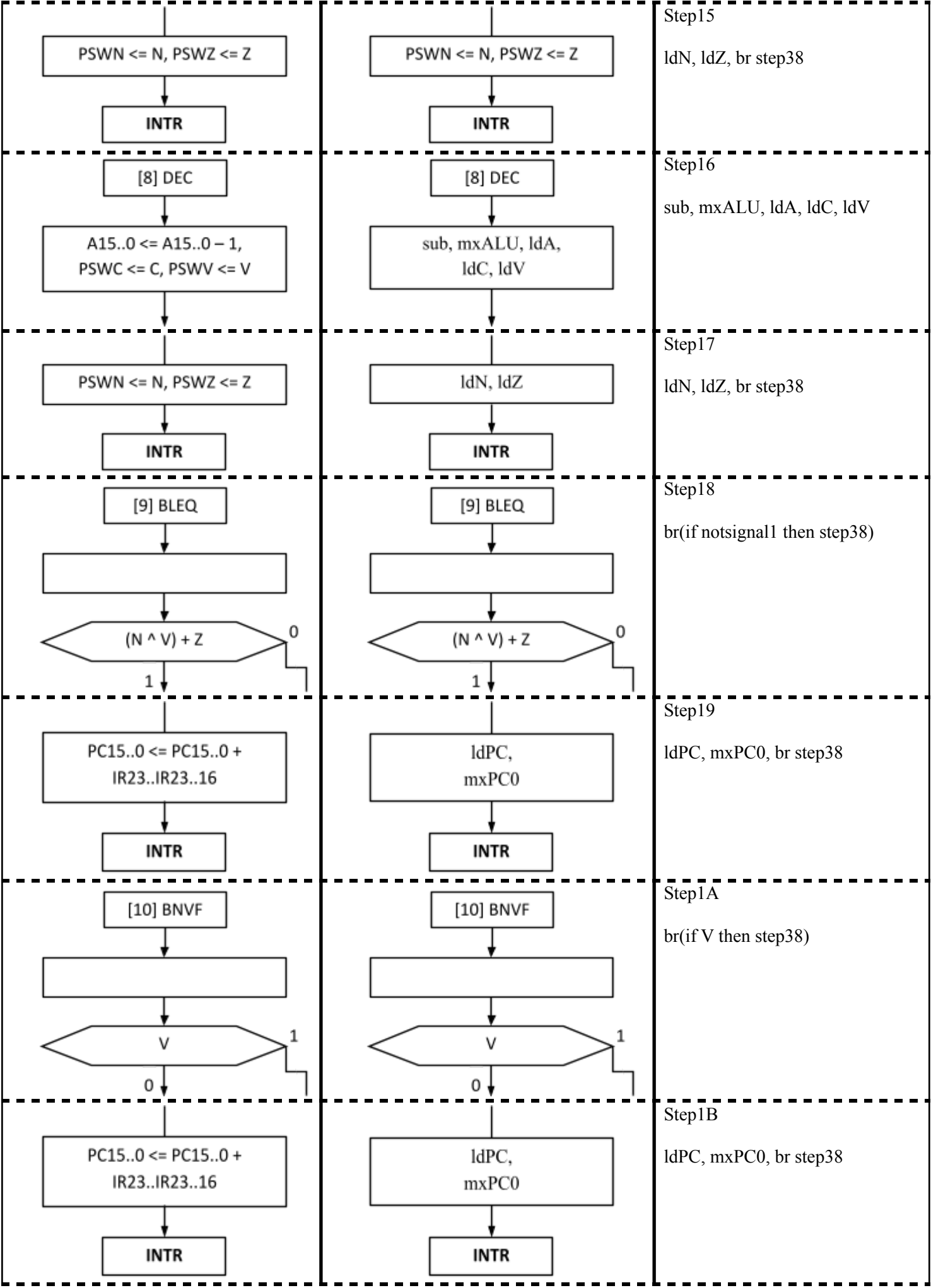
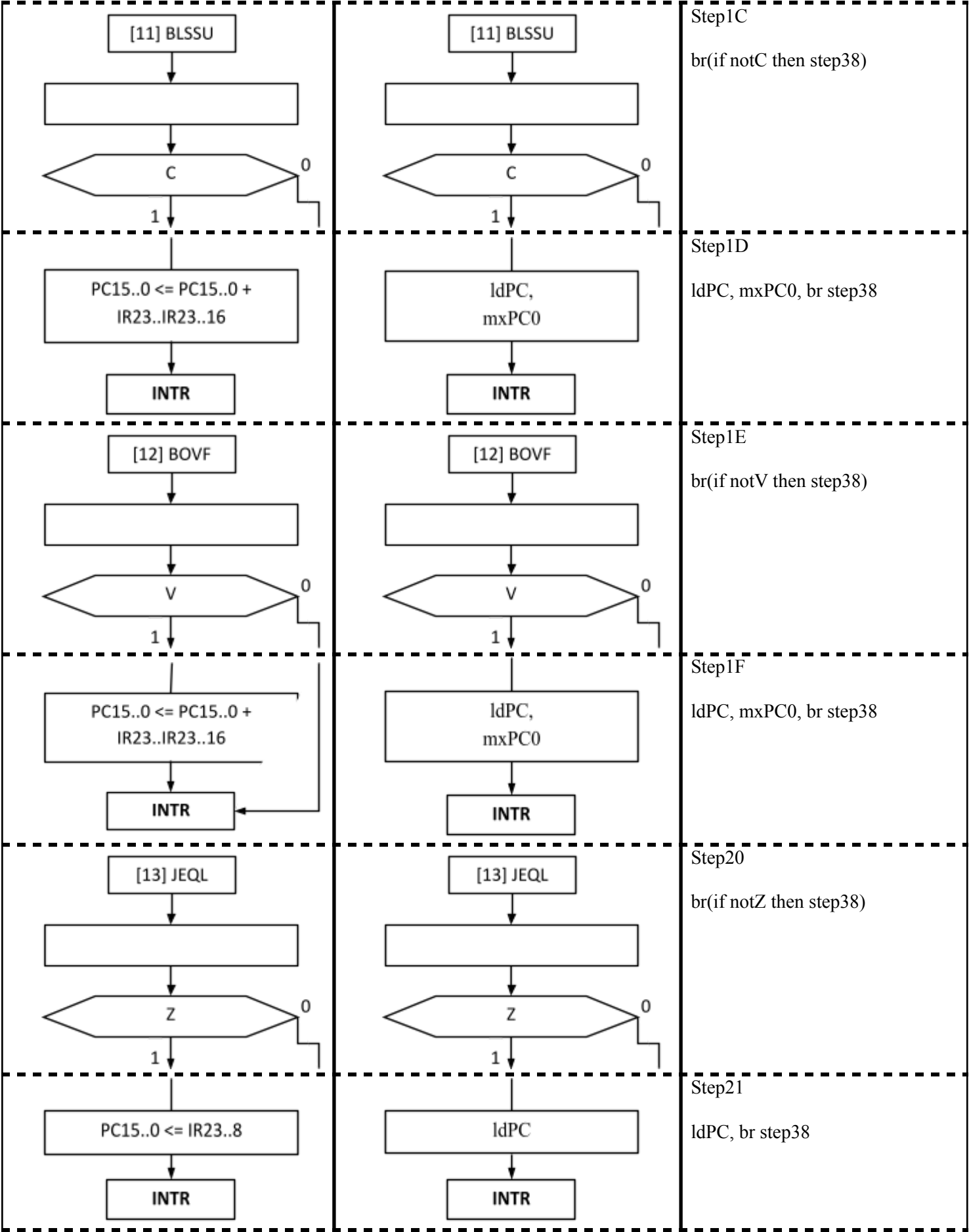


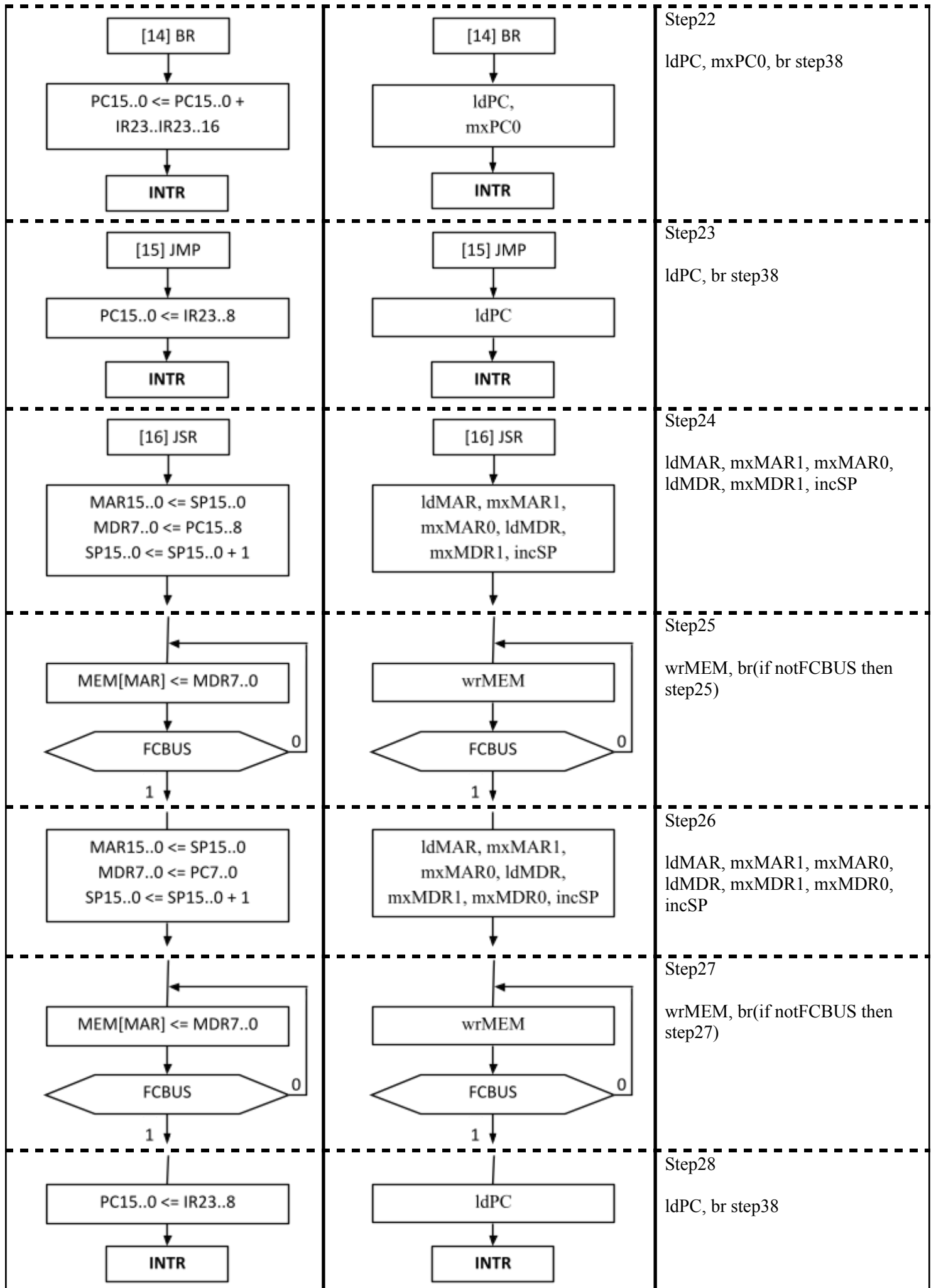
Дијаграм тока микрооперација	Дијаграм тока управљачких сигнала	Секвенца управљачких сигнала
		<p>Step00</p> <p>br(if notEXEC then step00)</p>
		<p>Step01</p> <p>br(case(HALT, RTS, RTI, INTE, INTD, ROL, ROR, DEC, BLEQ, BNVF, BLSSU, BOVF, JEQL, BR, JMP, JSR, LD, ST, SUB, ADD, MUL) then (HALT, step02), (RTS, step03), (RTI, step0A), (INTE, step10), (INTD, step11), (ROL, step12), (ROR, step14), (DEC, step16), (BLEQ, step18), (BNVF, step1A), (BLSSU, step1C), (BOVF, step1E), (JEQL, step20), (BR, step22), (JMP, step23), (JSR, step24), (LD, step29), (ST, step2B), (SUB, step32), (ADD, step34), (MUL, step36))</p>
<div>1 2 ... 21</div> 	<div>1 2 ... 21</div> 	<p>Step02</p> <p>cIPSWSTART, br step38</p>
		<p>Step03</p> <p>decSP</p>
		<p>Step04</p> <p>ldMAR, decSP, mxMAR0, mxMAR1</p>

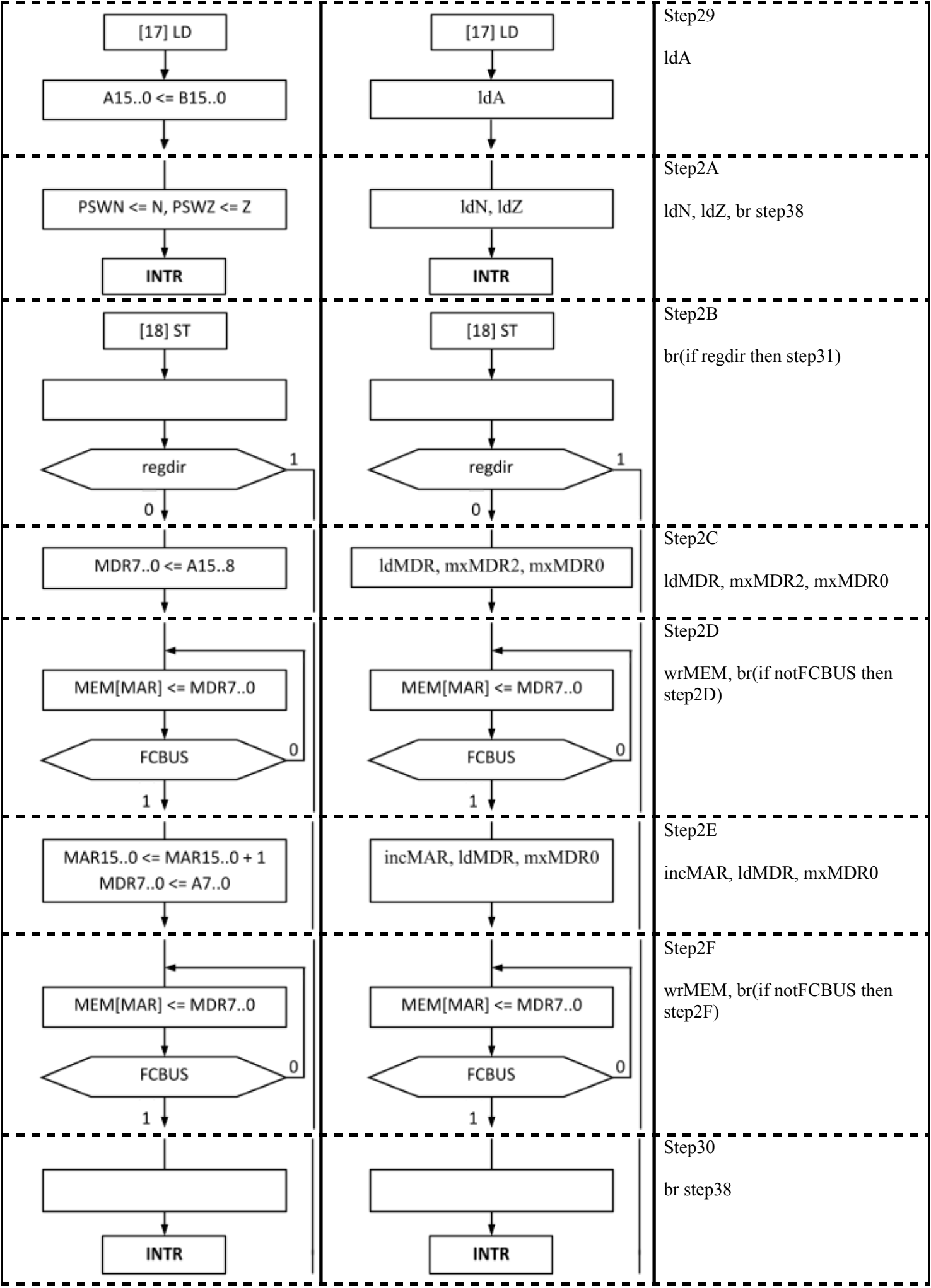


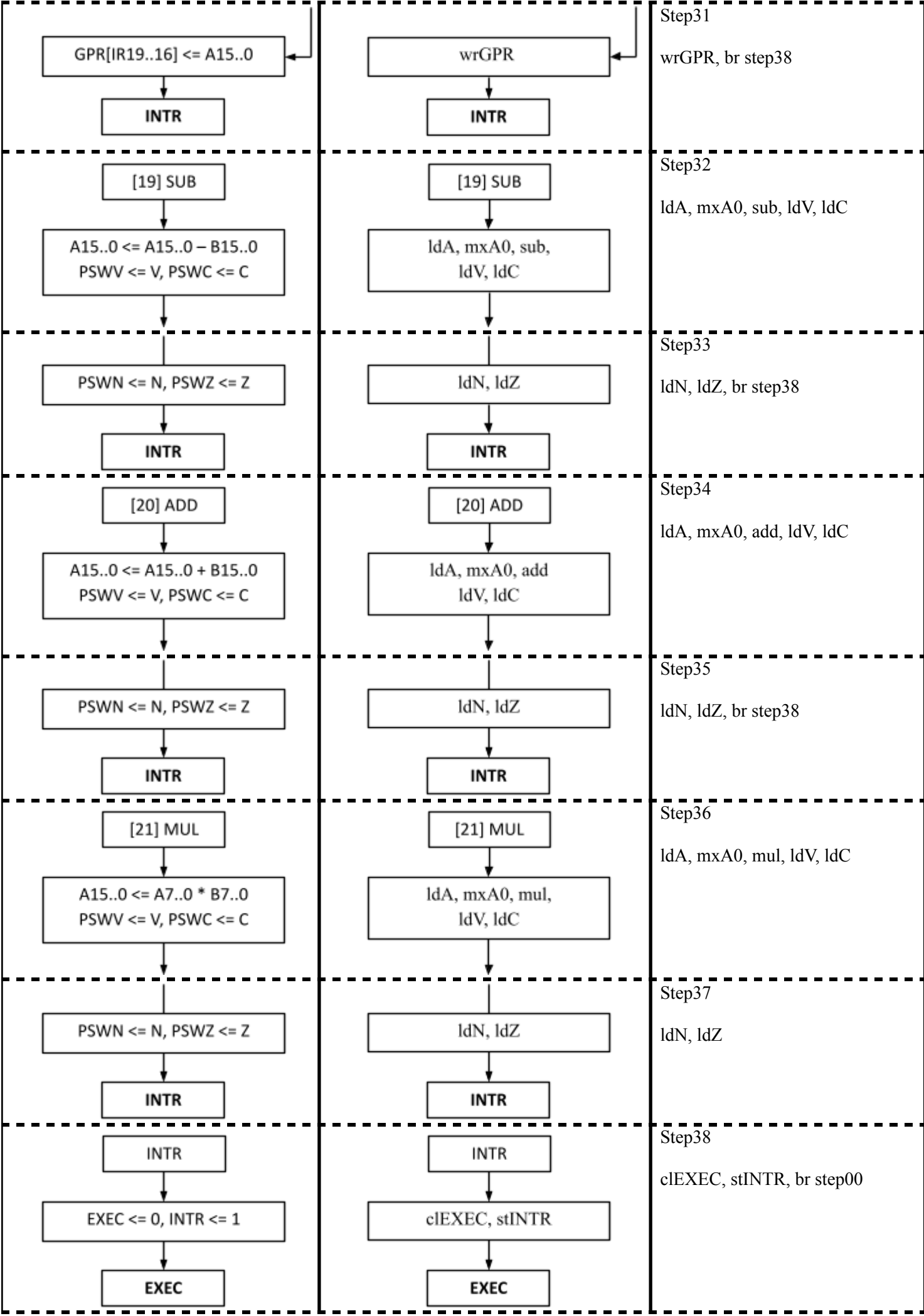
















Универзитет у Београду  
Електротехнички факултет

Име и презиме	Индекс	Потпис	Пројекат	
Назив Основи рачунарске технике 2			Датум	Страна