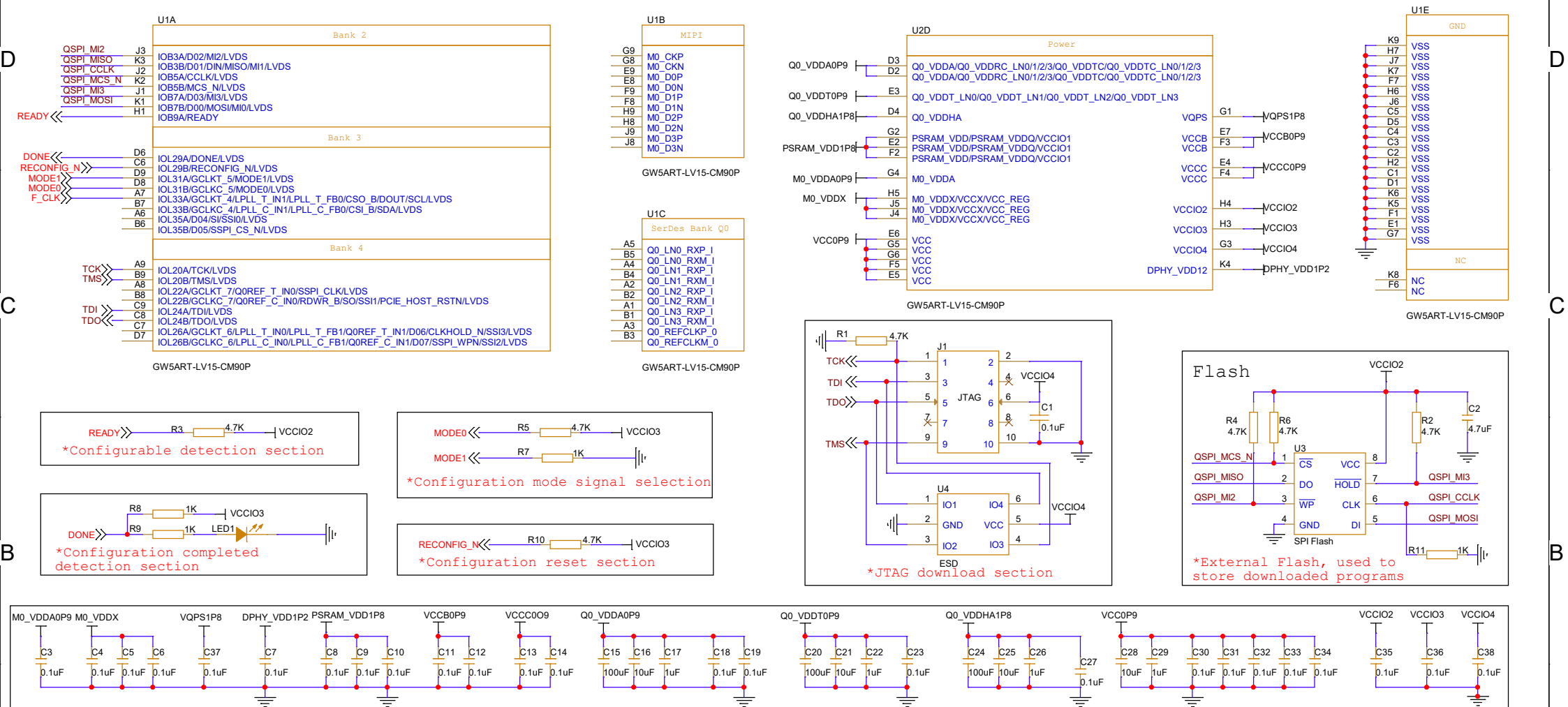
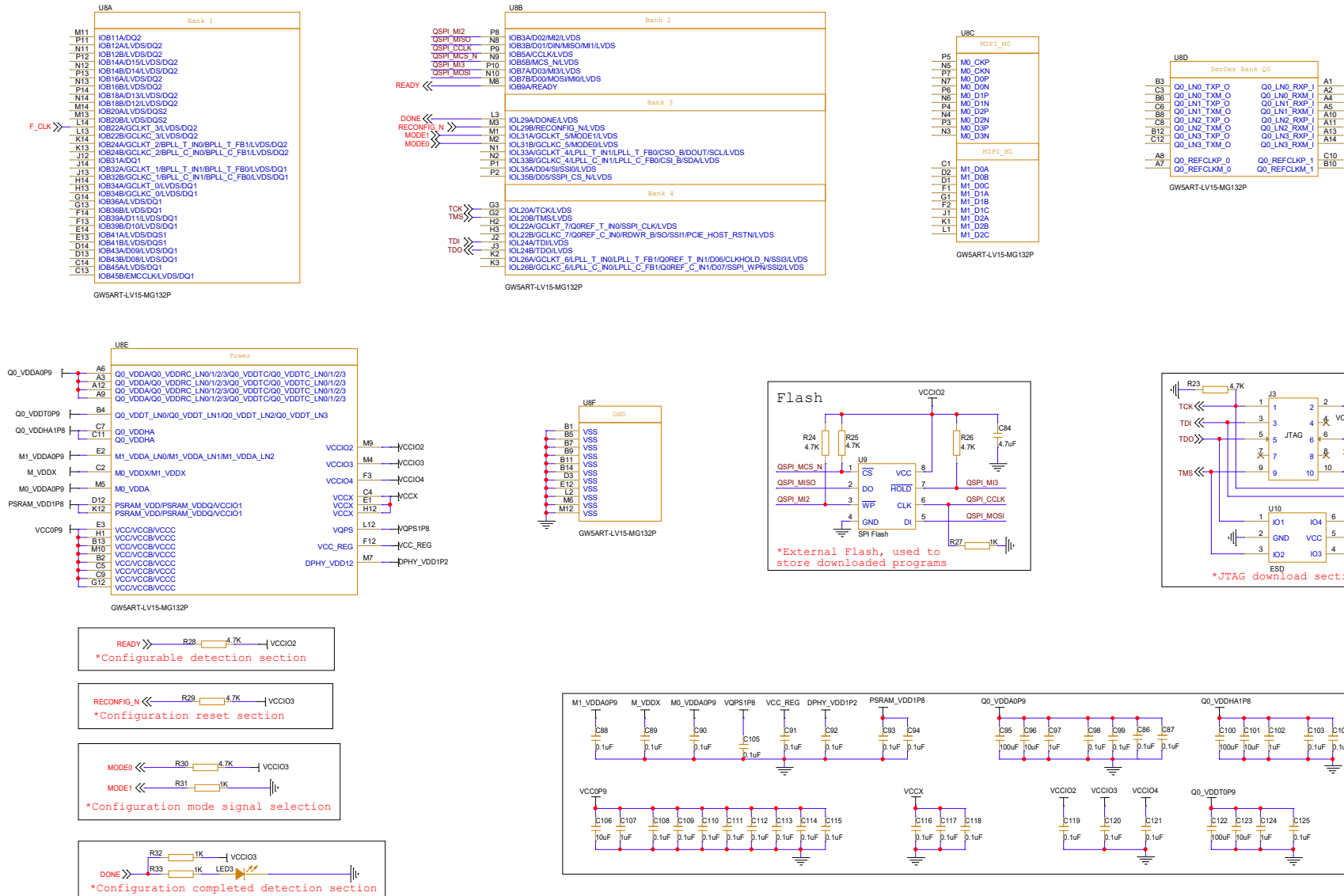


GW5ART-LV15CM90P



Title		
GOWIN Minimum System Diagram		
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B	GW5ART-LV15CM90P	1.0
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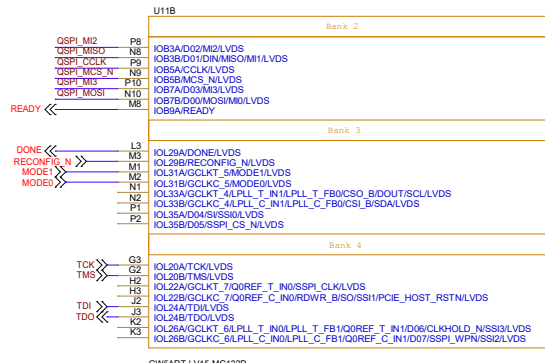
Notes:

- 1.F CLK signal is an external input clock signal.
It is recommended that F_CLK signal be provided through an active oscillator crystal.
- 2.External Flash memory is used to store downloaded programs.
- 3.It is recommended that add an ESD protection chip to the JTAG download circuit.
- 4.VCC core voltage requires a large current, so it is recommended to supply power separately.
- 5.The MODE pin is the GowinCONFIG configuration mode selection signal.

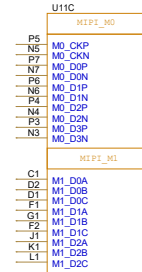
GOWIN Minimum System Diagram		
Size C	Document Number <Doc>	Rev 1.0
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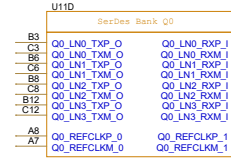
GW5ART-LV15-MG132P



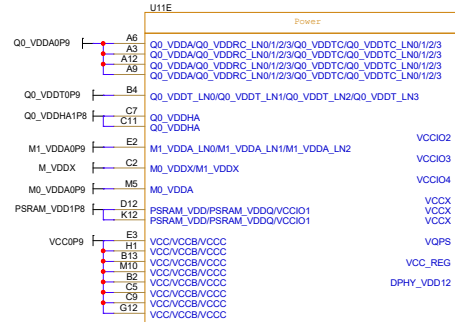
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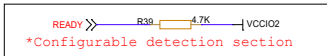
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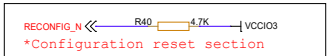
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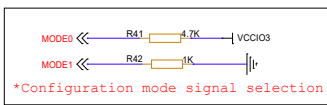
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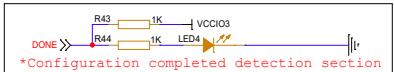
*Configurable detection section



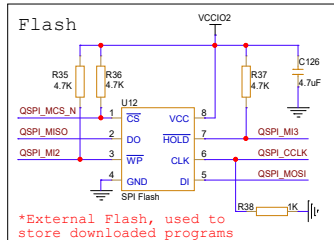
*Configuration reset section



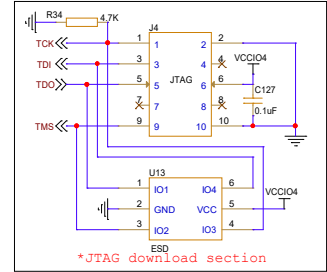
*Configuration mode signal selection



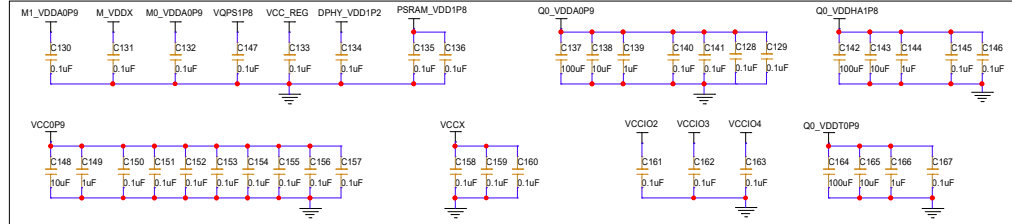
*Configuration completed detection section



*External Flash, used to store downloaded programs



*JTAG download section



- Notes:
- 1.F.CLK signal is an external input clock signal.
If it is recommended that F.CLK signal be provided through an active oscillator crystal.
 - 2.External Flash memory is used to store downloaded programs.
 - 3.It is recommended that add an ESD protection chip to the JTAG download circuit.
 - 4.VCC core voltage requires a large current, so it is recommended to supply power separately.
 - 5.The MODE pin is the GowinCONFIG configuration mode selection signal.